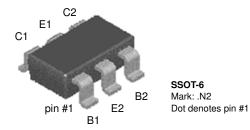


September 2007

FMB857B PNP Epitaxial Silicon Transistor

- · This device is designed for general purpose amplifier application at collector currents to 300mA.
- · Sourced from process 68.



Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	50	V
V _{CEO}	Collector-Emitter Voltage	45	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current (DC)	500	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 ~ 150	°C

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES

- 1. These ratings are based on a maximum junction temperature of 150 degrees C.
- 2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
- 3. All voltages (V) and currents (A) are negative polarity for PNP transistors.

Thermal Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Max.	Units
P _D	Total Device Dissipation	700	mW
	Derate above 25°C	5.6	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	180	°C/W

^{*}Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06".

Electrical Characteristics $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = 10\mu A$	50			٧
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 1 \text{mA}$	45			٧
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 10\mu A$	5			V
BV _{CEX}	Collector-Emitter Cutoff Voltage	$I_C = 10 \mu A, V_{BE} = 1 V$	50			nA
I _{CBO}	Collector Cut-off Current	V _{CB} = 30V, T = 25°C T =150°C			15 4000	nA
h _{FE}	DC Current Gain	$V_{CE} = 5V$, $I_{C} = 2mA$	220		475	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$ $I_C = 100 \text{ mA}, I_B = 5 \text{ mA}$			0.3 0.65	V
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = 5V, I_{C} = 2mA$ $V_{CE} = 5V, I_{C} = 10mA$	0.6		0.75 0.82	V

NOTES: All voltages (V) and currents (A) are negative polarity for PNP transistors.





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No Identification Needed Full Production		This datasheet contains final specifications. Fairchild Semiconductor reserv the right to make changes at any time without notice to improve design.	
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