TOSHIBA PHOTOCOUPLER GaAs IRED & PHOTO-TRANSISTOR

TLP291

Power Supplies Programmable Controllers Hybrid ICs

TLP291 consists of photo transistor, optically coupled to a gallium arsenide infrared emitting diode. TLP291 is housed in the SO4 package, very small and thin coupler.

Since TLP291 is guaranteed wide operating temperature (Ta=-55 to 110 °C) and high isolation voltage (3750Vrms), it's suitable for high-density surface mounting applications such as small switching power supplies and programmable controllers.

- Collector-Emitter Voltage : 80 V (min)
 - **Current Transfer Ratio** : 50% (min) : 100% (min)
 - Rank GB
 - Isolation Voltage : 3750 Vrms (min) : -55 to 110 °C
- Operation temperature
- UL recognized
- cUL approved
- : UL1577, File No. E67349

File No. 67349

- : CSA Component Acceptance Service No.5A,
- SEMKO approved:
- BSI approved
- Approved no. 1200315 : BS EN 60065: 2002, Approved no. 9036 : BS EN 60950-1: 2006, Approved no. 9037

EN 60065: 2002, Approved no. 1200315

EN 60950-1: 2001, EN 60335-1: 2002,

Option (V4)

VDE approved: EN 60747-5-5 Certificate, No. 40009347 Maximum operating insulation voltage: 707 Vpk Highest permissible over-voltage: 6000 Vpk (Note) When EN 60747-5-5 approved type is needed, please designate the "Option(V4)"



Unit: mm 11-3C1 11-3C1 TOSHIBA Weight: 0.05 g (typ.)

Pin Configuration

TLP291



1:ANODE 2:CATHODE **3:EMITTER** 4:COLLECTOR

Current Transfer Ratio (CTR) Rank (Unless otherwise specified, Ta = 25°C)

	Classification	Current Transfer Ratio (%) (I_C / I_F) $I_F = 5 mA, V_{CE} = 5 V, Ta = 25°C$				
TYPE	(Note1)			Marking of Classification		
		Min	Max			
	Blank	50	400	Blank, YE, Y+, GR, GB, G, G+,B		
	Rank Y	50	150	YE		
	Rank GR	100	300	GR		
TI P201	Rank GB	100	400	GB		
121231	Rank YH	75	150	Y+ ((//))		
	Rank GRL	100	200	G		
	Rank GRH	150	300	G+		
	Rank BLL	200	400	В		

Note1: Specify both the part number and a rank in this format when ordering

(e.g.) rank GB: TLP291 (GB,E

For safety standard certification, however, specify the part number alone.

(e.g.)TLP291 (GB,E: TLP291

	CHARACTERISTIC	SYMBOL	NOTE	RATING	UNIT
	Input forward current	lF		50	mA
	Input forward current derating (Ta≥90°C)	∆l _F /ΔTa		-1.5	mA /°C
	Input forward current (pulsed)	IFP	(Note 2)	1	А
LED	Input reverse voltage	V _R		5	V
-	Input power dissipation	PD			mW
	Input power dissipation derating (Ta \ge 90°C)	ΔΡ _D /ΔTa		-3.0	mW/°C
	Junction temperature	Тј		125	°C
	Collector-emitter voltage	V _{CEO}		80	V
ц	Emitter-collector voltage	V _{ECO}) 7	V
СТО	Collector current	IC		50	mA
ETE(Collector power dissipation	PC		150	mW
Ō	Collector power dissipation derating(Ta≥25°C)	ΔP _C /ΔTa	$\overline{\Omega}$	-1.5	mW /°C
	Junction temperature	Тј	(\bigcirc)	125	⊖ °C
Ope	erating temperature range	T _{opr}		-55 to 110	°C
Storage temperature range		T _{stg}	\sim	-55 to 125	°C
Lead soldering temperature		T _{sol}		260 (10s)	°C
Tota	al package power dissipation	RT	(7	200	mW
Tota	al package power dissipation derating(Ta≥25°C)	ΔΡτ /ΔΤα		-2.0	mW /°C
Isolation voltage		BVS	(Note3)	3750	Vrms

Absolute Maximum Ratings (Note)(Unless otherwise specified, Ta = 25°C)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note2: Pulse width \leq 100µs, frequency 100Hz

Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

CHARACTERISTIC SYMB		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
	Input forward voltage	VF	I _F = 10 mA	1.1	1.25	1.4	V
ĒD	Input reverse current	IR	V _R = 5 V	-	-	5	μA
\sim	Input capacitance	Ст	V = 0 V, f = 1 MHz	-	30	-	pF
	Collector-emitter breakdown voltage	V _(BR) CEO	I _C = 0.5 mA	80	-	-	V
-OR	Emitter-collector breakdown voltage	V _(BR) ECO	I _E = 0.1 mA	7	-	-	V
ECT	Deale surrent		V _{CE} = 48 V	-	0.01	0.08	μA
	Dark current	ICEO	V _{CE} = 48 V, Ta = 85°C	-	2	50	μA
	Collector-emitter capacitance	C _{CE}	V = 0 V, f = 1 MHz	-	10	-	pF

Note3: AC, 1 minute, R.H.≤60%, Device considered a two terminal device: LED side pins shorted together and DETECTOR side pins shorted together.

Coupled Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current transfer ratio	lo/le	I _F = 5 mA, V _{CE} = 5 V	50	-	400	%
		Rank GB	100	-	400	70
Saturated ourrant transfer ratio		I _F = 1 mA, V _{CE} = 0.4 V	60	-	0/	
	IC / IF (sat)	Rank GB	30	-	-	-/0
		I _C = 2.4 mA, I _F = 8 mA	(L) /~-	0.3	
Collector-emitter saturation voltage	V _{CE (sat)}	I _C = 0.2 mA, I _F = 1 mA	0.2	-	V	
		Rank GB))	-	0.3	
OFF-state collector current	I _{C (off)}	V _F = 0.7 V, V _{CE} = 48 V		-	10	μA

Isolation Characteristics (Unless otherwise specified, Ta = 25°C)

SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
CS	V _S = 0 V, f = 1 MHz	2-1	0.8) -	pF
R _S	V _S = 500 V, R.H.≤60%	1×10 ¹²	10 ¹⁴	-	Ω
	AC , 1 minute	3750	~ -	-	Vrma
BVS	AC , 1 second, in OIL	7	10000	-	vinis
	DC , 1 minute, in OIL	<u> </u>	10000	-	Vdc
	SYMBOL C _S R _S BV _S	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$	$\begin{tabular}{ c c c c c } \hline SYMBOL & TEST CONDITION & MIN \\ \hline C_S & V_S = 0 \ V, \ f = 1 \ MHz & - \\ \hline R_S & V_S = 500 \ V, \ R, \ H. \le 60\% & 1 \times 10^{12} \\ \hline R_S & AC \ , 1 \ minute & 3750 \\ \hline AC \ , 1 \ second, \ in \ OIL & - \\ \hline DC \ , 1 \ minute, \ in \ OIL & - \\ \hline \end{tabular}$	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$

Switching Characteristics (Unless otherwise specified, Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Rise time	(tr		-	4	-	
Fall time	t _f	$V_{CC} = 10 V, I_C = 2 mA$	-	7	-	
Turn-on time	t _{on}	$R_L = 100\Omega$	-	7	-	μs
Turn-off time	t _{off}	(\overline{O})	-	7	-	
Turn-on time	ton		-	2	-	
Storage time	ts	$R_{L} = 1.9 k\Omega$ (Fig.1) V _{CC} = 5 V, I _F = 16 mA	-	30	-	μS
Turn-off time	toff		-	60	-	

(Fig.1) Switching Time Test Circuit







Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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V_{CE(sat)} – Ta

I_С - Та



Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Soldering and Storage

1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as

much as possible by observing the following conditions.

1) Using solder reflow

·Temperature profile example of lead (Pb) solder



·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.

2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

- · Please preheat it at 150°C between 60 and 120 seconds.
- \cdot Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.
- 3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.

3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.

- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

EN 60747-5-5 Option:(V4)

: TLP291

Types

Type designations for "option: (V4)", which are tested under EN 60747 requirements.

- (e.g.): TLP291 (V4GB-TP,E V4 : EN 60747 option GB: CTR rank type TP : Standard tape & reel type
 - E : [[G]]/RoHS COMPATIBLE (Note4)

Note: Use TOSHIBA standard type number for safety standard application. (e.g.): TLP291(V4GB-TP,E → TLP291

Note4: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

EN 60747 Isolation Characteristics

Description	Symbol	Rating	Unit
Application classification	$\langle \rangle$		
for rated mains voltage ≤ 150Vrms for rated mains voltage ≤ 300Vrms	\square	I-IV I-III	-
Climatic classification		55 / 110 / 21	-
Pollution degree	/	2	-
Maximum operating insulation voltage	VIORM	707	Vpk
Input to output test voltage, Method A Vpr=1.5 × V _{IORM} , type and sample test tp=10s, partial discharge<5pC	Vpr	1060	Vpk
Input to output test voltage, Method B Vpr=1.875 × V _{IORM} , 100% production test tp=1s, partial discharge<5pC	Vpr	1325	Vpk
Highest permissible overvoltage (transient overvoltage, tpr=60s)	V _{TR}	6000	Vpk
Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve) current (input current: I _F , Psi=0mW) power (output or total power dissipation) temperature	I _{si} P _{si} T _{si}	250 400 150	mA mW °C
V _{IO} =500V, Ta=T _{si}	R _{si}	≧ 10 ⁹	Ω

Insulation Related Specifications

Minimum creepage distance	Cr	5.0mm
Minimum clearance	CI	5.0mm
Minimum insulation thickness	ti	0.4mm
Comparative tracking index	CTI	175

 If a printed circuit is incorporated, the creepage distance and clearance may be reduced below this value. (e.g. at a standard distance between soldering eye centers of 3.5mm). If this is not permissible, the user shall take suitable measures.

2. This photocoupler is suitable for 'safe electrical isolation' only within the safety limit data. Maintenance of the safety data shall be ensured by means of protective circuit.

VDE test sign: Marking on product for EN 60747 V



<u>TOSHIBA</u>



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