

# A111 – Pulsed Coherent Radar (PCR) Datasheet v2.8

# A111 Overview

The A111 is a radar system based on pulsed coherent radar (PCR) technology and is setting a new benchmark for power consumption and distance accuracy – fully integrated in a small package of 29 mm<sup>2</sup>.

The A111 60 GHz radar system is optimized for high precision and ultra-low power, delivered as a one package solution with integrated Baseband, RF front-end and Antenna in Package (AiP). This will enable easy integration into any portable battery driven device.

The A111 is based on leading-edge patented sensor technology with pico-second time resolution, capable of measuring absolute distance with mm accuracy up to a range of 2 m (1) and with configurable update rate.

The A111 60 GHz radar remains uncompromised by any natural source of interference, such as noise, dust, color and direct or indirect light.

# Applications

- High precision distance measurements with mm accuracy and high update rate
- Ultra-low power consumption, e.g. average power consumption 0.2 mW at 0.1 Hz update rate, 3 mW at 10 Hz update rate and 20 mW at 100 Hz update rate
- Proximity detection with high accuracy and the possibility to define multiple proximity zones
- Motion detection, Speed detection
- Enables material detection
- High precision object tracking, enabling gesture control
- High precision tracking of 3D objects
- Monitor vital life signs such as breathing and pulse rate



# Features

- **Fully integrated sensor** 
	- 60 GHz Pulsed Coherent Radar (PCR)
	- Integrated Baseband, RF front-end and Antenna in Package (AiP)
	- 5.5 x 5.2 x 0.88 mm fcCSP, 0.5 mm pitch
- **Accurate distance ranging and movements** 
	- Measures absolute range up to  $2 \text{ m}^{(1)}$ o Absolute accuracy in mm
	- Relative accuracy in  $\mu$ m
	- Possible to recognize movement and gestures for several objects
	- Support continuous and single sweep mode
	- HPBW of 80 (H-plane) and 40 degrees (E-plane)

#### • **Easy integration**

- One chip solution with integrated Baseband and RF
- Can be integrated behind plastic or glass without any need for a physical aperture
- Single reflowable component
- 1.8 V single power supply, enable with Power on Reset (PoR)
- Clock input for crystal 24 MHz
- SPI interface for data transfer, up to 50 MHz SPI clock support
- INTERRUPT support

*(1) 2m ranging is guaranteed for an object size, shape and dielectric properties corresponding to a spherical corner reflector of 5 cm radius.* 



# 1 Table of Contents





# <span id="page-3-0"></span>2 Revision History





# <span id="page-4-0"></span>3 Description

The A111 is an optimized low-power, high-precision, 60 GHz radar sensor with integrated Baseband, an RF front-end and an Antenna in Package (AIP).

The sensor is based on pulsed coherent radar (PCR) technology, featuring a leading-edge patented solution with picosecond time resolution. The A111 is the perfect choice for implementing highaccuracy, high-resolution sensing systems with low-power consumption.

#### **Ordering information**



#### **Acconeer A111 marking**



#### A111 marking:

XX = Product code  $\overline{a}$ 

 $YYYY = Lot number$ 

<span id="page-5-0"></span>

*Figure 3.1 The A111 functional block diagram.* 

The A111 silicon is divided into four functional blocks: Power, Digital, Timing and mmWave radio.

The Power functional block includes LDOs and a Power on Reset (PoR) block. Each LDO creates its own voltage domain. The PoR block generates a Reset signal on each power-up cycle. The host interfaces the Power functional block of the sensor via 1.8 V Single power supply and ENABLE.

The Digital functional block includes sensor control. The data memory stores the radar sweep data from the ADC. The host interfaces the Sensor via an SPI interface, a Clock (XIN, XOUT), INTERRUPT signal and optional CTRL signal.

The Timing block includes the timing circuitry. The PLL digital clock output is used to drive digital logic and is synthesized from external crystal (XIN/XOUT) . The operational oscillator (XIN) frequency is 24MHz..

The mmWave radio functional block generates and receives radar pulses and includes transmitter (TX), receiver (RX) and interfaces toward the integrated antennas. The A111 operates in the 57-64 GHz band.



# <span id="page-6-0"></span>4 Pin Configuration and Functions

The below figure shows the A111 pin configuration, top view:



*Figure 4.1. Pin configuration of the A111 sensor, top view.* 









*Table 4.1. A111 sensor pin list.* 

(1) VIO\_1a and VIO\_1b are short circuit inside the sensor. VIO\_2a and VIO\_2b are short circuit inside the sensor. VIO\_3a and VIO\_3b are short circuit inside the sensor.

# <span id="page-8-0"></span>5 Specifications

### <span id="page-8-1"></span>5.1 Absolute Maximum Ratings

The below table shows the A111 absolute maximum ratings over operating temperature range, on package, unless otherwise noted:



<span id="page-8-3"></span>*Table 5.1. Absolute maximum ratings.* 

(1) VIO\_1 and VIO\_2 must never exceed VIO\_3.

Stresses beyond those listed in [Table 5.1](#page-8-3) may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these conditions or at any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods of time may affect device reliability.

### <span id="page-8-2"></span>5.2 Environmental Sensitivity

The below table shows the A111 environmental sensitivity:



*Table 5.2. Environmental sensitivity.* 

(1) For reference only. The package is generically qualified by the manufacturer. Acconeer does not guarantee adherence to standard.



# <span id="page-9-0"></span>5.3 Recommended Operating Conditions

The below table shows the A111 recommended operating conditions, on package:



*Table 5.3. Recommended operating conditions.* 

### <span id="page-9-1"></span>5.4 Electrical Specification

The below table shows the A111 electrical DC specification conditions, on package,  $T_{op} = -40^{\circ}C$  to 85ºC:



*Table* 5.4*. Electrical DC conditions.* 



The below table shows the A111 electrical AC specification conditions, on package, at  $T_{op} = -40^{\circ}C$  to 85ºC:



*Table* 5.5*. Electrical AC conditions.* 

(1) Load capacitance 2 pF.

### <span id="page-10-0"></span>5.5 Power Consumption Summary

Table 5.6 summarizes the steady-state current consumption for the sensor states, average current ratings at all power terminals (VIO\_1, VIO\_2, VIO\_3), VIO 1.8 V, at  $T_{op} = -40^{\circ}C$  to 85 $^{\circ}C$ :







*Table* 5.6*. Average current ratings at power terminals for the sensor states.* 

(1) based on non Sparse service configuration

(2) based on Sparse service configuration

## <span id="page-11-0"></span>5.6 RF Specification

The below table shows the A111 RF specification at  $T_{op} = -40^{\circ}C$  to 85 $^{\circ}C$ :



*Table* 5.7*. A111 RF specification.* 

(1) Based on simulation.

# <span id="page-12-0"></span>6 Timing Requirements

### <span id="page-12-1"></span>6.1 Serial Peripheral Interface

The Serial Peripheral Interface (SPI) is a 4-wire serial bus, used for configuration and reading output from the A111 radar sensor. The A111 radar sensor is an SPI slave device connected to the SPI master, as described in [Figure 6.1.](#page-12-2) The A111 allows several devices to be connected on the same SPI bus, with a dedicated slave-select signal. Daisy-chain is not supported.



*Figure 6.1. SPI master-slave connection.* 

<span id="page-12-2"></span>The serial data transfer input (MOSI) and output (MISO) to the A111 are synchronized by the SPI\_CLK. The Slave Select signal (SS) must be low before and during transactions. The MOSI is always read on the rising edge of SCLK and the MISO changes value on the falling edge of SPI\_CLK (SPI mode 0, CPOL/CPHA = 0). SS requires release in between transactions. See [Figure 6.2](#page-12-3) an[d Table](#page-13-0)  [6.1](#page-13-0) for timing characteristics.



<span id="page-12-3"></span>*Figure 6.2. Timing diagram of SPI, CPOL=0 and CPHA=0.*



<span id="page-13-0"></span>*Table 6.1. SPI timing characteristics.* 

(1) 10pF load on SPI\_MISO

# <span id="page-14-0"></span>7 Typical Characteristics

### <span id="page-14-1"></span>7.1 Radar Loop Gain Pattern

The Radar Loop Gain (RLG) pattern includes the gain in both the TX and RX radar paths and is defined as the angular separation between the two points at which the gain has decreased by 3 dB relative to the maximum main lobe value, when the radar itself is used to measure the reflected power. For details regarding the measurement setup, refer to "Hardware and physical integration guideline", chapter 1.2.

Conditions:  $T_A = 25 \text{ °C}$ ,  $V_{DD} = 1.8 \text{ V}$ . Tested on 5 XR112 devices.

[Figure 7.1](#page-14-2) and [Figure](#page-14-3) *7.2* shows the RLG radiation pattern normalized to free space sensor boresight at elevation plane (E-plane) and azimuth plane (H-plane).





<span id="page-14-2"></span>*Figure 7.1. Normalized radar loop gain radiation pattern at E-plane.* 



<span id="page-14-3"></span>*Figure* 7.2*. Normalized radar loop gain radiation pattern at H-plane.* 

### <span id="page-15-0"></span>7.2 Relative Phase Accuracy

Conditions:  $T_A = 25 \text{ °C}$ ,  $V_{DD} = 1.8 \text{ V}$ . Statistical result based on sweep count 100, 20 tested devices.

Standard deviation of phase estimation, measured at a distance of 0.35 m. Object metal cylinder, 40 mm in diameter.

Average STD of relative phase estimation:

6.1 degrees in relative phase accuracy, translates to 42 µm in relative distance accuracy.



# <span id="page-16-0"></span>8 Functional Description

The below figure shows the A111 system integration with Host MCU:



*Figure 8.1. System integration.* 

The Acconeer software is executed on Host MCU that handles sensor initiation, configuration, sweep acquisition and signal processing.

The Serial Peripheral Interface (SPI) is a 4-wire serial bus, used for configuration and reading output from the A111 radar sensor. The A111 radar sensor is an SPI slave device, connected to the SPI master (Host MCU), and allows several devices to be connected on same SPI bus, with a dedicated slave-select signal. Daisy-chain is not supported.

The sensor provides support for ENABLE and INTERRUPT as interrupt signal, always output, that is used as an interrupt in the Host MCU. The sensor supports an optional control signal: CTRL, which is configured through software, e.g. for controlling the operating state of the sensor to idle in Hibernate.

### <span id="page-17-0"></span>8.1 Acconeer Software

The Acconeer software has been written in C and is portable to any OS and HW platform. The Acconeer software is executed on Host MCU and delivered as binaries, except for integration software that is delivered as source code.

The below figure shows the A111 software offer.



*Figure 8.2. Acconeer Software offer.* 

The RSS (Radar System Software) provides output at two different levels, Service and Detector. RSS provides an API (Application Programming Interface) for Application utilization of various Services and Detectors.

The Service output is pre-processed sensor data as a function of distance, e.g. Envelope data (amplitude of sensor data), Power bin data (integrated amplitude data in pre-defined range intervals), IQ modulated data (representation in cartesian) etc.

Detectors are built on Service data as input and the output is a result, e.g. Distance detector that presents distance and amplitude result based on envelope Service etc.

Customer can either use Acconeer detector or develop their own signal processing based on Service data.

Acconeer provides several example applications to support customer own application development. Also, customer guidelines are provided for application development utilizing the Acconeer RSS API.

Acconeer provides several reference drivers as source code, e.g. Support for Cortex M4, Cortex M7 MCU's.

### <span id="page-17-1"></span>8.2 Software Integration

Integration software shall implement functions defined in a definitions file provided in Acconeer Software offer. This includes handling of SPI, ENABLE, INTERRUPT and CTRL, as well as potential OS functions.

See reference HAL - User Guide for guideline on software integration and HAL implementation (https://www.acconeer.com/products).



# <span id="page-18-0"></span>8.3 Power Sequences

The power-up sequence is described using the recommended integration shown in the below figure:



*Figure 8.3. Recommended integration of the A111 radar sensor.*

The power up sequence is shown in below figure.



*Figure 8.4. Power up sequence.* 

<span id="page-19-0"></span> The power up sequence is initiated by turning on VIO\_3a,b. It must be turned on before or simultaneously with ENABLE and VIO\_1-2a,b. ENABLE and VIO\_1-2a,b can be turned on in any order and independently of each other. A111 should however not be considered as in state "ON" until all supply voltage levels are stable and ENABLE is high. The time constant  $t_1$  in figure 7.4 denotes this time. The actual value of *t*1 depends on the power supply and the decoupling capacitors used. If the CTRL signal is used, it must be held at 0V during time *t*1.

Next step in the power up sequence is to have a settling time for the XTAL oscillator to stabilize, shown as time  $t_2$  in [Figure 8.4.](#page-19-0) This may take up to several milliseconds depending on the XTAL performance. It is advised to have the clock inactive at 0 V while ENABLE is inactive.

Now the A111 radar sensor is ready for SPI communication.



After power up is complete, the sensor is loaded with a program. Up until the point where the sensor's program is started, the INTERRUPT is high impedance. However, after the sensor's program has started the INTERRUPT is configured to a push-pull CMOS output. It is therefore required that the host I/O is configured as input before any programs are started on the sensor.

The power down sequence is described i[n Figure 8.5.](#page-20-0)



*Figure 8.5. The power down sequence.* 

<span id="page-20-0"></span>The power down sequence is initiated by setting "ENABLE" low. The crystal oscillator will be disabled when ENABLE is set low.

After that, VIO\_1-2a,b can be turned off. Time constant  $t_3$  (refer to [Figure 8.5\)](#page-20-0) later, VIO\_3a,b can be disabled. The constant  $t_3$  >=0. The I/O inputs on A111 (including CTRL if it is being used) must be set to 0V before or simultaneously with VIO\_3a,b going low, otherwise the internal ESD protection diodes will draw current from the I/O source.



# <span id="page-21-0"></span>9 Layout Recommendations

The sensor antennas are of a folded dipole type, with its main ground reference being the internal package ground plane, extending below the whole area of the sensor. To further enhance the directivity of the sensor, the package ground plane should be extended by soldering all GND pads to the PCB top layer ground. In terms of regulatory compliance, any openings in the ground plane inside the A111 footprint must be significantly smaller than the wavelength (5 mm in free space) to effectively shield off any disturbance.

[Figure 9.1](#page-21-1) shows the simulated relative radar loop gain (RLG) as function of ground plane side length, assuming a square ground plane. As the ground plane size increases, the RLG increases because of increased antenna directivity. Constructive and destructive interference results in a non-monotonic increase in RLG.



<span id="page-21-1"></span>*Figure 9.1. Simulated relative radar loop gain as function of ground plane side length (x). Ground plane is a solid square ground plane.* 

VIO\_1a and VIO\_1b are internally connected and connecting both on the PCB is recommended as well. Likewise, VIO\_2a, VIO\_2b and, VIO\_3a, VIO\_3b are internally connected and connecting both is recommended on the PCB as well. It is recommended to have decoupling capacitors on the supplies placed as close as possible to the supply terminals. A1 µF decoupling capacitance on each supply pin pair is recommended.

More detailed PCB layout guidelines can be found in the "Hardware and physical integration guideline".



### Bill of Material (BoM)

The below table shows BOM for integration of the A111 using a crystal as input clock source:



*Table 9.1. BOM list.* 

(1) See details in chapter 9.1 XTAL for C4, C5 value calculation.

### <span id="page-22-0"></span>9.1 XTAL

The input clock origin from a crystal (XTAL), connected to XIN and XOUT.

The A111 sensor has a built-in XTAL oscillator and adding an external XTAL component, as shown in the [Figure 9.2.](#page-22-1)



*Figure 9.2. External XTAL schematics.* 

<span id="page-22-1"></span>To enable the internal XTAL oscillator to drive the external resonator, the relation in Equation 1 must be fulfilled.



The capacitance values are calculated in Equation 2.  $C_L$  and  $R_{ESR}$  are XTAL parameters and vary from XTAL to XTAL. The stray capacitance is the sum of the capacitance between XIN and XOUT, that is, the PCB trace capacitance plus package capacitance; 2 to 5 pF is a general estimation.

#### Example:

- $\bullet$  f = 24 MHz
- $C_L = 9$  pF
- $R_{ESR} = 40$  ohm

Assuming  $C_{\text{stray}} = 5$  pF gives C4, C5 = 8 pF and that the condition is met with the result 0.58 < 0.7.

# <span id="page-23-0"></span>9.2 Power Supply

The A111 sensor has got three power supplies where the VIO\_3 power supply is sensitive to power supply ripple. Power supply ripple on VIO\_3 may degrade performance since VIO\_3 supplies the internal clock generation blocks. [Table 9.2](#page-23-1) provides the required power supply ripple specification for VIO\_3.

| Frequency (Hz) | Min. | Typ. | Max. | <b>Unit</b> |
|----------------|------|------|------|-------------|
| 10 000         |      |      | 18.7 | $mV_{pp}$   |
| 100 000        |      |      | 2.6  | $mV_{pp}$   |
| 1 000 000      |      |      | 0.26 | $mV_{pp}$   |
| 3 000 000      |      |      | 0.09 | $mV_{pp}$   |
| 10 000 000     |      |      | 0.23 | $mV_{pp}$   |
| 100 000 000    |      |      | 3.0  | $mV_{pp}$   |

<span id="page-23-1"></span>*Table* 9.2*. Required power supply ripple specification for VIO\_3.* 

#### LC filter solution

When the VIO\_3 voltage source is connected to a switching power supply, a post LC filter as displayed in [Figure 9.3](#page-23-2) can be designed satisfy the ripple specification. The exact values for LC filter depend on switching frequency and ripple amplitude of the supply regulator. Be aware of LC filter peaking at the series resonance frequency f=1/( $2π\sqrt{LC}$ ). A small snubber resistor, 250 mΩ in the example filter, can be inserted to lower the Q factor.



<span id="page-23-2"></span>*Figure* 9.3*. LC supply filter.* 



*Figure* 9.4*. Simulated performance with 10 mVpp supply ripple with LC supply filter.* 

# <span id="page-25-0"></span>10 Regulatory Approval

To be noted is that some regulatory specifications also specify the usage of the sensor, so users of the sensor must check regulatory requirements for their own use case and determine if the regulatory approvals described below are sufficient.

### <span id="page-25-1"></span>10.1 ETSI

Hereby, Acconeer declares that the A111 sensor is compliant with directive 2014/53/EU.

<span id="page-25-2"></span>



### <span id="page-26-0"></span>10.2 FCC Approval

Hereby, Acconeer declares that the A111 sensor has modular approval granted by FCC.

Testing for the modular approval of the A111 has been performed using a binary file which represents highest Tx emission configuration. This test binary is available in the software provided by Acconeer and can be used for certification purposes.

The A111 sensor meets the title 47 of the Code of Federal Regulations, part 15 section 15.255 for intentional radiators operating in the 57-71 GHz band for the following type of applications.

- Field disturbance sensor employed for fixed operations.

- Short range device for interactive motion sensing.

Warning: The end user needs to maintain 20 cm distance to radiating parts of the device.

#### FCC ID: 2AQ6KA1001

The host product manufacturer is responsible for compliance to any other FCC rules that apply to the host not covered by the modular transmitter grant of certification.

#### <span id="page-26-1"></span>10.2.1 FCC Regulatory Notes

#### **Modifications**

Acconeer has not approved any changes to this device. Any changes or modifications to this device could invalid the FCC approval.

#### Interference statement

This device complies with Part 15 of the FCC rules. Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

#### RF exposure

This device complies with the FCC radiation exposure limits set forth for an uncontrolled environment. Co-location of this module with other transmitters that operate simultaneously are required to be evaluated using the FCC multi-transmitter procedures.

MPE RF exposure testing is not needed as the available maximum time-averaged power of the module is no more than 1 mW, according to 47 CFR  $1.1307(b)(3)(i)(A)$ .

#### Labelling requirements for the host device

The host device shall be labelled to identify the modules within the host device, which means that the host device shall be labelled to display the FCC ID of the module preceded by words "Contains transmitter module" or "Contains", E.g.

*Contains FCC ID: 2AQ6KA1001* 

#### <span id="page-27-0"></span>10.2.2 FCC Grant Authorization

**TCB** 

 $RF$ 

### **GRANT OF EQUIPMENT AUTHORIZATION**



Certification Issued Under the Authority of the **Federal Communications Commission** By:

> **CETECOM GmbH** Im Teelbruch 116 45219 Essen, Germany

Date of Grant: 11/08/2019

Application Dated: 11/08/2019

**Acconeer AB** mikael.rosenhed@acconeer.com mikael.egard@acconeer.com **Lund, 22370** Sweden

Attention: Mikael Rosenhed, Product Manager

#### NOT TRANSFERABLE

EQUIPMENT AUTHORIZATION is hereby issued to the named GRANTEE, and is VALID ONLY for the equipment identified hereon for use under the Commission's Rules and Regulations listed below.





### <span id="page-28-0"></span>10.3 Industry Canada Approval

The A111 sensor module meets the radio requirements for the 57-64 GHz band for the following type of applications:

- field disturbance sensor employed for fixed operations

IC certification number: 24388-A111

*L'approbation finale Industry Canada (IC) reste en instance.* 

*Le module du capteur A111 répond aux conditions de la bande 57-64 GHz pour le genre suivant d'applications: - capteur des champs de perturbation employé pour des activités fixées* 

*Numéro d'identification IC: 24388-A111* 

#### <span id="page-28-1"></span>10.3.1 Regulatory Information Canada

Acconeer has not approved any changes to this device. Any changes or modifications to this device could invalid the usage of the module.

*Acconeer n'a pas approuvé aucun changement de ce dispositif. Tout changement ou toute modification de ce dispositif pourrait invalider l'usage du module.* 

This device complies with Industry Canada's licence-exempt RSSs. Operation is subject to the following two conditions: (1) This device may not cause interference; and (2) This device must accept any interference, including interference that may cause undesired operation of the device.

*Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes : (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.* 

#### RF Exposure

Radiation Exposure Statement This equipment complies with Canada radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20cm between the radiator & your body.

Déclaration d'exposition aux radiations

Cet équipement est conforme Canada limites d'exposition aux radiations dans un environnement non contrôlé. Cet équipement doit être installé et utilisé à distance minimum de 20cm entre le radiateur et votre corps.



#### Labelling requirements for the host device

The host device should be labelled to identify the modules within the host device, which means that the host device shall be labelled to display the IC of the module preceded by words "Contains transmitter module" or "Contains", or similar wording expressing the same meaning, as follows

#### *Contains IC: 24388-A111*

*Le dispositif hôte doit être étiqueté afin d'identifier les modules du dispositif hôte, ce qui veut dire que le dispositif hôte doit être etiqueté pour exposer le IC du module précedé par les mots "Contient module émetteur" ou "Contient", ou des termes similaires exprimant le même sense, comme suit:* 

*Contient IC: 24388-A111* 

#### <span id="page-29-0"></span>10.3.2 Acceptance Certificate - Canada





# <span id="page-30-0"></span>11 Mechanical Data

The A111 is available in fcCSP package for mounting on a substrate. The below table shows mechanical data:

| <b>Parameter</b>     | Min. | Typ.  | <b>Max</b> | <b>Unit</b> |
|----------------------|------|-------|------------|-------------|
| Body X               | 5.15 | 5.20  | 5.25       | mm          |
| Body Y               | 5.45 | 5.50  | 5.55       | mm          |
| Body Z (height)      |      | 0.821 | 0.899      | mm          |
| <b>Ball pitch</b>    | 0.45 | 0.50  | 0.55       | mm          |
| <b>Ball diameter</b> | 0.25 | 0.30  | 0.35       | mm          |
| <b>Ball height</b>   | 0.15 | 0.24  |            | mm          |
| <b>Ball count</b>    |      | 50    |            | #           |

*Table 11.1. Mechanical data.* 

#### The A111 footprint is shown in [Figure 11.1.](#page-30-1)



<span id="page-30-1"></span>*Figure 11.1. A111 footprint.* 



*Figure 11.2. Physical layout of the A111 sensor, top view.* 



*Figure 11.3. Physical layout of the A111 sensor, side view.* 

Primary datum C and seating plane are defined by the spherical crowns of the solder balls. Dimension is measured at the maximum solder ball diameter, parallel to primary datum C. All dimensions and tolerances conform to ASME Y14.5 – 2009.

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*Figure 11.4. Physical layout of the A111 sensor, bottom view.* 

The bottom view shows 50 solder balls. The pitch of the BGA balls is 500 um, the ball diameter is 300  $\mu$ m  $\pm$ 5  $\mu$ m and the collapsed ball height is 0.244  $\pm$  0.050 mm.

### <span id="page-32-0"></span>11.1 Moisture Sensitivity Level and Recommended Reflow Profile

Acconeer A111 sensor is a Moisture Sensitive Devices (MSD) in accordance to the IPC/JEDEC specification. The Moisture Sensitivity Level (MSL) relates to the packaging and handling precautions required. A111 sensor is rated at MSL level 3.

Maximum number of reflow passes recommended for A111 is 2.

Soldering process qualified during qualification with "Preconditioning MSL 3: 30°C. 60%r.h., 192h, according to JEDEC JSTD20", and qualified for soldering heat resistance according to JEDEC J-STD-020.

### <span id="page-32-1"></span>11.2 RoHS and REACH Statement

Acconeer A111 sensor meet the requirements of Directive 2011/65/EC of the European Parliament and of the Council on the Restriction of Hazardous Substances (RoHS) and the requirements of the REACH regulation (EC 1907/2006) on Registration, Evaluation, Authorization and Restriction of Chemicals.

<span id="page-33-0"></span>



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# <span id="page-34-0"></span>Disclaimer

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Acconeer AB www.acconeer.com Västra Varvsgatan 19 info@acconeer.com info@acconeer.com 211 77 MALMÖ **+46 10 218 92 00** Sweden