# BLC9G20LS-361AVT

# Power LDMOS transistor Rev. 3 — 24 November 2017

AMMPLEON

Product data sheet

#### **Product profile** 1.

### 1.1 General description

360 W LDMOS packaged asymmetric Doherty power transistor for base station applications at frequencies from 1805 MHz to 1990 MHz.

#### Typical performance

Typical RF performance at  $T_{case}$  = 25 °C in an asymmetrical Doherty demo circuit.  $V_{DS}$  = 28 V;  $I_{Dq}$  = 400 mA (main);  $V_{GS(amp)peak}$  = 0.7 V, unless otherwise specified.

Test signal	f	V <sub>DS</sub>	P <sub>L(AV)</sub>	G <sub>p</sub>	η <sub>D</sub>	ACPR
	(MHz)	(V)	(dBm)	(dB)	(%)	(dBc)
1-carrier W-CDMA	1805 to 1880	28	47.8	16.4	50	-30 <u>[1]</u>

<sup>[1]</sup> Test signal: 3GPP test model 1; 64 DPCH; PAR = 7.2 dB at 0.01% probability on CCDF per carrier.

#### Table 2. Typical performance

Typical RF performance at  $T_{case}$  = 25 °C in an asymmetrical Doherty demo circuit.  $V_{DS}$  = 28 V;  $I_{Dq}$  = 450 mA (main);  $V_{GS(amp)peak}$  = 0.6 V, unless otherwise specified.

Test signal	f	V <sub>DS</sub>	$P_{L(AV)}$	G <sub>p</sub>	η <sub>D</sub>	ACPR
	(MHz)	(V)	(dBm)	(dB)	(%)	(dBc)
1-carrier W-CDMA	1930 to 1990	28	47.8	16.6	47.5	-35 <u>[1]</u>

<sup>[1]</sup> Test signal: 3GPP test model 1; 64 DPCH; PAR = 7.2 dB at 0.01% probability on CCDF per carrier.

#### 1.2 Features and benefits

- Excellent ruggedness
- High efficiency
- Low thermal resistance providing excellent thermal stability
- Designed for broadband operation (1805 MHz to 1990 MHz)
- Asymmetric design to achieve optimum efficiency across the band
- Lower output capacitance for improved performance in Doherty applications
- Designed for low memory effects providing excellent digital pre-distortion capability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

#### 1.3 Applications

 RF power amplifiers for base stations and multi carrier applications in the 1805 MHz to 1990 MHz frequency range

# 2. Pinning information

Table 3. Pinning

Pin	Description		Simplified outline	Graphic symbol
1	drain2 (peak)			0.7
2	drain1 (main)		7 2 1 6	2, 7
3	gate1 (main)		5	
4	gate2 (peak)		3 4	3——5
5	source	[1]		4—
6	video decoupling (peak)			<b>'</b> ⊢¬
7	video decoupling (main)			1, 6 aaa-014884

<sup>[1]</sup> Connected to flange.

# 3. Ordering information

Table 4. Ordering information

Type number	Packag	Package				
	Name	Description	Version			
BLC9G20LS-361AVT	-	air cavity plastic earless flanged package; 6 leads	SOT1258-1			

# 4. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage		-	65	V
V <sub>GS(amp)main</sub>	main amplifier gate-source voltage		-5	+13	V
V <sub>GS(amp)peak</sub>	peak amplifier gate-source voltage		-5	+13	V
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature	<u>[1]</u>	-	225	°C

Continuous use at maximum temperature will affect the reliability, for details refer to the online MTF calculator.

## 5. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
R <sub>th(j-c)</sub>		V <sub>DS</sub> = 28 V; I <sub>Dq</sub> = 400 mA (main); V <sub>GS(amp)peak</sub> = 0.5 V; T <sub>case</sub> = 80 °C		
		P <sub>L</sub> = 47.5 dBm	0.26	K/W
		P <sub>L</sub> = 49.5 dBm	0.19	K/W

## 6. Characteristics

Table 7. DC characteristics

 $T_i = 25 \,^{\circ}\text{C}$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Main dev	rice					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 1.2 \text{ mA}$	65	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 120 mA	1.5	2.0	2.5	V
$V_{GSq}$	gate-source quiescent voltage	$V_{DS}$ = 28 V; $I_{D}$ = 400 mA	1.65	2.25	2.85	V
I <sub>DSS</sub>	drain leakage current	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 32 V	-	-	2.8	μΑ
I <sub>DSX</sub>	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	-	26	-	Α
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 11 V; V <sub>DS</sub> = 0 V	-	-	280	nA
9 <sub>fs</sub>	forward transconductance	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 0.12 A	-	1.27	-	S
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 4.2 \text{ A}$	-	120	198	mΩ
Peak dev	rice					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 2.2 \text{ mA}$	65	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 220 mA	1.5	2.0	2.5	V
$V_{GSq}$	gate-source quiescent voltage	V <sub>DS</sub> = 28 V; I <sub>D</sub> = 1000 mA	1.55	2.15	2.75	V
I <sub>DSS</sub>	drain leakage current	V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 32 V	-	-	2.8	μΑ
I <sub>DSX</sub>	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	-	48	-	Α
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 11 V; V <sub>DS</sub> = 0 V	-	-	280	nA
9 <sub>fs</sub>	forward transconductance	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 0.22 A	-	2.32	-	S
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 7.7 \text{ A}$	-	65	112	mΩ

#### Table 8. RF characteristics

Specifications are tested with test signal: 1-carrier W-CDMA; PAR = 7.2 dB at 0.01 % probability on the CCDF; 3GPP test model 1; 1 to 64 DPCH;  $f_1$  = 1805 MHz;  $f_2$  = 1880 MHz; RF performance at  $V_{DS}$  = 28 V;  $I_{Dq}$  = 300 mA (main);  $V_{GS(amp)peak}$  = 0.5 V;  $T_{case}$  = 25 °C; unless otherwise specified; in an asymmetrical Doherty production test circuit at frequencies from 1805 MHz to 1880 MHz.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Gp	power gain	$P_{L(AV)} = 47.5 \text{ dBm}$	14.5	15.7	-	dB
RLin	input return loss	$P_{L(AV)} = 47.5 \text{ dBm}$	-	-9	-5	dB
$\eta_{D}$	drain efficiency	$P_{L(AV)} = 47.5 \text{ dBm}$	42.5	47.5	-	%
ACPR	adjacent channel power ratio	P <sub>L(AV)</sub> = 47.5 dBm	-	-31	-26	dBc

## 7. Test information

### 7.1 Ruggedness in Doherty operation

The BLC9G20LS-361AVT is capable of withstanding a load mismatch corresponding to a VSWR = 10 : 1 through all phases under the following conditions:  $V_{DS}$  = 28 V;  $I_{Dq}$  = 400 mA;  $V_{GS(amp)peak}$  = 0.5 V;  $P_L$  = 120 W (CW); f = 1805 MHz; tested on the Doherty development test circuit.

## 7.2 Impedance information

Table 9. Typical impedance of main device Measured load-pull data of main device;  $I_{Dq}$  = 720 mA;  $V_{DS}$  = 28 V.

f	Z <sub>S</sub> [1]	Z <sub>L</sub> [1]	P <sub>L</sub> [2]	η <sub>D</sub> [2]	G <sub>p</sub> [3]
(MHz)	<b>(</b> Ω <b>)</b>	<b>(</b> Ω <b>)</b>	(W)	(%)	(dB)
Maximum	power load			,	
1805	1.0 – j4.0	1.4 – j3.5	155	57.5	18.4
1843	1.4 – j3.9	1.4 – j3.5	151	57.1	18.0
1880	1.1 – j4.1	1.4 – j3.5	151	57.1	18.5
Maximum	drain efficiency lo	ad		,	
1805	1.0 – j4.0	2.8 - j2.0	104	69.0	20.9
1843	1.4 – j3.9	2.6 – j1.8	102	69.1	20.5
1880	1.1 – j4.1	2.4 – j2.1	106	68.3	21.0

<sup>[1]</sup>  $Z_S$  and  $Z_L$  defined in Figure 1.

Table 10. Typical impedance of peak device

Measured load-pull data of peak device;  $I_{Dq} = 1320 \text{ mA}$ ;  $V_{DS} = 28 \text{ V}$ .

f	Z <sub>S</sub> [1]	Z <sub>L</sub> [1]	P <sub>L</sub> [2]	η <b>ρ <sup>[2]</sup></b>	G <sub>p</sub> [3]
(MHz)	(Ω)	(Ω)	(W)	(%)	(dB)
Maximum po	ower load		·	·	
1805	0.9 – j4.8	2.3 - j3.6	262	55.3	19.2
1843	1.8 – j4.9	2.3 - j3.6	256	54.7	18.7
1880	1.5 – j5.4	2.3 - j3.6	254	54.6	19.3
Maximum dr	ain efficiency lo	ad			<u>'</u>
1805	0.9 – j4.8	3.4 – j1.5	183	64.2	21.5
1843	1.8 – j4.9	3.1 – j1.4	176	63.5	21.1
1880	1.5 – j5.4	2.7 – j1.5	179	63.1	21.6

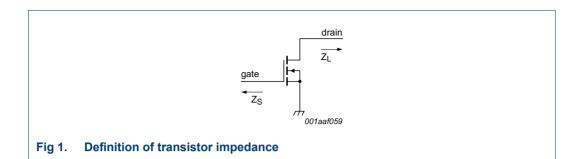
<sup>[1]</sup>  $Z_S$  and  $Z_L$  defined in Figure 1.

<sup>[2] 0.3</sup> dB power back off from 3 dB compression points.

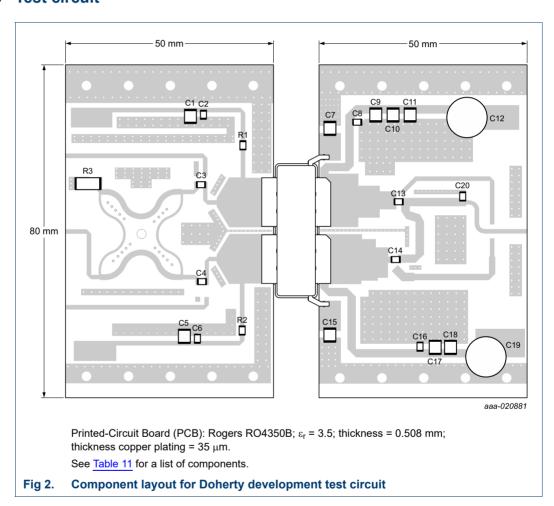
<sup>[3] 6.0</sup> dB power back off from 3 dB compression points.

<sup>[2]</sup>  $\,$  0.3 dB power back off from 3 dB compression points.

<sup>[3] 6.0</sup> dB power back off from 3 dB compression points.



#### 7.3 Test circuit



**Table 11. List of components** See Figure 2 for component layout.

Component	Description	Value	Remarks
C1, C5, C7, C9, C10, C11, C15, C17, C18	multilayer ceramic chip capacitor	10 μF, 50 V	Murata
C2, C3, C4, C6, C8, C14, C16	multilayer ceramic chip capacitor	9.1 pF	ATC600F
C12, C19	electrolytic capacitor	2200 μF, 63 V	
C13	multilayer ceramic chip capacitor	8.2 pF	ATC600F

Table 11. List of components ...continued

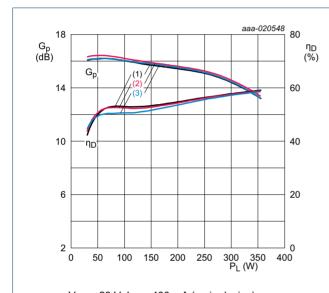
See Figure 2 for component layout.

Component	Description	Value	Remarks
C20	multilayer ceramic chip capacitor	0.5 pF	ATC600F
R1, R2	resistor	5.1 Ω	SMD 0805
R3	resistor	50 Ω	SMD 2512

## 7.4 Graphical data

All data are measured on the Doherty development test circuit.

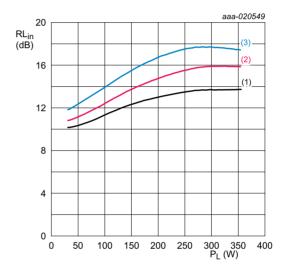
#### 7.4.1 Pulsed CW



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 400 mA (main device);  $V_{GS(amp)peak}$  = 0.5 V;  $t_p$  = 100  $\mu$ s;  $\delta$  = 10 %.

- (1) f = 1805 MHz
- (2) f = 1843 MHz
- (3) f = 1880 MHz

Fig 3. Power gain and drain efficiency as function of output power; typical values

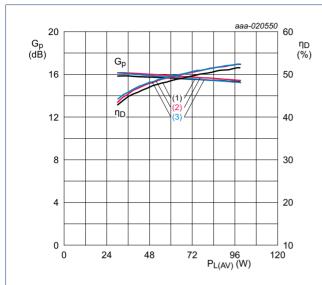


 $V_{DS}$  = 28 V;  $I_{Dq}$  = 400 mA (main device);  $V_{GS(amp)peak}$  = 0.5 V;  $t_p$  = 100  $\mu$ s;  $\delta$  = 10 %.

- (1) f = 1805 MHz
- (2) f = 1843 MHz
- (3) f = 1880 MHz

Fig 4. Input return loss as a function of output power; typical values

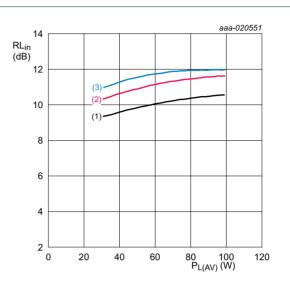
#### 7.4.2 1-Carrier W-CDMA



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 400 mA (main device);  $V_{GS(amp)peak}$  = 0.5 V.

- (1) f = 1805 MHz
- (2) f = 1843 MHz
- (3) f = 1880 MHz

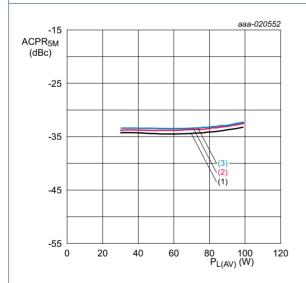
Fig 5. Power gain and drain efficiency as function of average output power; typical values



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 400 mA (main device);  $V_{GS(amp)peak}$  = 0.5 V.

- (1) f = 1805 MHz
- (2) f = 1843 MHz
- (3) f = 1880 MHz

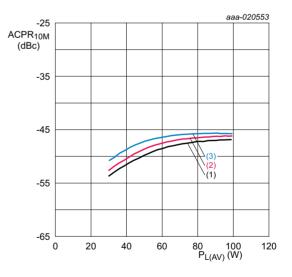
Fig 6. Input return loss as a function of average output power; typical values



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 400 mA (main device);  $V_{GS(amp)peak}$  = 0.5 V.

- (1) f = 1805 MHz
- (2) f = 1843 MHz
- (3) f = 1880 MHz

Fig 7. Adjacent channel power ratio (5 MHz) as a function of average output power; typical values



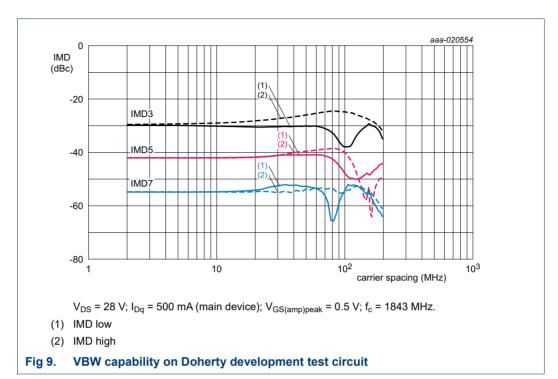
 $V_{DS}$  = 28 V;  $I_{Dq}$  = 400 mA (main device);  $V_{GS(amp)peak}$  = 0.5 V.

- (1) f = 1805 MHz
- (2) f = 1843 MHz
- (3) f = 1880 MHz

Fig 8. Adjacent channel power ratio (10 MHz) as a function of average output power; typical values

BLC9G20LS-361AVT

#### 7.4.3 2-Tone VBW



# 8. Package outline

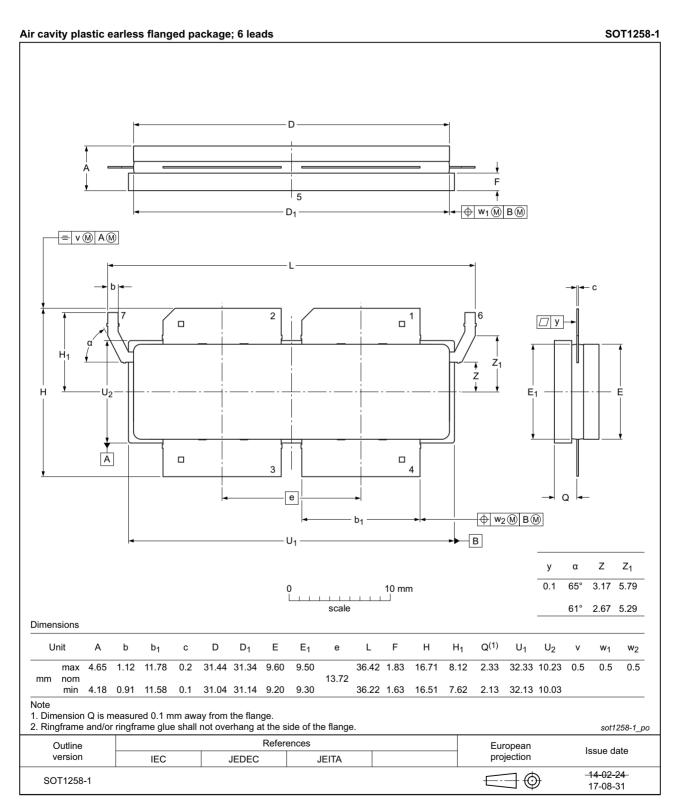


Fig 10. Package outline SOT1258-1

# 9. Handling information

#### **CAUTION**



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

Table 12. ESD sensitivity

ESD model	Class
Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002	C2A [1]
Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001	2 [2]

- [1] CDM classification C2A is granted to any part that passes after exposure to an ESD pulse of 500 V, but fails after exposure to an ESD pulse of 750 V.
- [2] HBM classification 2 is granted to any part that passes after exposure to an ESD pulse of 2000 V, but fails after exposure to an ESD pulse of 4000 V.

## 10. Abbreviations

Table 13. Abbreviations

Acronym	Description		
3GPP	3rd Generation Partnership Project		
CCDF	Complementary Cumulative Distribution Function		
CW	Continuous Wave		
DPCH	Dedicated Physical CHannel		
ESD	ElectroStatic Discharge		
LDMOS	Laterally Diffused Metal-Oxide Semiconductor		
MTF	Median Time to Failure		
PAR	Peak-to-Average Ratio		
SMD	Surface Mounted Device		
VBW	Video BandWidth		
W-CDMA	Wideband Code Division Multiple Access		

# 11. Revision history

Table 14. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLC9G20LS-361AVT v.3	20171124	Product data sheet	-	BLC9G20LS-361AVT v.2
Modifications:	<u>Table 3 on page 2</u> : changed simplified version drawing SOT1258-3 to SOT1258-1			
	• Table 4 on page 2: changed version SOT1258-3 to SOT1258-1			
	• Figure 2 on page 5: updated figure			
	• Figure 10 on page 9: changed package outline drawing SOT1258-3 to SOT1258-1			
BLC9G20LS-361AVT v.2	20161202	Product data sheet	-	BLC9G20LS-361AVT v.1
BLC9G20LS-361AVT v.1	20160225	Product data sheet	-	-

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## 12. Legal information

#### 12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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# **BLC9G20LS-361AVT**

#### **Power LDMOS transistor**

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