TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74LCX32F,TC74LCX32FN,TC74LCX32FT

Low-Voltage Quad 2-Input OR Gate with 5-V Tolerant Inputs and Outputs

The TC74LCX32F/FN/FT is a high-performance CMOS 2-input OR gate. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low-power dissipation.

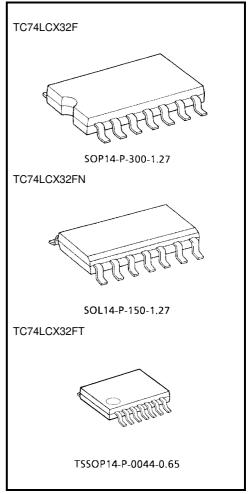
The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

All inputs are equipped with protection circuits against static discharge.

#### **Features**

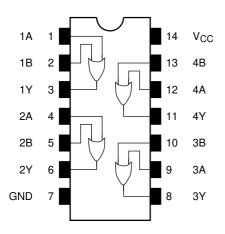
- Low-voltage operation: V<sub>CC</sub> = 2.0 to 3.6 V
- High-speed operation:  $t_{pd} = 5.5 \text{ ns (max) (VCC} = 3.0 \text{ to } 3.6 \text{ V)}$
- Output current:  $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: ±500 mA
- Available in JEDEC SOP, JEITA SOP and TSSOP
- · Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 32 type

Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight SOP14-P-300-1.27: 0.18 g (typ.) SOL14-P-150-1.27: 0.12 g (typ.) TSSOP14-P-0044-0.65: 0.06 g (typ.)

# Pin Assignment (top view)



# **IEC Logic Symbol**

1A	1 2	≥ 1	3 1Y
1B 2A	<u>4</u> 5		62Y
2B 3A	9		8 3Y
3B 4A	10 12		11
4B	13		———— 4Y

#### **Truth Table**

Inp	uts	Outputs		
Α	В	Y		
L	L	L		
L	Н	Н		
Н	L	Н		
Н	Н	Н		

# **Maximum Ratings**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V <sub>CC</sub>	-0.5 to 7.0	V	
DC input voltage	V <sub>IN</sub>	-0.5 to 7.0	V	
		-0.5 to 7.0 (Note 1)		
DC output voltage	V <sub>OUT</sub>	$-0.5$ to $V_{CC}+0.5$	V	
		(Note 2)	<u> </u>	
Input diode current	I <sub>IK</sub>	-50	mA	
Output diode current	lok	±50 (Note 3)	mA	
DC output current	lout	±50	mA	
Power dissipation	P <sub>D</sub>	180	mW	
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA	
Storage temperature	T <sub>stg</sub>	-65 to 150	°C	

Note 1:  $V_{CC} = 0 V$ 

Note 2: High or low state.  $I_{\mbox{OUT}}$  absolute maximum rating must be observed.

Note 3:  $V_{OUT} < GND, V_{OUT} > V_{CC}$ 



# **Recommended Operating Conditions**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V <sub>CC</sub>	2.0 to 3.6	V	
rower supply voltage	<b>V</b> CC	1.5 to 3.6 (Note 4)	V	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	V	0 to 5.5 (Note 5)	٧	
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub> (Note 6)	V	
Output current	I <sub>OH</sub> /I <sub>OL</sub>	±24 (Note 7)	mA	
Output current	IOH/IOL	±12 (Note 8)		
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 9)	ns/V	

Note 4: Data retention only

Note 5:  $V_{CC} = 0 V$ 

Note 6: High or low state

Note 7:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 8:  $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$ 

Note 9:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V

#### **Electrical Characteristics**

# DC Characteristics (Ta = -40 to $85^{\circ}$ C)

Characteristics		Symbol	Test (	Condition		Min	Max	Unit
		Cymbol	rest condition		V <sub>CC</sub> (V)	IVIIII	IVIAX	Offic
Input voltage	H-level	V <sub>IH</sub>	_		2.7 to 3.6	2.0		V
input voitage	L-level	V <sub>IL</sub>		_	2.7 to 3.6	_	0.8	v
			V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -100 μA	2.7 to 3.6	V <sub>CC</sub> - 0.2	_	
	H-level	V <sub>OH</sub>		$I_{OH} = -12 \text{ mA}$	2.7	2.2	_	V
				$I_{OH} = -18 \text{ mA}$	3.0	2.4	_	
Output voltage				I <sub>OH</sub> = -24 mA	3.0	2.2	_	
	L-level V <sub>OL</sub>			$I_{OL} = 100 \mu A$	2.7 to 3.6		0.2	
		Vai	$V_{OL}$ $V_{IN} = V_{IL}$	I <sub>OL</sub> = 12 mA	2.7	_	0.4	
		VOL		I <sub>OL</sub> = 16 mA	3.0	_	0.4	
				I <sub>OL</sub> = 24 mA	3.0		0.55	
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V	•	2.7 to 3.6		±5.0	μΑ
Power-off leakage current		I <sub>OFF</sub>	V <sub>IN</sub> /V <sub>OUT</sub> = 5.5 V		0	_	10.0	μΑ
Quiocoont cupply current		loo	V <sub>IN</sub> = V <sub>CC</sub> or GND		2.7 to 3.6	_	10.0	
Quiescent supply current		Icc	V <sub>IN</sub> = 3.6 to 5.5 V		2.7 to 3.6	_	±10.0	μΑ
Increase in Icc per input		Δlcc	$V_{IH} = V_{CC} - 0.6 V$		2.7 to 3.6	_	500	



#### AC Characteristics ( $Ta = -40 \text{ to } 85^{\circ}\text{C}$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	2.7	_	6.2	ns
i Topagation delay time	$t_{pHL}$		$3.3 \pm 0.3$	1.5	5.5	
Output to output skew	t <sub>osLH</sub>	(Note 10)	2.7	_	_	20
Output to output skew	$t_{osHL}$	(Note 10)	$3.3 \pm 0.3$	_	1.0	ns

Note 10: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{DLHm} - t_{DLHn}|, t_{OSHL} = |t_{DHLm} - t_{DHLn}|)$ 

#### **Dynamic Switching Characteristics**

(Ta = 25°C, input:  $t_r = t_f = 2.5$  ns,  $C_L = 50$  pF,  $R_L = 500 \Omega$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Quiet output maximum dynamic V <sub>OL</sub>	V <sub>OLP</sub>	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	٧
Quiet output minimum dynamic V <sub>OL</sub>	V <sub>OLV</sub>	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	٧

#### **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>	_		3.3	7	pF
Output capacitance	C <sub>OUT</sub>	_		0	8	pF
Power dissipation capacitance	C <sub>PD</sub>	$f_{IN} = 10 \text{ MHz}$	Note 11)	3.3	25	pF

Note 11: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per gate)}$ 

#### **AC Test Circuit**

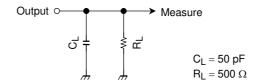


Figure 1

#### **AC Waveform**

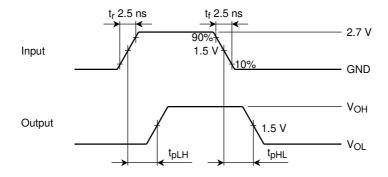
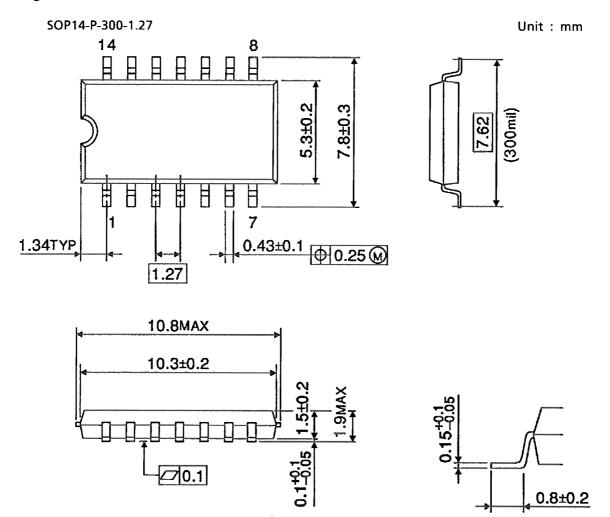


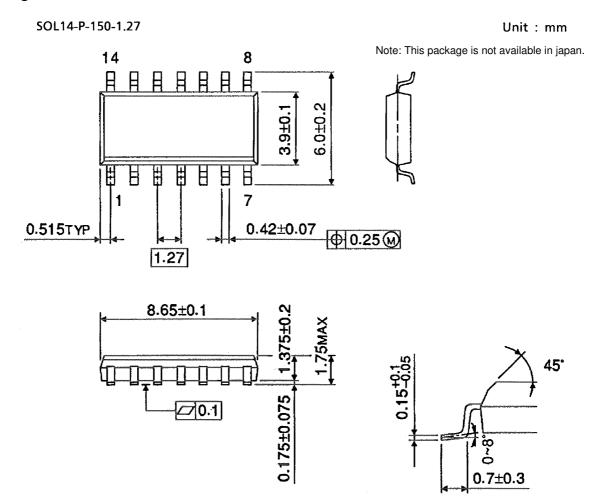
Figure 2 t<sub>pLH</sub>, t<sub>pHL</sub>

# **Package Dimensions**



Weight: 0.18 g (typ.)

# **Package Dimensions**



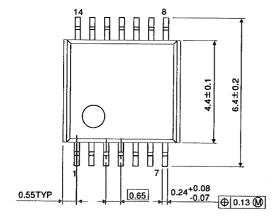
7

Weight: 0.12 g (typ.)

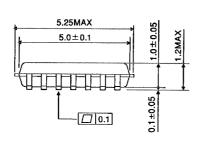
Unit: mm

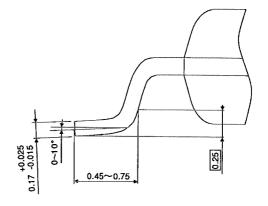
# **Package Dimensions**

TSSOP14-P-0044-0.65









Weight: 0.06 g (typ.)

### RESTRICTIONS ON PRODUCT USE

000707EBA

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document are subject to the foreign exchange and foreign trade laws.
- The information contained herein is presented only as a guide for the applications of our products. No
  responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other
  rights of the third parties which may result from its use. No license is granted by implication or otherwise under
  any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.