



ZXMN3B04N8

30V N-CHANNEL ENHANCEMENT MODE MOSFET 2.5V GATE DRIVE

Product Summary

BV _{DSS}	Rds(on)	Ι _D T _A = +25°C	
30V	0.025Ω@V _{GS} = 4.5V	8.9A	

Description

This new generation of Trench MOSFETs from Diodes Incorporated utilizes a unique structure that combines the benefits of low onresistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Disconnect Switches
- Motor Control

Features

- Low On-Resistance
- Fast Switching Speed
- Low Threshold
- Low Gate Drive
- Low Profile SO-8 Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

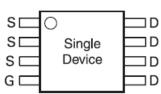
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.076 grams (Approximate)



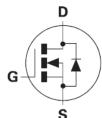
SO-8

Top View



Top View

Pin Out Configuration



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Reel Size	Tape Width	Quantity Per Reel
ZXMN3B04N8TA	SO-8	7"	12mm	500 Units
ZXMN3B04N8TC	SO-8	13"	12mm	2500 Units

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:



ZXMN3B04 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 17 = 2017) WW = Week Code (01 to 53)



Maximum Ratings

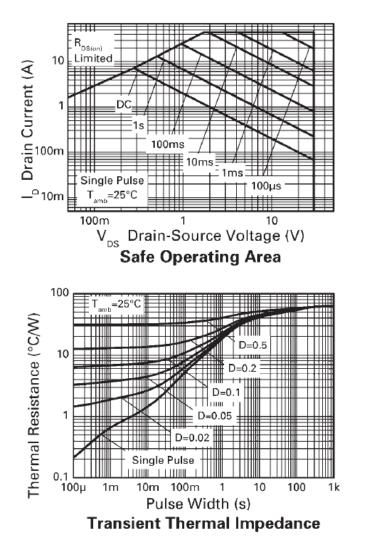
Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	30	V	
Gate-Source Voltage		V _{GS}	±12	V
Continuous Drain Current @V _{GS} = 4.5V	$T_A = +25^{\circ}C$ (Note 6) $T_A = +70^{\circ}C$ (Note 6) $T_{A} = +25^{\circ}C$ (Note 5)	ID	8.9 7.3 7.2	А
Pulsed Drain Current (Note 7)	L , , , , , , , , , , , , , , , , , , ,	I _{DM}	45	А
Continuous Source Current (Body Diode) (Note 6)		IS	4.5	A
Pulsed Source Current (Body Diode) (Note 7)		I _{SM}	45	А
Power Dissipation at T_{A} = +25°C (Note 5) Linear Derating Factor		PD	2 16	W mW/°C
Power Dissipation at T_A = +25°C (Note 6) Linear Derating Factor		PD	3 24	W mW/°C
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

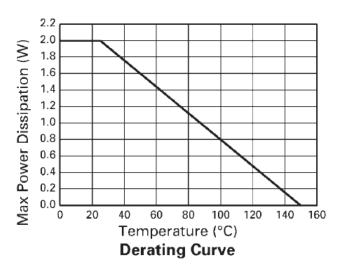
Thermal Characteristics

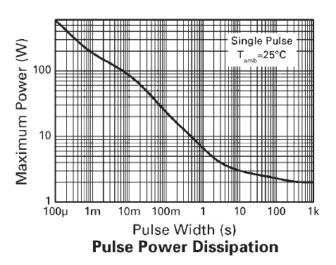
Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA} 62.5		°C/W
Thermal Resistance, Junction to Ambient (Note 6)	R _{0JA}	41.4	°C/W

Notes: 5. For a device surface mounted on 50mm x 50mm FR-4 PCB with high coverage of single sided 2oz copper, in still air conditions. 6. For a device surface mounted on FR-4 PCB measured at t ≤10 sec.

7. Repetitive rating - 25mm x 25mm FR-4 PCB, D=0.02, pulse width 300µs - pulse width limited by maximum junction temperature.









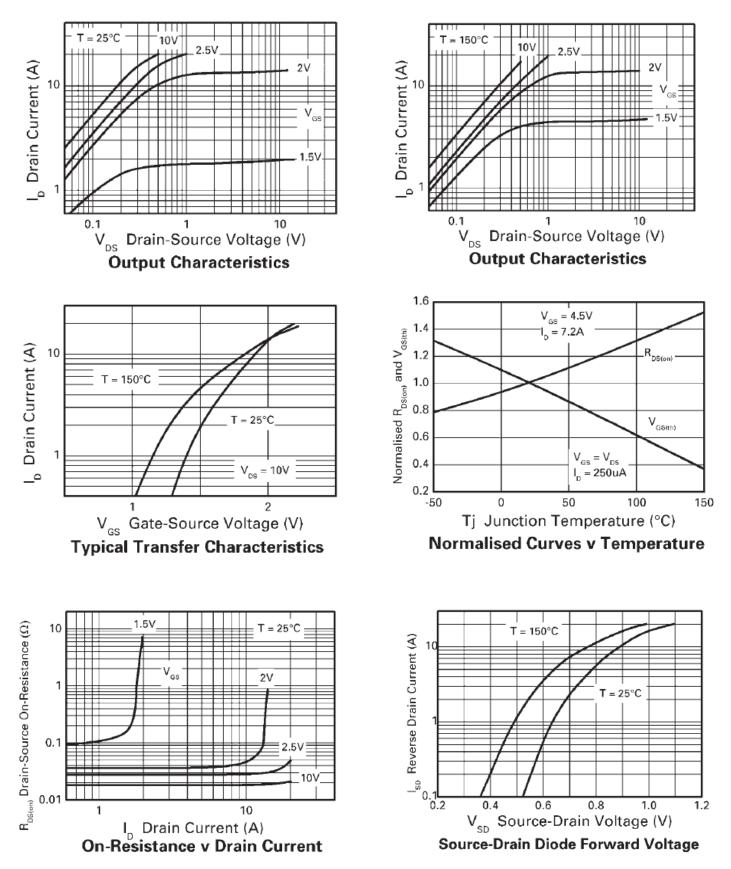
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
STATIC						-	
Drain-Source Breakdown Voltage	BV _{DSS}	30		_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS	_	_	0.5	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Body Leakage	IGSS	_	_	100	nA	$V_{GS} = \pm 12V$, $V_{DS} = 0V$	
Gate-Source Threshold Voltage	V _{GS(TH)}	0.7	_	_	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance (Note 8)	_	—	0.021	0.025	Ω	$V_{GS} = 4.5V, I_D = 7.2A$	
	R _{DS(ON)}	_	0.028	0.040		$V_{GS} = 2.5V, I_D = 5.7A$	
Forward Transconductance (Notes 8 and 10)	g fs	—	24		S	$V_{DS} = 15V, I_D = 7.2A$	
DYNAMIC (Note 10)							
Input Capacitance	Ciss		2480	—		V _{DS} = 15V, f = 1.0MHz, V _{GS} = 0V	
Output Capacitance	Coss	_	318	—	pF		
Reverse Transfer Capacitance	C _{rss}	_	184	_			
SWITCHING (Notes 9 and 10)			-		-		
Turn-On Delay Time	t _{D(ON)}	_	9	_		$\label{eq:VDD} \begin{split} V_{DD} &= 15V, \ R_G = 6.0\Omega, \ I_D = 1A, \\ V_{GS} &= 4.5V \end{split}$	
Rise Time	t _R	_	11.5	_	ns		
Turn-Off Delay Time	t _{D(OFF)}		40		115		
Fall Time	t _F	_	16.6	_			
Total Gate Charge	Qg	_	23.1	—		V_{DS} = 15V, V_{GS} = 4.5V, I_{D} = 7.2A	
Gate-Source Charge	Q _{gs}	_	4.9	_	nC		
Gate-Drain Charge	Q _{gd}	_	6.2	_			
SOURCE-DRAIN DIODE						-	
Diode Forward Voltage (Note 8)	V _{SD}	_	0.85	0.95	V	$T_J = +25^{\circ}C$, $I_S = 8A$, $V_{GS} = 0V$	
Reverse Recovery Time (Note 10)	t _{RR}	_	17.9	_	ns	di/dt = 100A/µs, I _F = 3.2A,	
Reverse Recovery Charge (Note 10)	Q _{RR}		10		nC	$T_J = +25^{\circ}C$	

 8. Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%.
 9. Switching characteristics are independent of operating junction temperature.
 10. For design aid only, not subject to production testing. Notes:

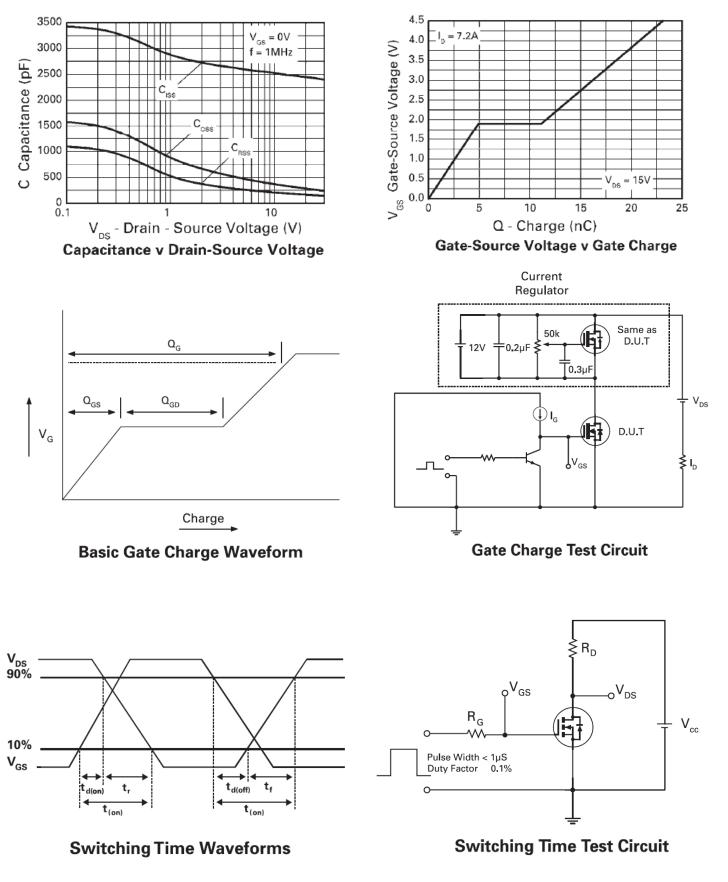


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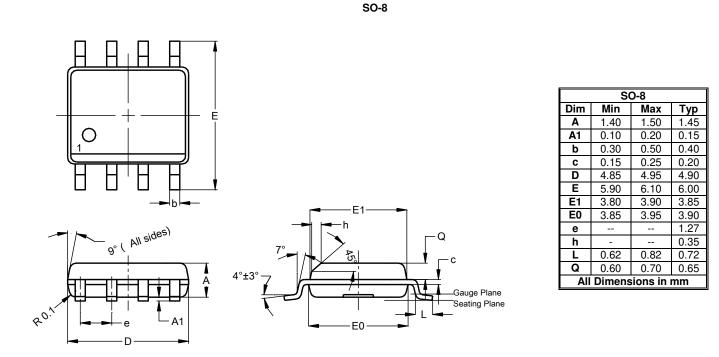
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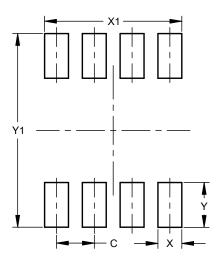
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SO-8

Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Y	1.505
Y1	6.50



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