N-Channel Power MOSFET 600 V, 0.75 Ω

Features

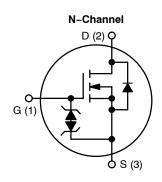
- Low ON Resistance
- Low Gate Charge
- ESD Diode–Protected Gate
- 100% Avalanche Tested
- 100% Rg Tested
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

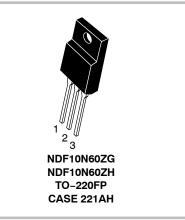


ON Semiconductor®

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V _{DSS} (@ T _{Jmax})	R _{DS(ON)} (MAX) @ 5 A
650 V	0.75 Ω





ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise noted)

Rating	Symbol	NDF	Unit
Drain-to-Source Voltage	V _{DSS}	600	V
Continuous Drain Current, $R_{\theta JC}$ (Note 1)	۱ _D	10	А
Continuous Drain Current $T_A = 100^{\circ}C, R_{\theta JC}$ (Note 1)	۱ _D	6.0	A
Pulsed Drain Current, t _P = 10 μs	I _{DM}	40	A
Power Dissipation, $R_{\theta JC}$	PD	39	W
Gate-to-Source Voltage	V _{GS}	±30	V
Single Pulse Avalanche Energy (L = 6.0 mH, I _D = 10 A)	E _{AS}	300	mJ
ESD (HBM) (JESD22–A114)	V _{esd}	3900	V
RMS Isolation Voltage (t = 0.3 sec., R.H. \leq 30%, T _A = 25°C) (Figure 13)	V _{ISO}	4500	V
Peak Diode Recovery (Note 2)	dV/dt	4.5	V/ns
MOSFET dV/dt	dV/dt	60	V/ns
Continuous Source Current (Body Diode)	I _S	10	А
Maximum Temperature for Soldering Leads	ΤL	260	°C
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Limited by maximum junction temperature.

2. $I_S \leq$ 10 A, di/dt \leq 200 A/µs, V_{DD} = 80% BV_{DSS}



See detailed ordering, marking and shipping information on page 6 of this data sheet.

THERMAL RESISTANCE

Parameter	Symbol	NDF10N60Z	Unit
Junction-to-Case (Drain)	$R_{ extsf{ heta}JC}$	3.2	°C/W
Junction-to-Ambient Steady State (Note 3)	$R_{\theta JA}$	50	

3. Insertion mounted

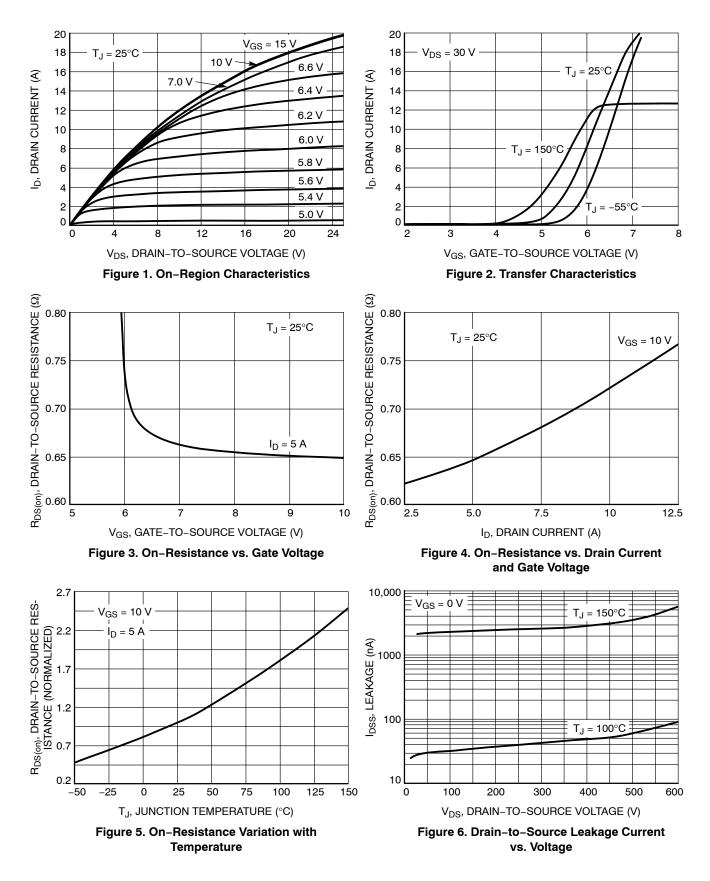
ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise noted)

Characteristic	Test Conditions		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS							-
Drain-to-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 1 m	۱A	BV _{DSS}	600			V
Breakdown Voltage Temperature Coefficient	Reference to 25°C I _D = 1 mA	Э,	$\Delta BV_{DSS}/\Delta T_{J}$		0.6		V/°C
Drain-to-Source Leakage Current		25°C	I _{DSS}			1	μA
	$V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V}$	150°C				50	
Gate-to-Source Forward Leakage	V _{GS} = ±20 V		I _{GSS}			±10	μA
ON CHARACTERISTICS (Note 4)							
Static Drain-to-Source On-Resistance	V _{GS} = 10 V, I _D = 5.0) A	R _{DS(on)}		0.65	0.75	Ω
Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 100	μA	V _{GS(th)}	3.0	3.9	4.5	V
Forward Transconductance	V _{DS} = 15 V, I _D = 10	A	9FS		7.9		S
OYNAMIC CHARACTERISTICS							
Input Capacitance (Note 5)			C _{iss}	1097	1373	1645	pF
Output Capacitance (Note 5)	V _{DS} = 25 V, V _{GS} = 0 f = 1.0 MHz	0 V,	C _{oss}	118	150	178	
Reverse Transfer Capacitance (Note 5)			C _{rss}	20	35	50	
Total Gate Charge (Note 5)			Qg	23	47	68	nC
Gate-to-Source Charge (Note 5)	V _{DD} = 300 V, I _D = 10) A,	Q _{gs}	5.0	9.0	14	
Gate-to-Drain ("Miller") Charge (Note 5)	V _{GS} = 10 V		Q _{gd}	12	26	36	
Plateau Voltage			V _{GP}		6.4		V
Gate Resistance			Rg	0.5	1.5	4.5	Ω
RESISTIVE SWITCHING CHARACTERIST	ICS						
Turn-On Delay Time			t _{d(on)}		15		ns
Rise Time	V _{DD} = 300 V, I _D = 10) A,	t _r		31		-
Turn-Off Delay Time	$V_{GS} = 10 \text{ V}, \text{ R}_{G} = 5$	Ω	t _{d(off)}		40		
Fall Time			t _f		23		

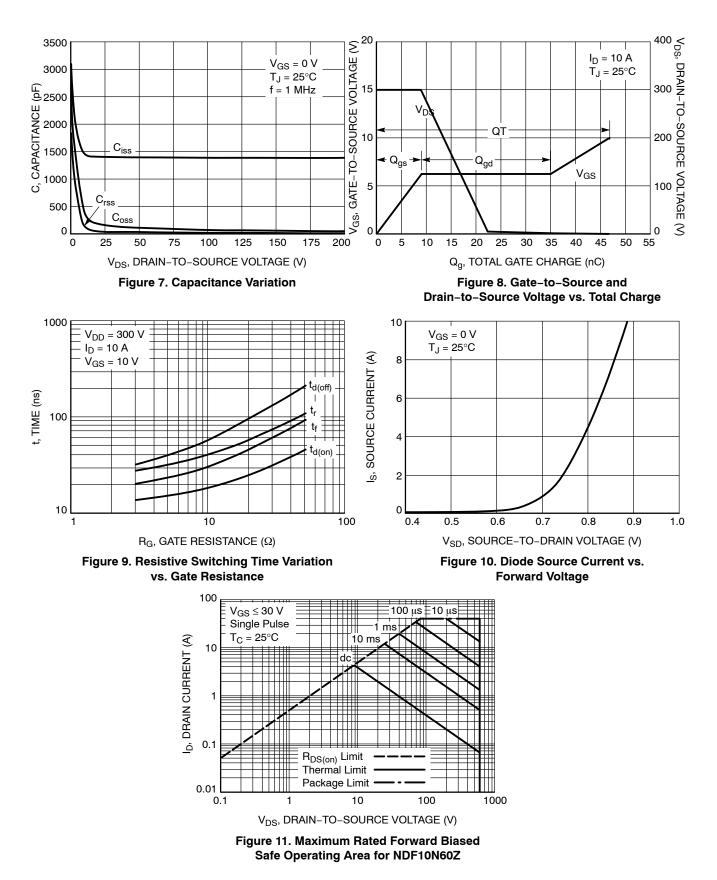
Diode Forward Voltage	$I_{S} = 10 \text{ A}, V_{GS} = 0 \text{ V}$	V _{SD}		1.6	V
Reverse Recovery Time	V_{GS} = 0 V, V_{DD} = 30 V	t _{rr}	395		ns
Reverse Recovery Charge	I _S = 10 A, di/dt = 100 A/μs	Q _{rr}	3.0		μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
4. Pulse Width ≤ 380 µs, Duty Cycle ≤ 2%.
5. Guaranteed by design.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

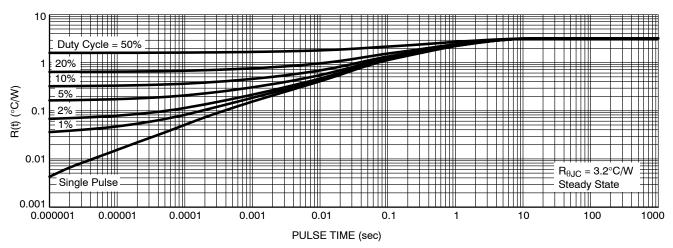
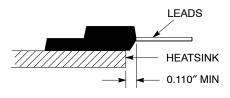


Figure 12. Thermal Impedance for NDF10N60Z





Measurement made between leads and heatsink with all leads shorted together.

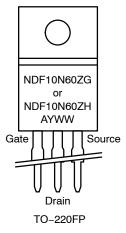
*For additional mounting information, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ORDERING INFORMATION

Order Number	Package	Shipping [†]
NDF10N60ZG	TO-220FP (Pb-Free, Halogen-Free)	50 Units / Rail
NDF10N60ZH	TO–220FP (Pb–Free, Halogen–Free)	50 Units / Rail

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MARKING DIAGRAMS

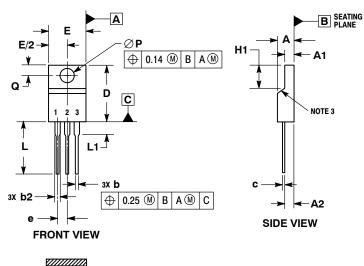


- A Y = Location Code
- = Year
- WW = Work Week
- G, H = Pb-Free, Halogen-Free Package

PACKAGE DIMENSIONS

TO-220 FULLPACK, 3-LEAD CASE 221AH

ISSUE F



- NOTES: DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS. 3. CONTOUR UNCONTROLLED IN THIS AREA.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH AND GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO 4. EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE TO BE MEASURED AT OUTERMOST EXTREME OF THE PLASTIC BODY. DIMENSION b2 DOES NOT INCLUDE DAMBAR PROTRUSION. LEAD WIDTH INCLUDING PROTRUSION SHALL NOT EXCEED 2.00.

- CONTOURS AND FEATURES OF THE MOLDED PACKAGE BODY 6. MAY VARY WITHIN THE ENVELOP DEFINED BY DIMENSIONS A1 AND H1 FOR MANUFACTURING PURPOSES.

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.30	4.70	
A1	2.50	2.90	
A2	2.50	2.90	
b	0.54	0.84	
b2	1.10	1.40	
C	0.49	0.79	
D	14.70	15.30	
Ε	9.70	10.30	
е	2.54	BSC	
H1	6.60	7.10	
L	12.50	14.73	
L1		2.80	
Ρ	3.00	3.40	
Q	2.80	3.20	

NOTE 6 D D

SECTION D-D

ALTERNATE CONSTRUCTION

NOTE 6 **SECTION A-A**

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