Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSVII)

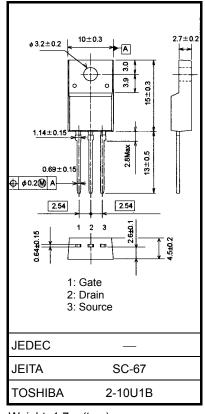
# **TK10A50D**

#### Switching Regulator Applications

- Low drain-source ON-resistance:  $RDS(ON) = 0.62 \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 5.0 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 500 \ V)$
- Enhancement mode:  $V_{th} = 2.0$  to 4.0 V ( $V_{DS} = 10 \text{ V}$ ,  $I_D = 1 \text{ mA}$ )

Charaota	riation	Symbol	Rating	Unit	
Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	500	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	10		
	Pulse (t = 1 ms) (Note 1)	I <sub>DP</sub>	40	A	
Drain power dissipation (Tc = 25°C)		PD	45	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	264	mJ	
Avalanche current		I <sub>AR</sub>	10	А	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	4.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

#### Absolute Maximum Ratings (Ta = 25°C)



Weight: 1.7 g (typ.)

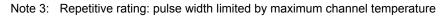
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

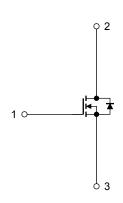
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 4.49 mH, R<sub>G</sub> = 25  $\Omega$ , I<sub>AR</sub> = 10 A



This transistor is an electrostatic-sensitive device. Handle with care.

#### Internal Connection



Start of commercial production 2008-10

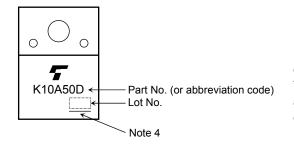
**Electrical Characteristics (Ta = 25°C)** 

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_		±1	μA
Drain cut-off curr	rent	I <sub>DSS</sub>	$V_{DS} = 500 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	500			V
Gate threshold v	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0		4.0	V
Drain-source ON	l-resistance	R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		0.62	0.72	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$	1.3	5.0		S
Input capacitance		C <sub>iss</sub>		_	1050	—	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	_	5	—	pF
Output capacitance		C <sub>oss</sub>			100	_	
Switching time	Rise time	tr	$V_{GS} \\ 0 V \\ 50 \Omega \\ V_{DD} \approx 200 V$		25		
	Turn-on time	t <sub>on</sub>			60		. ns
	Fall time	t <sub>f</sub>			10	_	
	Turn-off time	t <sub>off</sub>	Duty $\leq$ 1%, t <sub>w</sub> = 10 µs		75	_	
Total gate charge		Qg		_	20		
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$		13		nC
Gate-drain charge		Q <sub>gd</sub>	1	_	7		

#### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	10	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	40	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V},$	_	1300	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	12		μC

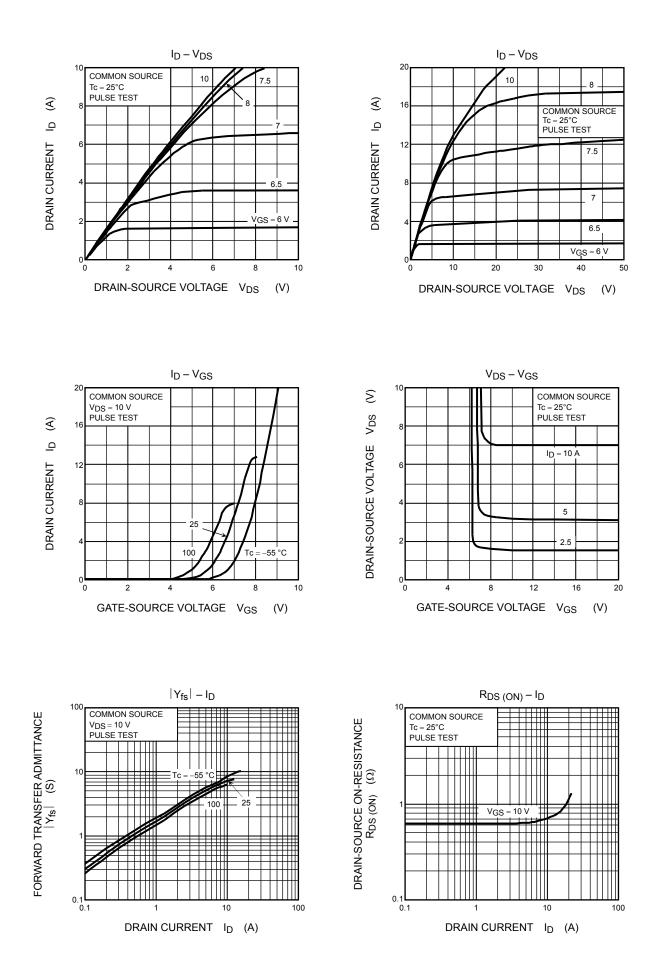
#### Marking



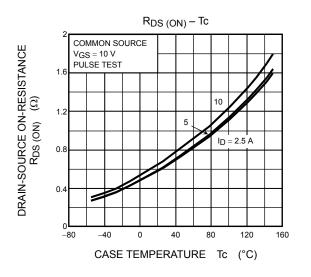
Note 4: A line under a Lot No. identifies the indication of product Labels.: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

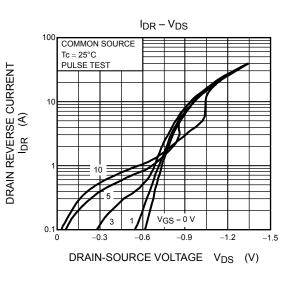
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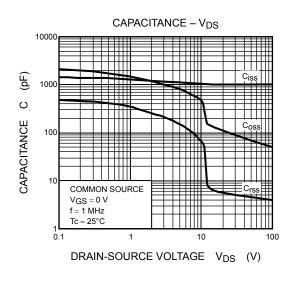
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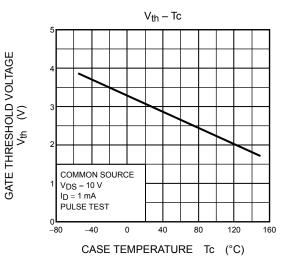


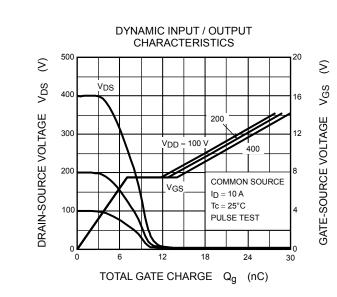
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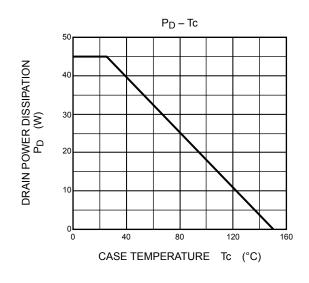


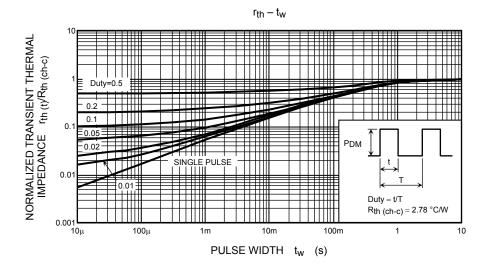




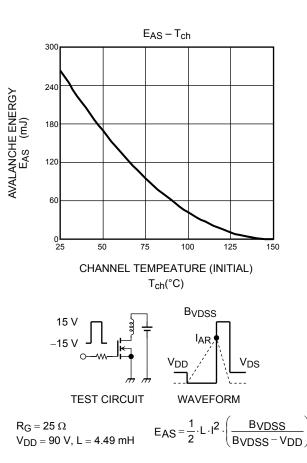








SAFE OPERATING AREA 100 ID max (pulsed) \* ▤┼┼₩ 100 µs ID max (continuous) 10  $\overline{\mathsf{A}}$ DRAIN CURRENT ID DC operation Tc = 25°C 0.1 \*: SINGLE NONREPETITIVE PULSE Tc = 25°C 0.01 CURVES MUST BE DERATED LINEARLY WITH INCREASE IN TEMPERATURE. 0.001 0.1 V<sub>DSS</sub> max 1 10 100 1000 DRAIN-SOURCE VOLTAGE VDS (V)



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