

SEIKO EPSON CORPORATION

REAL TIME CLOCK MODULE (SPI & I²C-Bus)

Power Switching and Low current consumption

RX6110SA B

 Built in frequency adjusted 32.768 	kHz crystal unit.
Interface Type	: SPI & I ² C -Bus
 Operating voltage range 	: 1.6 V to 5.5 V
•The wide voltage for time keeping.	: 1.1 V to 5.5 V
•Low backup current	: 130 nA / 3 V (
•Built-in user RAM	: 128 bit (8 bit ×
 Auto power switching functions 	: When VDD det
	than 1.6V inter

(Typ.) 16, SRAM) teriorates ernal source is switched to VBAT.

•The various functions include full calendar, alarm, timer. Epson prepared Linux driver for development. (http://www5.epsondevice.com/en/information/support/linux_rtc/) The registered trademark Linux® is used pursuant to a sublicense from LMI(Linux Mark Institute)

Block diagram

Vdd VDD Detector Vio Ŧ SPISEL CE/FOE Battery backup connection example (1) /0 Contro Bus VBAT Interfac Registe Control m (System Control) Alarm CLK/SCL Register Timer Register DI/SDA VBAT LVEL Interrupts Controller h Battery backup connection example (2) SHIFTER Clock DO VBAT and OSC Divide Calender /IRQ2 R≹ User RAM С /IRQ1 EDLC ₩ FOUT Controller 401 or Т secondary battery

Pin Function

Signal Name	Input/Output	Function		
SPISEL	Input	The interface select pin. SPI is chosen at a "H" level (Vio voltage) / I ² C is chosen at a "L" level (GND voltage).		
CE/FOE	Input	SPI: Should be held high to allow access to the CPU. Incorporates a pull-down resistor. I ² C: It is an input pin for controlling the DO/FOUT output. When the frequency output from a DO/FOUT pin does not need, CE/FOE pin must be connected to GND.		
CLK/SCL	Input	This is a shift clock input pin for serial data transmission.		
DI/SDA	Input / Output	SPI: This is the data input pin for serial data transfer. I ² C: This is the data input/output pin for serial data transfer.		
DO/FOUT	Output	SPI: This is the data output pin for serial data transfer. I ² C: This is the C-MOS output pin with output control provided via the CE/FOE pin. (frequency selection: 32.768 kHz / 1024 Hz / HHz / HHz)		
/ IRQ1	Output	This pin outputs interrupt signals ("L" level) for alarm, timer, time update, and FOUT. This is an N-ch open-drain output. This pin can output even a backup mode.		
/ IRQ2	Output	This pin outputs interrupt signals ("L" level) for timer and FOUT. This is an C-MOS output. This pin becomes Hi-z in less than VDD=1.6V.		
VDD	-	This is a power-supply pin. It can impress the voltage unlike Vio.		
Vio	-	This pin is a power supply for input and the output and input / output pins. Connected to a positive power supply.		
VBAT	-	Connect a secondary battery or capacitor for backup power supply. If a backup power supply is not present, this pin connect to Vod		
GND	-	Connected to a ground.		

Specifications (characteristics)

Item	Symbol	bol Conditions Min.		Тур.	Max.	Unit
Power voltage	Vdd	VDD - 1.		3.0	5.5	V
Clock voltage	VCLK	Vclk - 1.		3.0	5.5	V
Operating temperature	Topr	Topr —		+25	+85	°C
Frequency characteristics						
Item	Symbol	Conditions		Rating		Unit
Frequency tolerance	∆f/f	Ta = +25 °C VDD = 3.0 V		B: 5 ± 23 *1		× 10 ⁻⁶
Oscillation start-up time	t sta	Ta = +25 °C VDD = 1.6 V		1 Max.		s

(e



Product Number RX6110SA B : X1B000232000100



Overview

Interface type.

 SPI-Bus and I²C-Bus interface. •By a terminal, a switchover of the interface is possible.

- ·Built-in auto power switching function
- •To efficiently charge from VDD to backup battery (Secondary battery, Large capacitor) connected to VBAT is possible. Detects VDD voltage drop(VDET-) and automatically switches to the backup battery. •Frequency output function
- •Output frequency is selectable from 32.768kHz, 1024Hz,1Hz. •Timer function •Timer function is selectable in 1/4096 second from 65535 hours.
- •Timer source clock are thour, 1min, 64Hz, 4096Hz. •It is recorded automatic to TF-bit at the time of event occurrence, and possible to output with /IRQ1 or /IRQ2 pin.

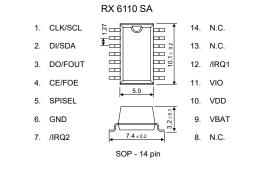
•Alarm function •Alarm function can be set to day of week,

day, hour, and minute. •It is recorded automatic to AF-bit at the alarm occurrence,

and possible to output with /IRQ1 pin output.

- User RAM
 - •128 bit (8 bit x 16, SRAM)

Terminal connection / External dimensions (Unit:mm)



The metal case inside of the molding compound may be exposed on the top or bottom of this product This purely cosmetic and does not have any effect on quality, reliability or electrical specs.

* Refer to application manual for details.

■ Current consumption characteristics T _a = -40 °C to +88					+85 °C	
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Current Consumption	Івк	VBAT = 3.0 V Input pins are "L", VDD = 0 V DO/FOUT=OFF, fCLK = 0 Hz, /IRQ1,2 = OFF, TSEL2="1" It include an OFF leak current of SW between the power supply (VBAT-VDD)	-	130	250	nA
	132k	VDD = 3.0 V fctk = 0 Hz, /IRQ1,2 = OFF, CE/FOE = VIO, DO/FOUT : 32.768 kHz ON, CL = 0 pF	-	1.5	2.1	μA

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IATF 16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

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