

45 V, 100 mA PNP general-purpose transistors Rev. 1 — 26 August 2015

Product data sheet

1. **Product profile**

1.1 General description

PNP general-purpose transistors in a leadless ultra small DFN1010D-3 (SOT1215) Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

Table 1. **Product overview**

Type number	Package	Package		
	Nexperia	JEITA	JEDEC	
BC857AQA	DFN1010D-3	-	-	BC847AQA
BC857BQA	(SOT1215)			BC847BQA
BC857CQA				BC847CQA

1.2 Features and benefits

- General-purpose transistors
- Three current gain selections
- Low package height of 0.37 mm
- Suitable for Automatic Optical Inspection (AOI) of solder joint
- AEC-Q101 qualified

1.3 Applications

- General-purpose switching and amplification
- Mobile applications

1.4 Quick reference data

Table 2. Quick reference data

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-45	V
I _C	collector current		-	-	-100	mA
h _{FE}	DC current gain	$V_{CE} = -5 \text{ V}; \text{ I}_{C} = -2 \text{ mA}$				
	BC857AQA		125	-	250	
	BC857BQA		220	-	475	
	BC857CQA		420	-	800	

nexperia

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2. Pinning information

Table 3.	Pinning			
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		
2	E	emitter		c
3	С	collector		B
4	С	collector	4 3	<u>`</u>
				E sym132
			Transparent top view	

3. Ordering information

Table 4.Ordering information

Type number	Package							
	Name	Description	Version					
BC857AQA	DFN1010D-3	plastic thermal enhanced ultra thin small outline	SOT1215					
BC857BQA	-	package; no leads; 3 terminals; body: $1.1 \times 1.0 \times 0.37$ mm						
BC857CQA								

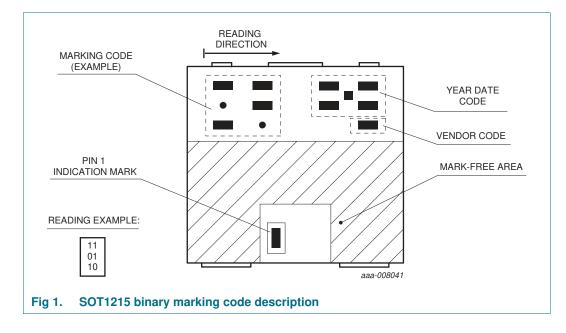
BC857XQA_SER
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4. Marking

Table 5. Marking codes	
Type number	Marking code
BC857AQA	00 11 10
BC857BQA	00 11 11
BC857CQA	01 00 01

4.1 Binary marking code description



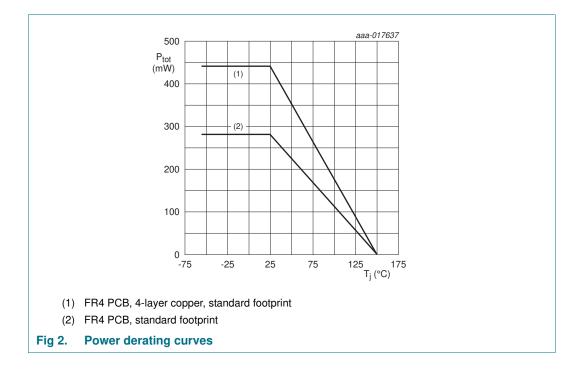
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Limiting values 5.

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter	-	-50	V
V _{CEO}	collector-emitter voltage	open base	-	-45	V
V _{EBO}	emitter-base voltage	open collector	-	-6	V
l _C	collector current		-	-100	mA
I _{CM}	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	-200	mA
I _{BM}	peak base current	single pulse; $t_p \le 1 \text{ ms}$	-	-100	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$			
			<u>[1]</u> _	280	mW
			[2] _	440	mW
Tj	junction temperature		-	+150	°C
T _{amb}	ambient temperature		-55	+150	°C
T _{stg}	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

Device mounted on an FR4 PCB, 4-layer copper; tin-plated and standard footprint. [2]



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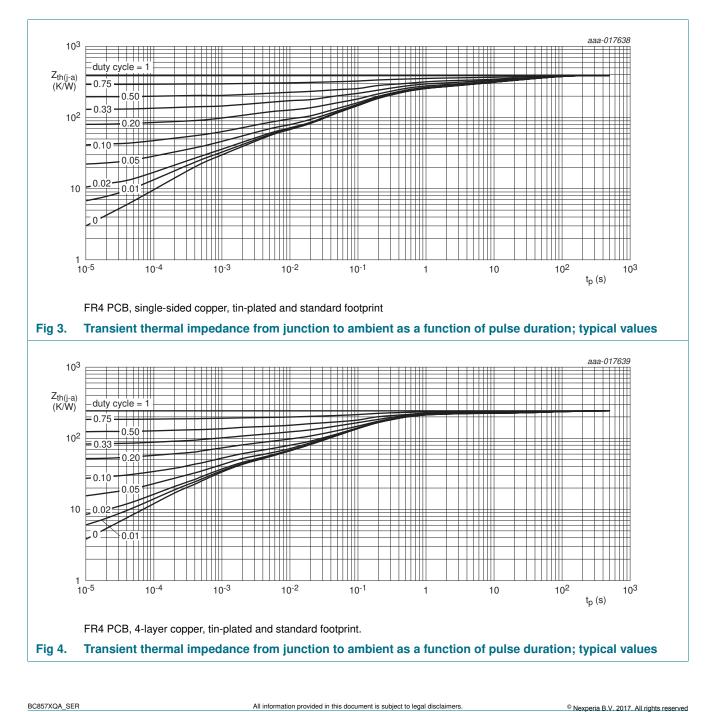
6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	<u>[1]</u>	-	-	446	K/W
			[2]	-	-	284	K/W

Table 7. Thermal characteristics

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, 4-layer copper; tin-plated and standard footprint.



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7. Characteristics

Table 8. Characteristics

 $T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified.

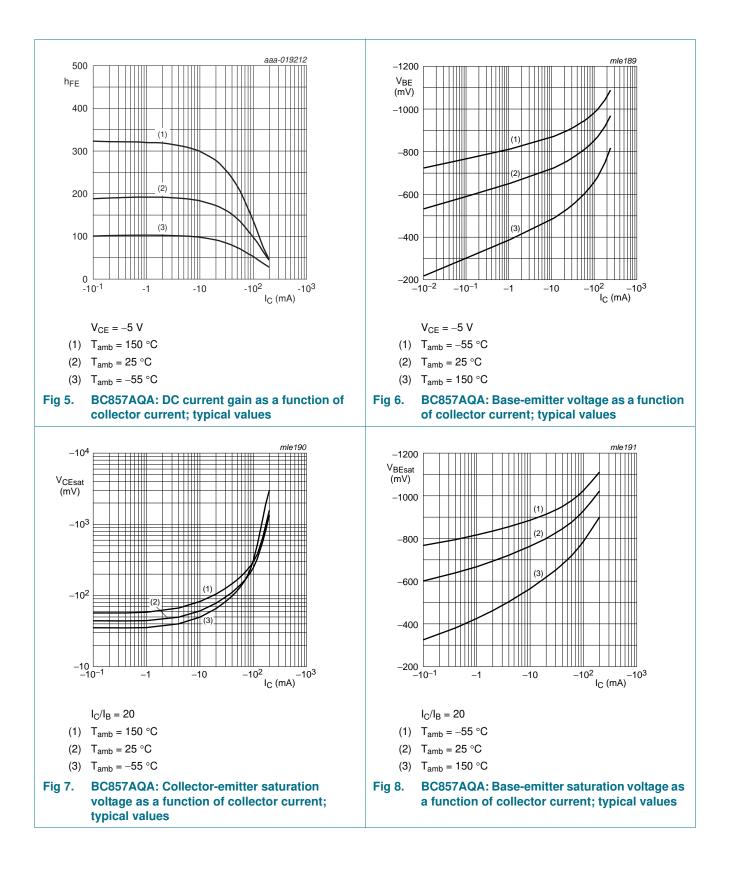
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	$V_{CB} = -30 \text{ V}; \text{ I}_{E} = 0 \text{ A}$	-	-	-15	nA
	current	$V_{CB} = -30 \text{ V}; I_E = 0 \text{ A};$ T _j = 150 °C	-	-	-5	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0 \text{ A}$	-	-	-100	nA
h _{FE}	DC current gain	$V_{CE} = -5 \text{ V}; I_{C} = -2 \text{ mA}$				
	BC857AQA		125	-	250	
	BC857BQA		220	-	475	
	BC857CQA		420	-	800	
V _{CEsat}	collector-emitter saturation	$I_{C} = -10 \text{ mA}; I_{B} = -0.5 \text{ mA}$	-	-	-200	mV
`	voltage	$I_{\rm C} = -100 \text{ mA}; I_{\rm B} = -5 \text{ mA}$	U -	-	-400	mV
V _{BEsat}	base-emitter saturation voltage	$I_{\rm C} = -10$ mA; $I_{\rm B} = -0.5$ mA	-	-760	-	mV
		$I_{\rm C} = -100 \text{ mA}; I_{\rm B} = -5 \text{ mA}$	<u>u</u> -	-900	-	mV
V _{BE}	base-emitter voltage	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}$	-600	-	-750	mV
		$I_{C} = -10 \text{ mA}; V_{CE} = -5 \text{ V}$	-	-	-820	mV
f _T	transition frequency	$V_{CE} = -5 \text{ V}; I_{C} = -10 \text{ mA};$ f = 100 MHz	100	-	-	MHz
C _c	collector capacitance	$\label{eq:VCB} \begin{array}{l} V_{CB} = -10 \ \text{V}; \ \text{I}_{\text{E}} = \text{i}_{\text{e}} = 0 \ \text{A}; \\ \text{f} = 1 \ \text{MHz} \end{array}$	-	-	2.5	pF
C _e	emitter capacitance	$\label{eq:Veb} \begin{array}{l} V_{EB}=-0.5 \ V; \ I_C=i_c=0 \ A; \\ f=1 \ MHz \end{array}$	-	10	-	pF
NF	noise figure	$ I_{C} = -200 \ \mu \text{A}; \ \text{V}_{CE} = -5 \ \text{V}; \\ \text{R}_{S} = 2 \ \text{k}\Omega; \ \text{f} = 1 \ \text{kHz}; \ \text{B} = 200 \ \text{Hz} $	-	-	10	dB

[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta = 0.02$

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BC857XQA series

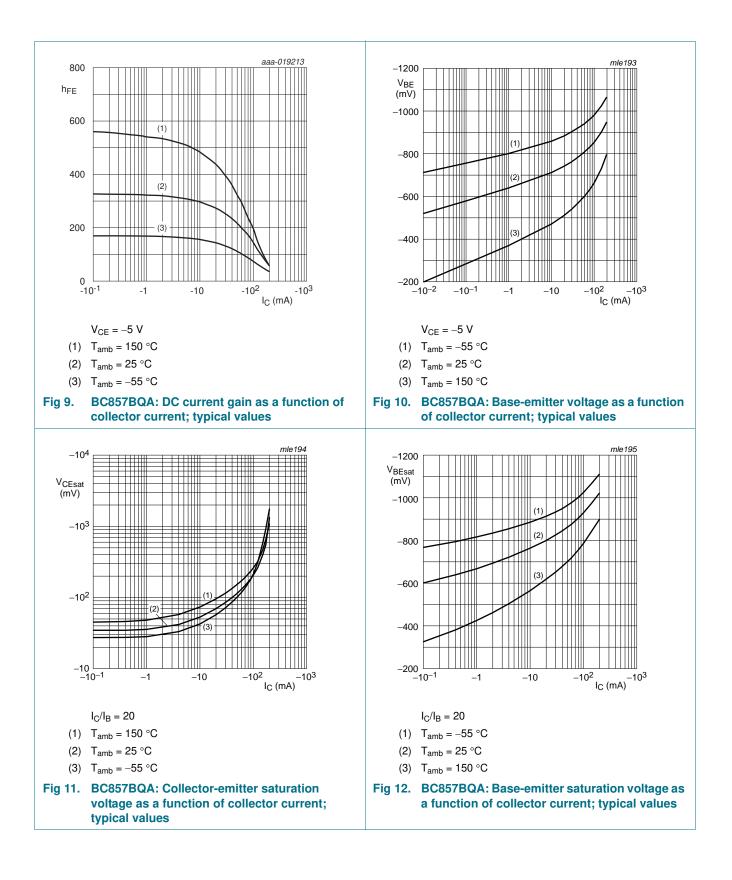
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BC857XQA series

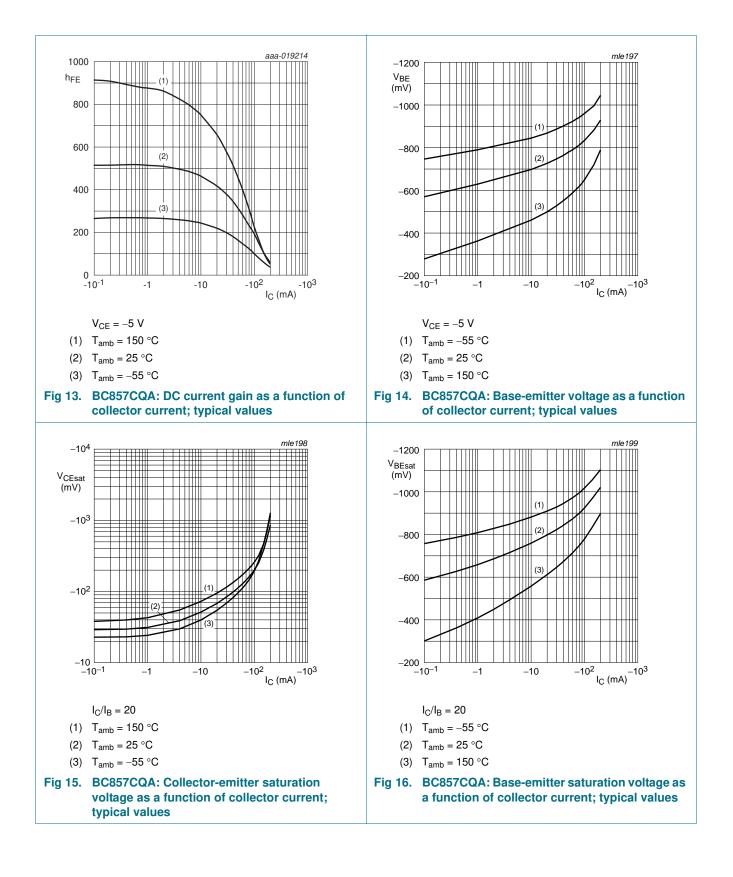
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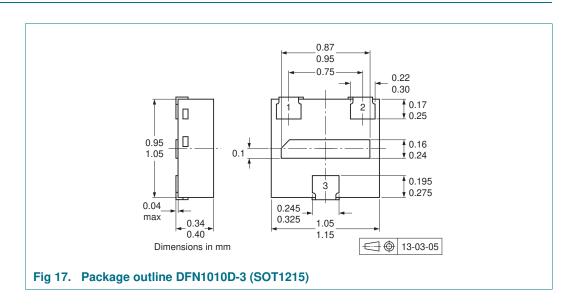
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8. Test information

8.1 Quality information

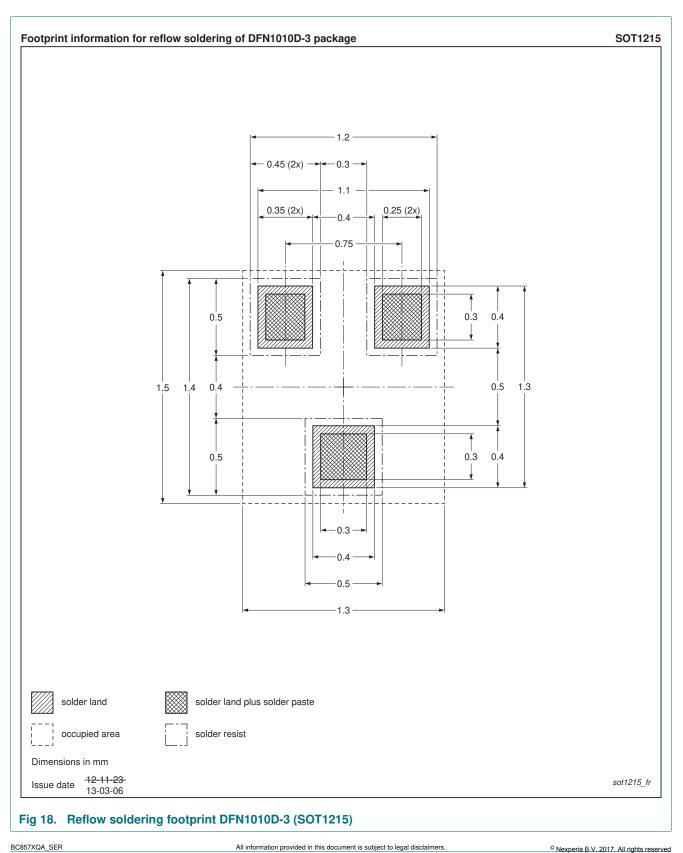
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline



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10. Soldering



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11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BC857XQA_SER v.1	20150826	Product data sheet	-	-

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12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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