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- Package Options include Plastic "Small Outline" Packages, Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs
- Dependable Texas Instruments Quality and Reliability

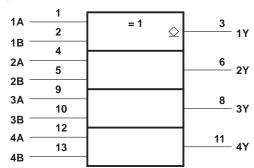
description

These devices contain four independent Exclusive-OR gates with open-collector outputs. They perform the Boolean functions $Y = A \oplus B = \overline{AB} + A\overline{B}$ in positive logic.

A common application is a true/complement element. If one of the inputs is low, the other input will be reproduced in true form at the output. If one of the inputs is high, the signal on the other input will be reproduced inverted at the output.

The SN54ALS136 and SN54AS136 are characterized for operation over the full military temperature range of -55°C to 125°C. The SN74ALS136 and SN74AS136 are characterized for operation from 0°C to 70°C.

logic symbol[†]



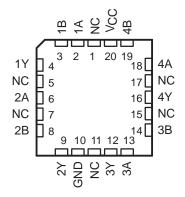
[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, and N packages.

SN54ALS136, SN54AS136...J PACKAGE SN74ALS136, SN74AS136...D OR N PACKAGE

1A 1 14 V _{CC} 1B 2 13 4B 1Y 3 12 4A 2A 4 11 4Y 2B 5 10 3B 2Y 6 9 3A GND 7 8 3Y	(TOP VIEW)									
	1Y [2A [2B [2Y [3 4 5 6	12 11 10	4A 4Y 3B						

SN54ALS136, SN54AS136 ... FK PACKAGE (TOP VIEW)



NC - No internal connection

FUNCTION TABLE
(each gate)

INP	UTS	OUTPUT
Α	В	Y
L	L	L
L	Н	н
н	L	н
н	Н	L

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



SN54ALS136, SN54AS136, SN74ALS136, SN74AS136 QUADRUPLE 2 INPUT EXCLUSIVE OR GATES WITH OPEN COLLECTOR OUTPUTS

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exclusive-OR logic

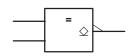
An Exclusive-OR gate has many applications, some of which can be represented better by alternative logic symbols.

EXCLUSIVE-OR



These are five equivalent Exclusive-OR symbols valid for an 'ALS136 gate in positive logic; negation may be shown at any two ports.

LOGIC IDENTITY ELEMENT

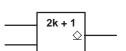


The output is active (low) if all inputs stand at the same logic level (i.e., A=B).





The output is active (low) if an even number of inputs (i.e., 0 or 2) are active.



ODD-PARITY ELEMENT

The output is active (high if an odd number of inputs (i.e., only 1 of the 2) are active.



SN54ALS136, SN74ALS136 QUADRUPLE 2 INPUT EXCLUSIVE OR GATES WITH OPEN COLLECTOR OUTPUTS

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)								
Supply voltage, V _{CC}								
Input voltage								
Operating free-air temperature range: SN54	ALS136							
SN74	ALS136 0°C to 70°C							
Storage temperature range	–65°C to 150°C							

recommended operating conditions

		SN54ALS136		SN74ALS136			UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply Voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input Voltage	2			2			V
\vee_{IL}	Low-level input voltage			0.7			0.8	V
VOH	High-level output current			5.5			5.5	V
IOL	Low-level output current			4			8	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS		SN	SN54ALS136			SN74ALS136		
PARAMETER		TEST CONDITIONS	MIN	TYP [†]	MAX	MIN	TYP†	MAX	UNIT
VIK	$V_{CC} = 4.5 V,$	lj = -18 mA			-1.5			-1.5	V
ЮН	$V_{CC} = 4.5 V,$	V _{OH} = 5.5 V			0.1			0.1	mA
V _{OL}	$V_{CC} = 4.5 V,$	I _{OL} = 4 mA		0.25	0.4		0.25	0.4	V
VOL	$V_{CC} = 4.5 V,$	I _{OL} = 8 mA					0.35	0.5	v
Ц	$V_{CC} = 5.5 V$,	$V_{I} = 7 V$			0.1			0.1	mA
Чн	$V_{CC} = 5.5 V,$	V _I = 2.7 V			20			20	μΑ
ΙL	$V_{CC} = 5.5 V,$	$V_{I} = 0.4 V$			-0.1			-0.1	mA
ICC	V _{CC} = 5.5 V	All inputs at 4.5 V		3.9	5.9		3.9	5.9	mA

[†] All typical values are at V_{CC} = 5, T_A = 25°C.

switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	C F	CL = 50 CL = 2 ks			UNIT
			SN54A	LS136	SN74A	LS136	
			MIN	MAX	MIN	MAX	
^t PLH	A or B	×	20	55	20	50	n 0
^t PHL	(other input low)	I	3	18	3	15	ns
^t PLH	A or B	Y	20	55	20	50	20
^t PHL	(other input low)		3	18	3	15	ns

NOTE 1: Load circuit and voltage waveforms are shown in Section 1 of the ALS/AS Logic Data Book, 1986.



SN54AS136, SN74AS136 QUADRUPLE 2 INPUT EXCLUSIVE OR GATES WITH OPEN COLLECTOR OUTPUTS

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)								
Supply voltage, V _{CC}								
Input voltage								
Off-state output voltage	7 V							
	SN54AS136							
	SN74AS136 0°C to 70°C							
Storage temperature range								

recommended operating conditions

		SN54AS136		SN74ALS136			UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply Voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input Voltage	2			2			V
\vee_{IL}	Low-level input voltage			0.8			0.8	V
VOH	High-level output current			5.5			5.5	V
IOL	Low-level output current			20			20	mA
TA	Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

		S	N54AS13	6	SN	174AS13	6	UNIT		
PARAMETER		TEST CONDITIONS		MIN	TYP [†]	MAX	MIN	TYP†	MAX	UNIT
VIK	$V_{CC} = 4.5 V,$	lj = -18 mA				-1.5			-1.5	V
ЮН	$V_{CC} = 4.5 V,$	V _{OH} = 5.5 V				2			0.1	mA
V _{OL}	$V_{CC} = 4.5 V,$	$I_{OL} = 4 \text{ mA}$			0.35	0.5		0.25	0.4	V
Ц	$V_{CC} = 5.5 V,$	V _I = 7 V				0.1			0.1	mA
Чн	$V_{CC} = 5.5 V,$	V _I = 2.7 V				20			20	μΑ
۱ _{۱L}	$V_{CC} = 5.5 V,$	$V_{ } = 0.4 V$				-0.5			-0.5	mA
ICC	$V_{CC} = 5.5 V$	V _{IA} = 4.5 V,	$V_{IB} = 0$		14	20		14	20	mA
ICCL	$V_{CC} = 5.5 V$	VIA = 4.5 V			22	31		22	31	mA

[†] All typical values are at V_{CC} = 5, T_A = 25°C.

switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	C R	C = 50 C = 2 k			UNIT
			SN54A	S136	SN74A	LS136	
			MIN	MAX	MIN	MAX	
^t PLH	A or B	×	6.3	14.8	6.3	12.5	
^t PHL	(other input low)	Ι	3.3	7.9	3.3	7.1	ns
^t PLH	A or B	V	6.3	12.4	6.3	11.4	
^t PHL	(other input low)	Ŷ	3.3	14.1	3.3	10.7	ns

NOTE 1: Load circuit and voltage waveforms are shown in Section 1 of the ALS/AS Logic Data Book, 1986.



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