

Features

- · Advanced Trench Cell Design
- High Speed Switch
- · Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- · Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

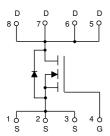
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 1.78°C/W Junction to Case(Note 2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Volltage	V _{GS}	±20	V
Continuous Drain Current ^(Note 2)	I _D	40	Α
Pulsed Drain Current(Note 2)	I _{DM}	160	Α
Total Power Dissipation ^(Note 2)	P _D	70	W

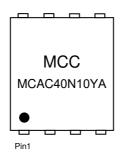
Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. Surface Mounted on 1 in² pad area, $t \le 10$ sec

Internal Structure

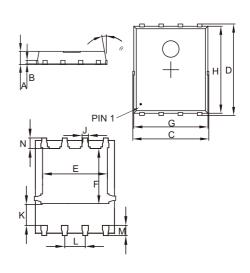


Device Marking



N-CHANNEL MOSFET

DFN5060



DIMENSIONS					
DIM	INC	HES	MM		NOTE
DIIVI	MIN	MAX	MIN	MAX	NOIL
Α	0.031	0.047	0.80	1.20	
В	0.010		0.010 0.254		TYP.
С	0.193	0.222	4.90	5.64	
D	0.232	0.250	5.90	6.35	
Е	0.148	0.167	3.75	4.25	
F	0.126	0.154	3.20	3.92	
G	0.189	0.213	4.80	5.40	
Н	0.222	0.239	5.65	6.06	
K	0.045	0.059	1.15	1.50	
J	0.012	0.020	0.30	0.50	
L	0.046	0.054	1.17	1.37	
M	0.012	0.028	0.30	0.71	
N	0.016	0.028	0.40	0.71	



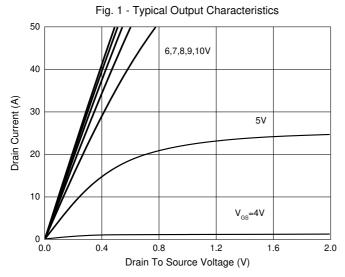
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

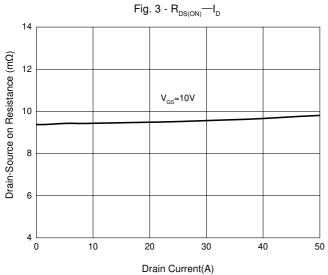
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static Characteristics	1			1	I	I
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	100			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =80V, V _{GS} =0V			1	μA
Gate-Threshold Voltage ^(Note 3)	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2		4	V
Drain-Source On-Resistance ^(Note 3)	R _{DS(on)}	V _{GS} =10V, I _D =20A		9.3	12	mΩ
Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V _{SD}	V _{GS} =0V, I _{SD} =20A			1.3	V
Reverse Recovery Time	trr	I _{SD} = 20 A, dI _{SD} / dt = 100 A / μs		62		ns
Reverse Recovery Charge	Q _{rr}	ISD = 20 A,		83		nC
Dynamic Characteristics(Note 4)			•	•		
Input Capacitance	C _{iss}			1684		pF
Output Capacitance	C _{oss}	V_{DS} =50V, V_{GS} =0V,f=1MHz		259		
Reverse Transfer Capacitance	C _{rss}			31		
Total Gate Charge	Qg			30.6		
Gate-Source Charge	Q _{gs}	V_{DS} =50V, V_{GS} =10V, I_{D} =20A		10		nC
Gate-Drain Charge	Q_{gd}			8		
Turn-On Delay Time	t _{d(on)}			10		
Turn-On Rise Time	t _r	$V_{DS} = 50 \text{ V}, V_{GEN} = 10 \text{ V},$ $R_G = 4.5 \Omega, R_L = 2.5 \Omega,$		21.4		ns
Turn-Off Delay Time	t _{d(off)}	I _{DS} = 20 A		21.2		
Turn-Off Fall Time	t _f			7.8		

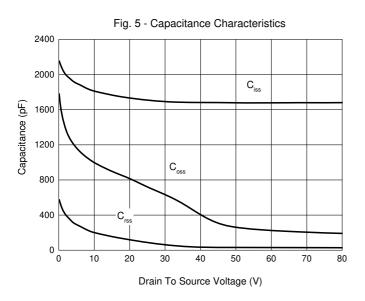
Note 3. Pulse Test : Pulse Width≤300µs, Duty Cycle ≤2%.

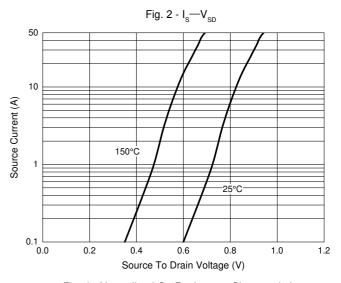
^{4.} Guaranteed by Design, Not Subject to Production Testing.

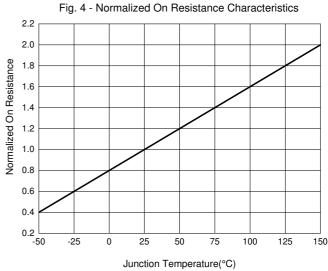


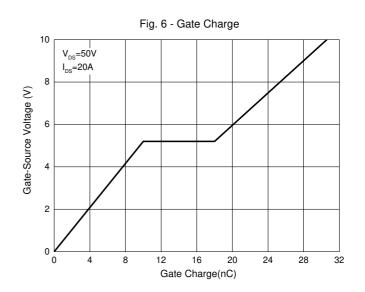




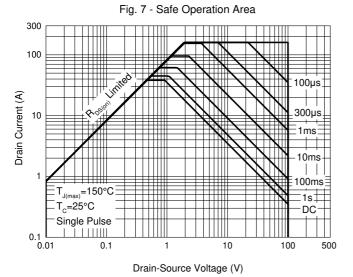














Ordering Information

Device	Packing	
Part Number-TP	Tape&Reel: 5Kpcs/Reel	

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Rev.3-4-12012020 5/5 MCCSEMI.COM