



25V PNP SMALL SIGNAL TRANSISTOR IN SOT23

Features

- **Epitaxial Planar Die Construction**
- Ideal for Medium Power Amplification and Switching
- Complementary NPN Type: MMBT4124
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23 •
- Case Material: Molded plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.008 grams (Approximate)



C C B E Device Symbol Top View Pin-Out

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
MMBT4126-7-F	Standard	K2B	7	8	3,000

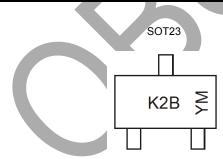
Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



K2B = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: D = 2016) M or \overline{M} = Month (ex: 9 = September)

Date Code Key

Year	2016	20	017	2018	2	019	2020		2021	2022		2023
Code	D		E	F		G	Н		I	J		K
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Absolute Maximum Ratings (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-25	V
Collector-Emitter Voltage	V _{CEO}	-25	V
Emitter-Base Voltage	V _{EBO}	-4.0	V
Collector Current	Ic	-200	mA

Thermal Characteristics (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	200	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	R _{0JA}	625	°C/W
Operating and Storage Temperature Range	TJ, T _{STG}	-55 to +150	0°

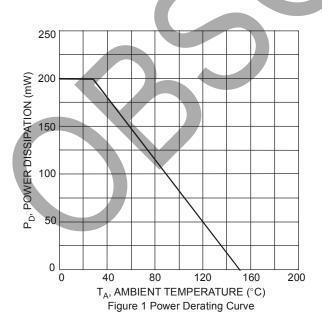
ESD Ratings (Note 6)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes: 5. For the device mounted on minimum recommended pad layout FR-4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

6. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

Thermal Characteristics and Derating Information





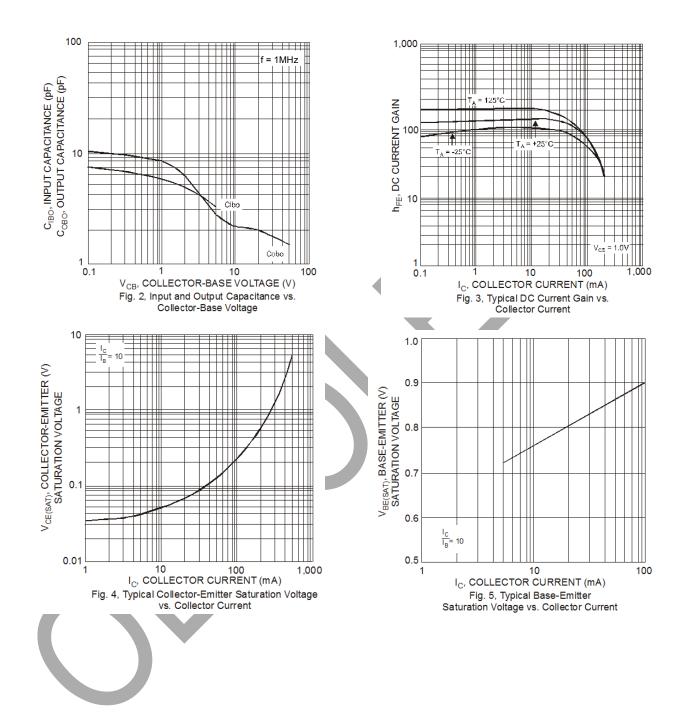
Electrical Characteristics (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS	-					•
Collector-Base Breakdown Voltage	BV CBO	-25	_		V	I _C = -10μA, I _B = 0
Collector-Emitter Breakdown Voltage (Note 7)	BV _{CEO}	-25	—	_	V	I _C = -10mA, I _B = 0
Emitter-Base Breakdown Voltage	BV _{EBO}	-4.0	—	_	V	I _E = -10μA, I _C = 0
Collector Cut-Off Current	I _{CBO}	_	_	-50	nA	$V_{CB} = -20V, I_E = 0$
Collector Cut-Off Current	I _{EBO}	_	—	-50	nA	$V_{EB} = -3.0V, I_{C} = 0$
ON CHARACTERISTICS (Note 7)						
DC Current Gain	h _{FE}	120	—	360		I _C = -2.0mA, V _{CE} = -1.0V
	IIFE	60	—	—		I _C = -50mA, V _{CE} = -1.0V
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	_	—	-0.4	V	$I_{C} = -50 \text{mA}, I_{B} = -5.0 \text{mA}$
Base-Emitter Saturation Voltage	V _{BE(SAT)}		—	-0.95	V	I _C = -50mA, I _B = 5.0mA
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C _{OBO}	_	—	4.5	pF	V_{CB} = -5.0V, f = 1.0MHz, I _E = 0
Input Capacitance	CIBO	_	—	10	pF	V_{EB} = -0.5V, f = 1.0MHz, I _C = 0
Small Signal Current Gain	h _{FE}	120	_	480	-	V _{CE} = -1.0V, I _C = -2.0mA, f = 1.0kHz
Current Gain Bandwidth Product	f⊤	250	-	_	MHz	V _{CE} = -20V, I _C = -10mA, f = 100MHz
Noise Figure	NF	_	-	4.0	dB	V _{CE} = -5.0V, I _C = -100μA, R _S = 1.0kΩ, f = 1.0kHz

Note: 7. Short duration pulse test used to minimize self-heating effect.



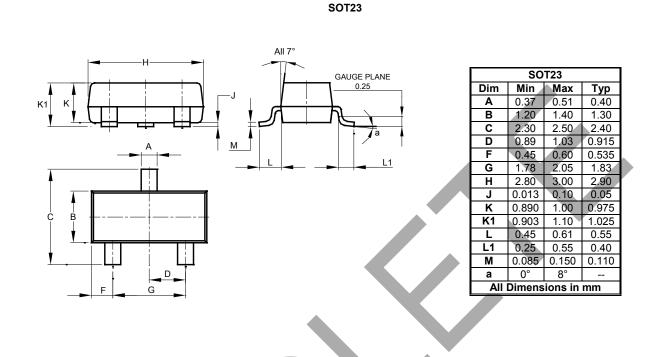
Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)





Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



IMPORTANT NOTICE

1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.

3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.

4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.

5 products provided to Diodes' Standard Terms and Conditions of Sale Diodes are subject (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.

6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.

7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.

8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

Copyright © 2021 Diodes Incorporated

www.diodes.com