

Micro Commercial Components



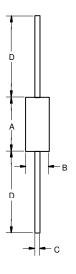
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# DB3/DC34 AND DB4

## SILICON BIDIRECTIONAL DIAC

## **DO-35G**



DIMENSIONS								
	INCHES		ММ					
DIM	MIN	MAX	MIN	MAX	NOTE			
Α		.150		3.8				
В		.079		2.00				
C		.020		.52				
D	1.083		27.50					

## **Features**

- The three layer, two terminal, axial lead, hermetically sealed diacs are designed specifically for triggering thyristors.
- Lead Free Finish/Rohs Compliant (Note1) ("P"Suffix designates Compliant. See ordering information)
- Moisture Sensitivity: Level 1
- These diacs are intended for use in thyrisitors phase control, circuits for lamp dimming, universal motor speed control, and heat control. Type number is marked.

## Maximum Patings

- Operating Temperature: -40°C to +125°C
- Storage Temperature: -40°C to +125°C
- Thermal Resistance Junction to Lead:167°C/W
- Thermal Resistance Junction to Ambient: 400°C/W

Bectrical Characteristics @25°C Unless Otherwise Specified

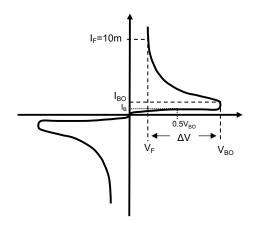
Power dissipation on Printed Circuit(I=10mm)	P <sub>C</sub>	150mW	T <sub>A</sub> =65°C
Repetitive Peak			
on-state Current DB3,DC34,DB4	I <sub>TRM</sub>	2.0A	tp=10us, f=100HZ
Breakover Voltage DB3 DC34 DB4	V <sub>BO</sub>	Min Typ Max 28 32 36V 30 34 38V 35 40 45V	C=22nF(Note 3)
Dynamic Breakover Voltage(Note 2)	Δ۷	5V(Min.)	VBO and VF at10mA
Breakover Voltage Symmetry DB3, DC34, DB4	+V <sub>BO</sub>   - -V <sub>BO</sub>	±3V	C=22nF(Note 3)
Output Voltage(Note 2)	V <sub>o(min)</sub>	5V	
Breakover Current(Note 2)	I <sub>BO(max)</sub>	100µA	C=22nF
Rise Time(Note 2)	T <sub>r</sub>	1.5us	
Leakage Current(Note 2)	I <sub>B(max)</sub>	10µA	$V_B=0.5V_{BO(max)}$

Note: 1. Lead in Glass Exemption Applied, see EU Directive Annex 7(C)-I.

- 2. Electrical characteristics applicable in both forward and reverse directions.
- 3. Connected in parallel with the devices.



### **Typical Performance Characteristics**



 $\begin{array}{lll} \textbf{V}_{BO} & : \text{Break-Over Voltage} \\ \textbf{I}_{BO} & : \text{Break-Over Current} \\ \textbf{\Delta V} & : \text{Dynamic Breakover Voltage} \\ \textbf{I}_{B} & : \text{Leakage Current at V}_{B} = 0.5^* \text{V}_{BO} \\ \textbf{V}_{F} & : \text{Voltage at Current I}_{F} = 10 \text{mA} \end{array}$ 

Diagram 1 : Test circuit

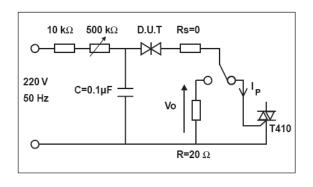


Figure 1. Admissible Power Dissipation Curve

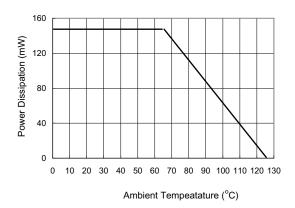


Figure 2. Relative Variation of VBO versus Junction Temperature

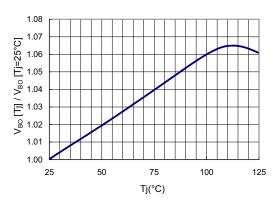
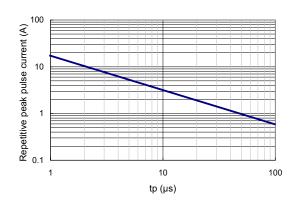


Figure 3. Repetitive Peak Pulse Current versus Pulse Duration (maximum values)





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#### Ordering Information:

Device	Packing	
Part Number-TP	Tape&Reel: 5Kpcs/Reel	
Part Number-AP	Ammo Packing: 5Kpcs/Ammo Box	
Part Number-BP	Bulk: 100 Kpcs/Carton	

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