



Micro Commercial Components 20736 Marilla Street Chatsworth CA 91311

Phone: (818) 701-4933 Fax: (818) 701-4939 MCQ4822

Features

- Halogen free available upon request by adding suffix "-HF"
- High power and current handing capability
- Lead free product is acquired
- Surface mount package
- Moisture Sensitivity Level 1
- Epoxy meets UL 94 V-0 flammability rating

N-Channel Enhancement Mode Field Effect Transistor

Mechanical Data

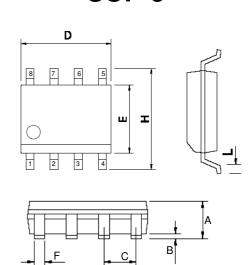
- Case: SOP-8, Molded Plastic
- Terminals: Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking: Q4822

Maximum Ratings

- Operating Temperature: -55°C to +150°C
 Storage Temperature: -55°C to +150°C
- Thermal Resistance; 89°C/W Junction To Ambient

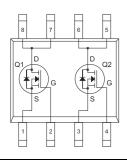
Parameter	Symbol	Value	Unit	
Drain-Source-Voltage	V_{DSS}	30	V	
Gate-Source Voltage	V_{GS}	±20	V	
Continuous Drain Current (t ≤10s) (note 1)	I _D	8.5	Α	
Pulsed Drain Current (note 2)	I _{DM}	30	Α	
Power Dissipation	P_D	1.4	W	

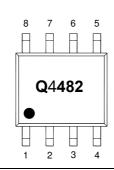
SOP-8



DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.053	.069	1.35	1.75	
В	.004	.010	0.10	0.25	
С	0.050BSC		1.27BSC		
F	.013	.020	0.33	0.51	
D	.189	.197	4.80	5.00	
Е	.150	.157	3.80	4.00	
Η	.228	.244	5.80	6.20	
L	.016	.050	0.40	1.27	

Internal Structure and Marking Code





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MCQ4822

Electrical characteristics (T_a=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	V (BR)DSS	$V_{GS} = 0V, I_{D} = 250 \mu A$	30			V
Zero gate voltage drain current	Idss	Vps =24V,Vgs = 0V			1	μΑ
Gate-body leakage current	Igss	$V_{GS} = \pm 20V$, $V_{DS} = 0V$			±100	nA
Gate threshold voltage	$V_{\text{GS(th)}}$	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	1		3	V
Drain-source on-resistance (note 3)	RDS(on)	Vgs =10V, Ip =8.5A			16	mΩ
		Vgs =4.5V, ID =6A			26	mΩ
Forward tranconductance (note 3)	g _{FS}	V _{DS} =5V, I _D =8.5A		20		S
Diode forward voltage (note 3)	V_{SD}	$I_S=1A$, $V_{GS}=0V$			1	V
DYNAMIC PARAMETERS(Note 4)						
Input capacitance	C_{iss}				1250	pF
Output capacitance	C_{oss}	$V_{DS} = 15V, V_{GS} = 0V, f = 1MHz$		180		pF
Reverse transfer capacitance	C_{rss}			110		pF
SWITCHING PARAMETERS(Note 4)						
Turn-on delay time	td(on)				7.5	ns
Turn-on rise time	tr	V_{GS} =10V, V_{DS} =15V,			6.5	ns
Turn-off delay time	td(off)	$R_L{=}1.8\Omega, R_{GEN}{=}3\Omega$			25	ns
Turn-off fall time	tf				5	ns
Total gate charge (10V)	0	Voc. 15V Voc. 10V Ip. 9.5A			23	nC
Total gate charge (4.5V)	Q_g				11.2	nC
Gate-source charge	Q _{gs}	VDS =15V,VGS =10V,ID =8.5A		2.6		nC
Gate-drain charge	Q_{gd}			4.2		nC

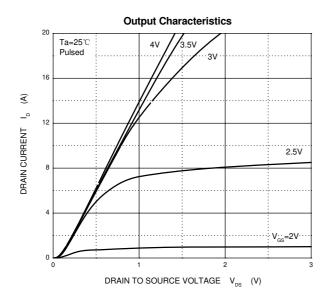
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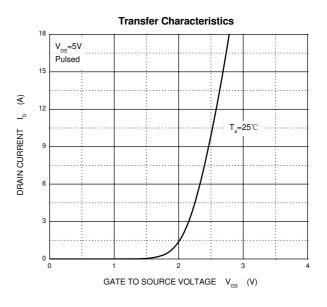
- 1. The value of $R_{\theta JA}$ is measure with the device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with T_a =25°C. The value in any given application depends on the user's specific board design. The current rating is based on the t≤10s thermal resistance rating.
- 2. Repetitive rating : Pulse width limited by junction temperature.
- 3. Pulse Test : Pulse Width≤300 μ s, Duty Cycle≤2%.
- 4. Guaranteed by design, not subject to production testing.

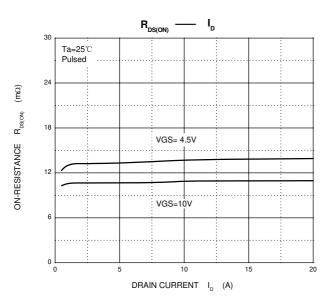
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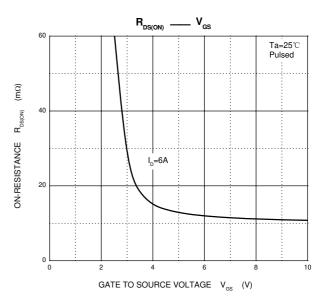
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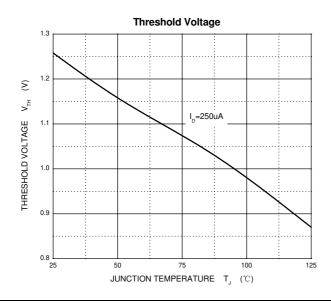


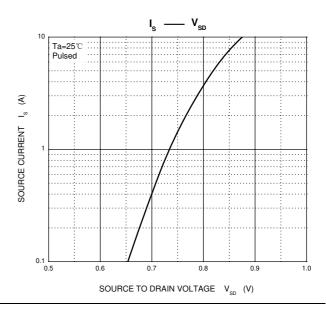














Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel: 4Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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