

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON) MAX}$	I_D $T_A = +25^\circ C$
20V	11mΩ @ $V_{GS} = 4.5V$	10.5A
	13mΩ @ $V_{GS} = 2.5V$	9.4A
	30mΩ @ $V_{GS} = 1.8V$	6.5A
	50mΩ @ $V_{GS} = 1.5V$	5.5A

Description

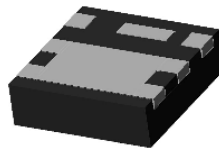
This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high-efficiency, power-management applications.

Applications

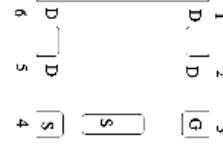
- General Purpose Interfacing Switch
- Power Management Functions



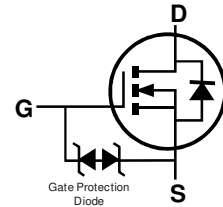
U-DFN2020-6/SWP



Bottom View



Pin Out



Equivalent Circuit

Features

- 0.6mm Profile – Ideal for Low Profile Applications
- Low Gate Threshold Voltage
- ESD Protected Gate
- Additional Tin-Plated on Sidewall Pads for Optical Solder Inspection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- Qualified to AEC-Q101 Standards for High Reliability**
- PPAP Capable (Note 4)**

Mechanical Data

- Case: U-DFN2020-6/SWP
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 Ⓜ3
- Weight: 0.0065 grams (Approximate)

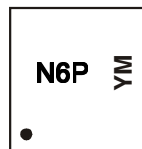
Ordering Information (Notes 4 & 5)

Part Number	Compliance	Case	Quantity per reel
DMW2013UFDEQ-7	Automotive	U-DFN2020-6/SWP	3,000
DMW2013UFDEQ-13	Automotive	U-DFN2020-6/SWP	10,000

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 - See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.
 - For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information

U-DFN2020-6/SWP



N6P / N7P = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: A = 2013)
 M = Month (ex: 9 = September)

Date Code Key

Year	2013	2014	2015	2016	2017	2018	2019
Code	A	B	C	D	E	F	G

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V _{DSS}	20	V
Gate-Source Voltage	V _{GSS}	±8	V
Continuous Drain Current (Note 6) V _{GS} = 4.5V	I _D	T _A = +25°C	10.5
		T _A = +70°C	8.5
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I _{DM}	T _A = +25°C	12.5
		T _A = +70°C	10.0
Maximum Body Diode Continuous Current	I _S	2.5	A
Avalanche Current (Notes 7) L = 0.1mH	I _{AS}	28	A
Single Pulse Avalanche Energy (Notes 8) L = 0.1mH	E _{AS}	39.2	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P _D	T _A = +25°C	0.81
		T _A = +70°C	0.57
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	Steady State	185
		t < 10s	127
Total Power Dissipation (Note 6)	P _D	T _A = +25°C	2.3
		T _A = +70°C	1.6
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	Steady State	65
		t < 10s	45
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	7	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	—	V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	1	µA	V _{DS} = 16V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±5	µA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	0.5	—	1.1	V	V _{DS} = V _{GS} , I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(on)}	—	8.4	11	mΩ	V _{GS} = 4.5V, I _D = 8.5A
			9.8	13		V _{GS} = 2.5V, I _D = 8.5A
			12	30		V _{GS} = 1.8V, I _D = 1A
			15	50		V _{GS} = 1.5V, I _D = 0.5A
Diode Forward Voltage	V _{SD}	—	—	1.2	V	V _{GS} = 0V, I _S = 8.5A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	2,508	—	pF	V _{DS} = 10V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	259	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	242	—	pF	
Gate Resistance	R _g	—	1.2	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	28.7	—	nC	V _{DS} = 10V, I _D = 8.5A
Total Gate Charge (V _{GS} = 8V)	Q _g	—	52.6	—	nC	
Gate-Source Charge	Q _{gs}	—	3.3	—	nC	
Gate-Drain Charge	Q _{gd}	—	5.8	—	nC	
Turn-On Delay Time	t _{D(on)}	—	5.8	—	ns	V _{DS} = 10V, I _D = 8.5A V _{GS} = 4.5V, R _G = 1.8Ω
Turn-On Rise Time	t _r	—	7.8	—	ns	
Turn-Off Delay Time	t _{D(off)}	—	33.3	—	ns	
Turn-Off Fall Time	t _f	—	9.4	—	ns	
Body Diode Reverse Recovery Time	t _{rr}	—	11.6	—	ns	I _F = 8.5A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q _{rr}	—	4.6	—	nC	I _F = 8.5A, di/dt = 100A/µs

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 - I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.

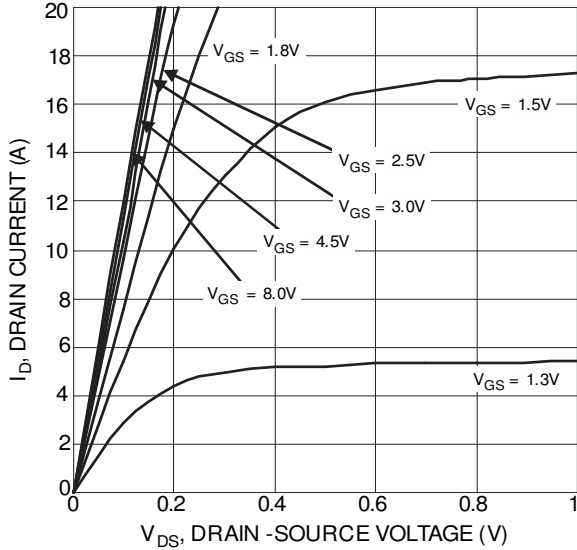


Figure 1 Typical Output Characteristics

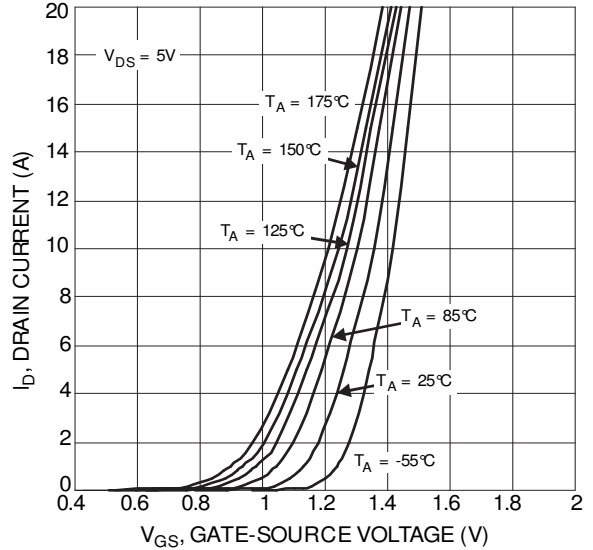


Figure 2 Typical Transfer Characteristics

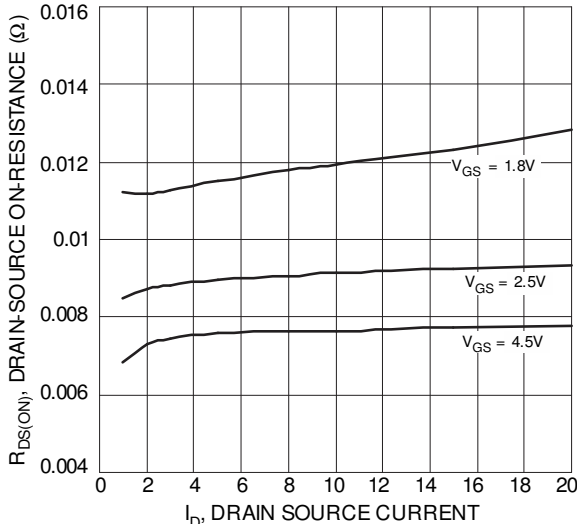


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

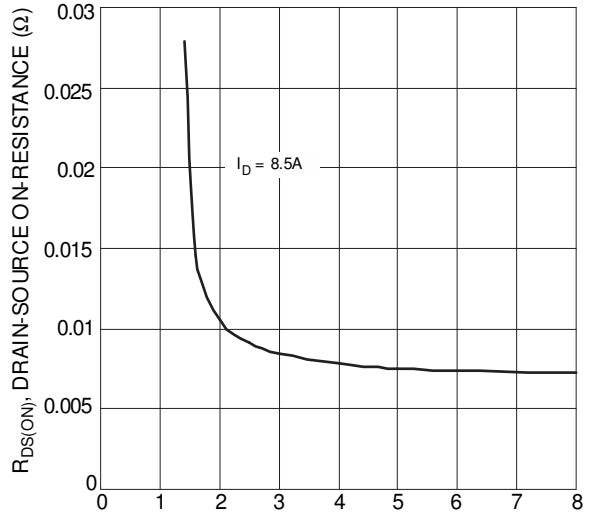


Figure 4 Typical Transfer Characteristic

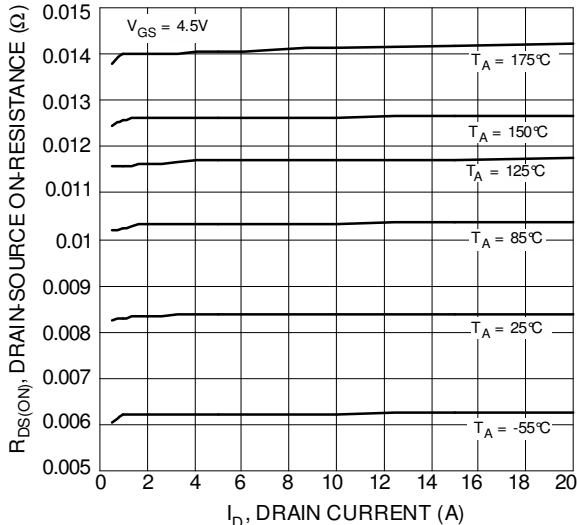


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

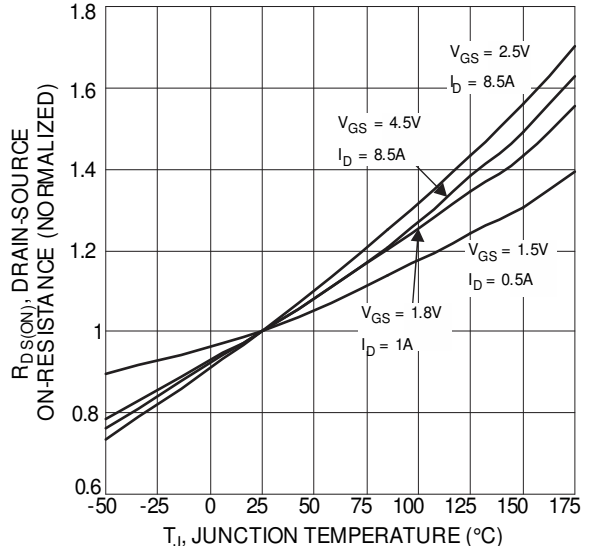


Figure 6 On-Resistance Variation with Temperature

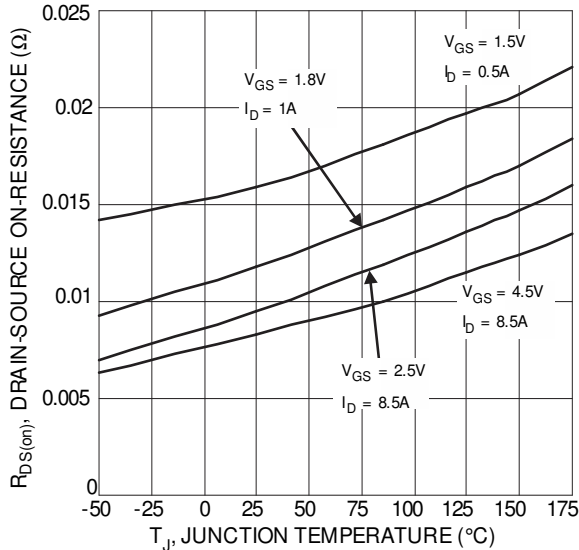


Figure 7 On-Resistance Variation with Temperature

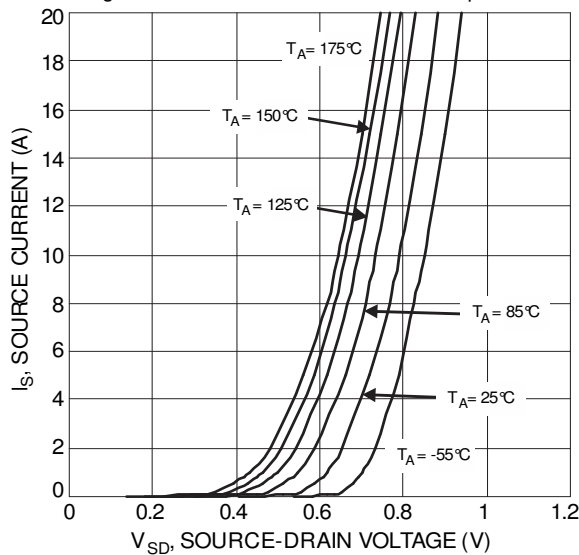


Figure 9 Diode Forward Voltage vs. Current

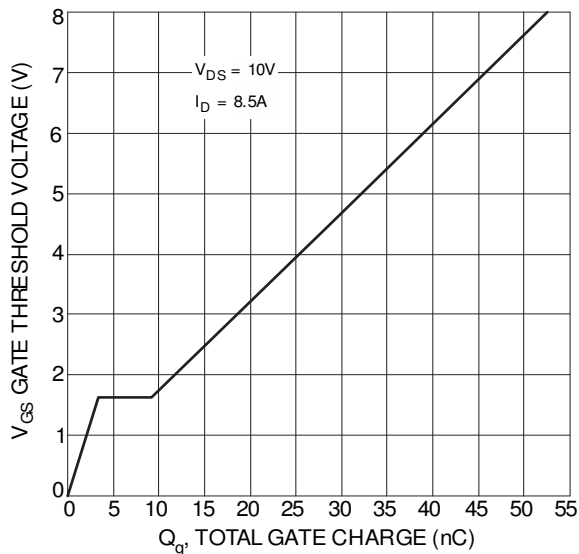


Figure 11 Gate Charge

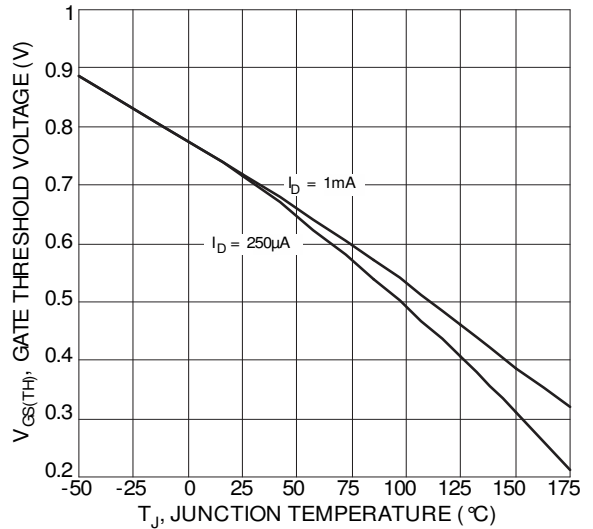


Figure 8 Gate Threshold Variation vs. Ambient Temperature

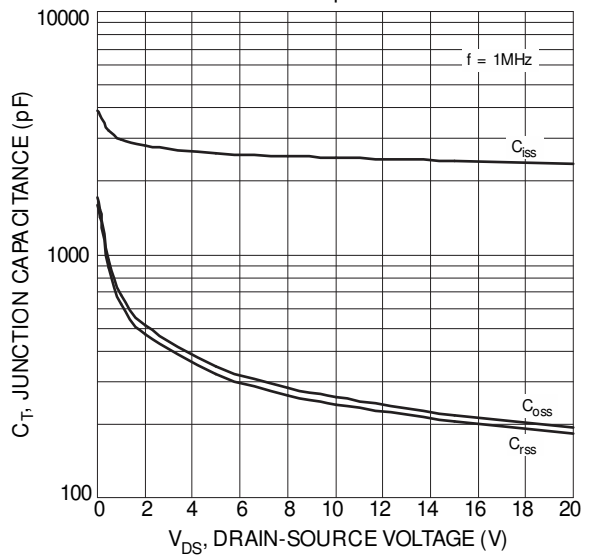


Figure 10 Typical Junction Capacitance

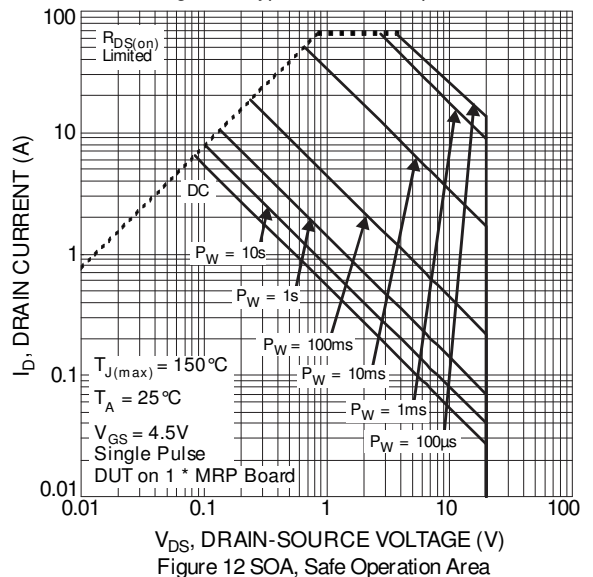


Figure 12 SOA, Safe Operation Area

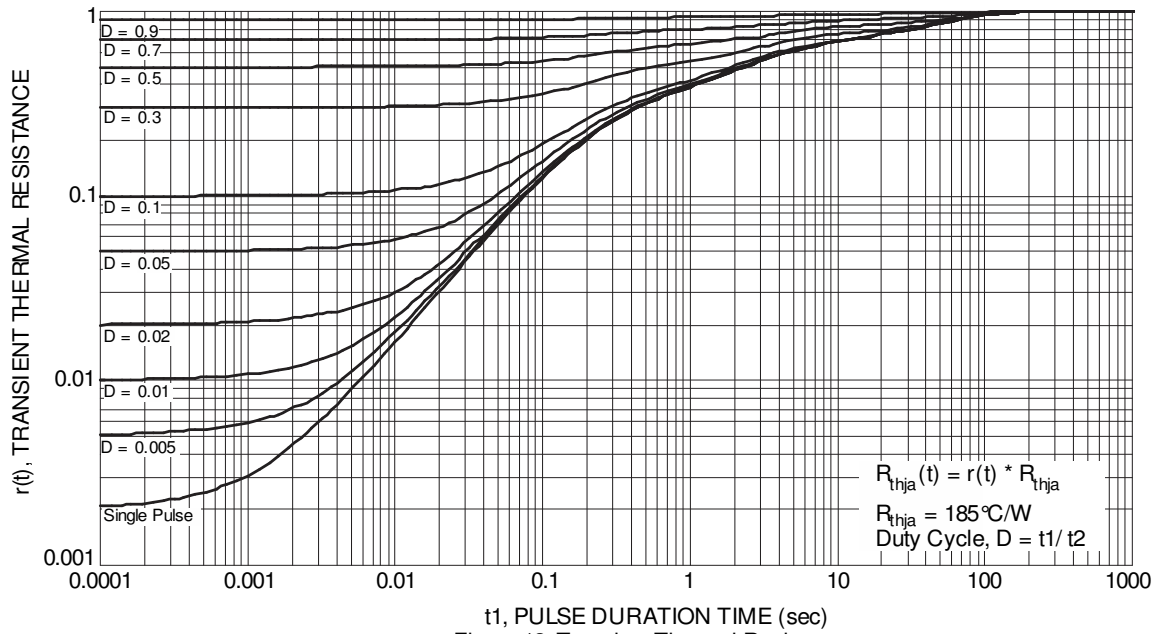
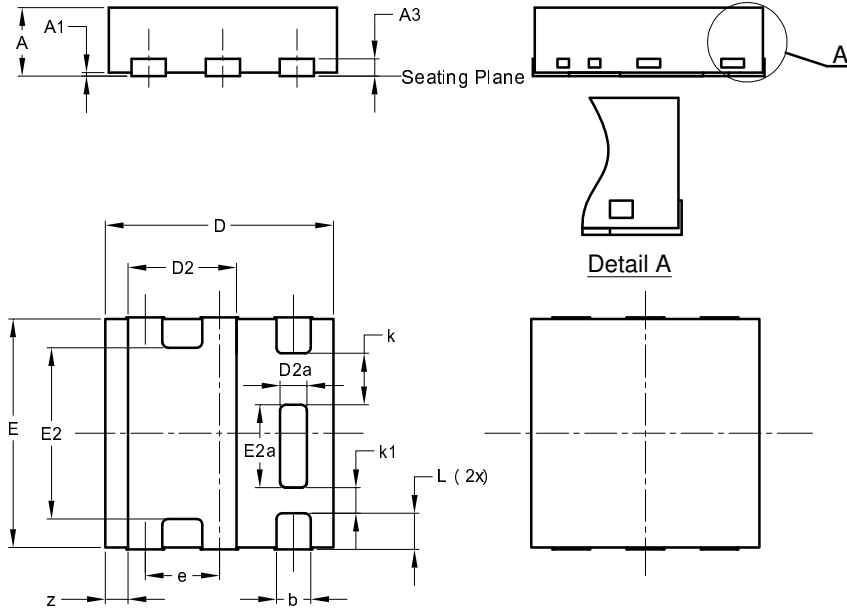


Figure 13 Transient Thermal Resistance

Package Outline Dimensions

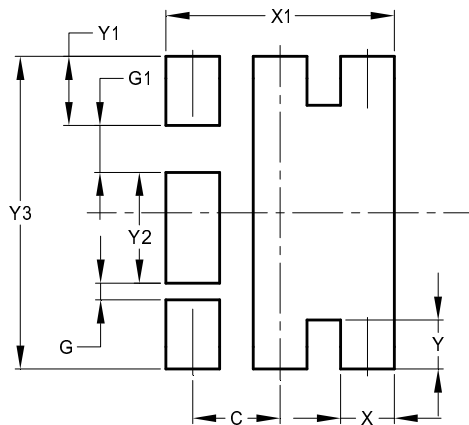
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



U-DFN2020-6/SWP			
Dim	Min	Max	Typ
A	0.59	0.65	0.62
A1	0	0.05	0.03
A3	-	-	0.19
b	0.28	0.38	0.33
D	1.95	2.05	2.00
D2	0.87	1.07	0.97
D2a	0.205	0.305	0.255
E	1.95	2.05	2.00
E2	1.42	1.62	1.52
E2a	0.69	0.79	0.74
e	0.65 BSC		
L	0.28	0.38	0.33
k	0.450 BSC		
k1	0.225 BSC		
Z	-	-	0.20
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	0.650
G	0.125
G1	0.350
X	0.400
X1	1.700
Y	0.365
Y1	0.515
Y2	0.825
Y3	2.330

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