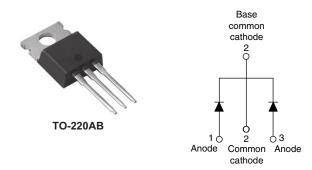


VS-63CTQ100GPbF, VS-63CTQ100G-N3

Vishay Semiconductors

Schottky Rectifier, 2 x 30 A



PRODUCT SUMMARY				
Package	TO-220AB			
I _{F(AV)}	2 x 30 A			
V _R	100 V			
V _F at I _F	0.69 V			
I _{RM} max.	20 mA at 125 °C			
T _J max.	175 °C			
Diode variation	Common cathode			
E _{AS}	11.25 mJ			

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



RoHS

COMPLIANT HALOGEN

FREE

- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform (per device)	60	А		
V _{RRM}		100	V		
I _{FRM}	$T_{\rm C} = 139 \ ^{\circ}{\rm C}$ (per leg)	60	А		
I _{FSM}	t _p = 5 μs sine	1500	A .		
V _F	30 A _{pk} , T _J = 125 °C	0.69	V		
TJ	Range	- 65 to 175	°C		

VOLTAGE RATINGS						
PARAMETER SYMBOL VS-63CTQ100GPbF VS-63CTQ100G-N3 UNIT						
Maximum DC reverse voltage	V _R	100	100	V		
Maximum working peak reverse voltage	V _{RWM}	100	100	v		

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST COND	TEST CONDITIONS		UNITS		
Maximum average	per leg		50 % duty cycle at T _C = 139 °C, rectangular waveform				30	
forward current	per device	IF(AV)	30% duty cycle at $1^\circ_{\rm C} = 139$ C	50% duty cycle at $T_{\rm C} = 159\%$ C, rectangular wavelorm				
Peak repetitive forward current per leg		I _{FRM}	Rated V _R , square wave, 20 kHz, T_C = 140 °C		60	А		
Maximum peak one cycle non-repetitive surge current per leg		1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1500	-		
		IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	300			
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 0.75 A, L = 40 mH		11.25	mJ		
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.75	А		

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS				
		30 A	т ос ос	0.78	0.82	V	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	60 A	T _J = 25 °C	0.94	1.0		
maximum lorward voltage drop	VFM (1)	30 A	T.I = 125 °C	0.64	0.69		
		60 A	$1_{\rm J} = 125$ C	0.78	0.83		
Maximum instantaneous reverse current	I _{RM}	T _J = 25 °C	$V_{\rm B} = \text{Rated } V_{\rm B}$	0.02	0.3	mA	
Maximum instantaneous reverse current		T _J = 125 °C	VR - naleu VR	11	20	IIIA	
Maximum junction capacitance	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		11	00	pF	
Typical series inductance	L _S	Measured from top of terminal to mounting plane		8.0		nH	
Maximum voltage rate of change	dV/dt	Rated V _R	10 000		V/µs		

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	•	T _J , T _{Stg}		- 65 to 175	°C	
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation	1.2	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	0/11	
Approximate weight				2	g	
Approximate weight	Approximate weight			0.07	OZ.	
Mounting torrung			Non-lubricated threads	6 (5)	kgf ⋅ cm	
Mounting torque —	maximum		Non-Inducated inteads	12 (10)	(lbf · in)	
Marking device			Case style TO-220AB	63CTC	100G	



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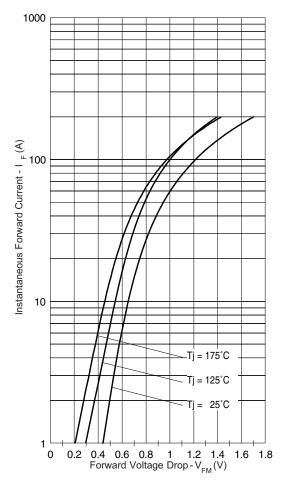


Fig. 1 - Maximum Forward Voltage Drop Characteristics

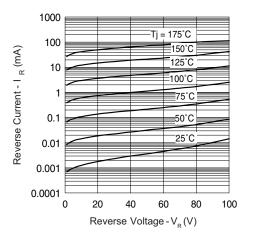


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

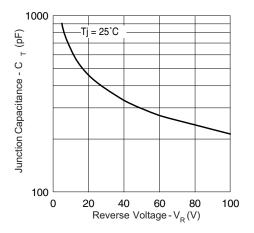
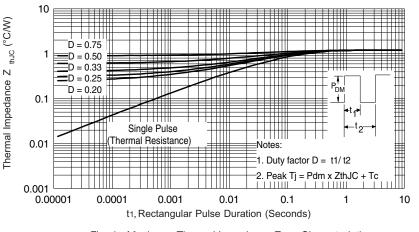
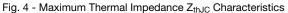
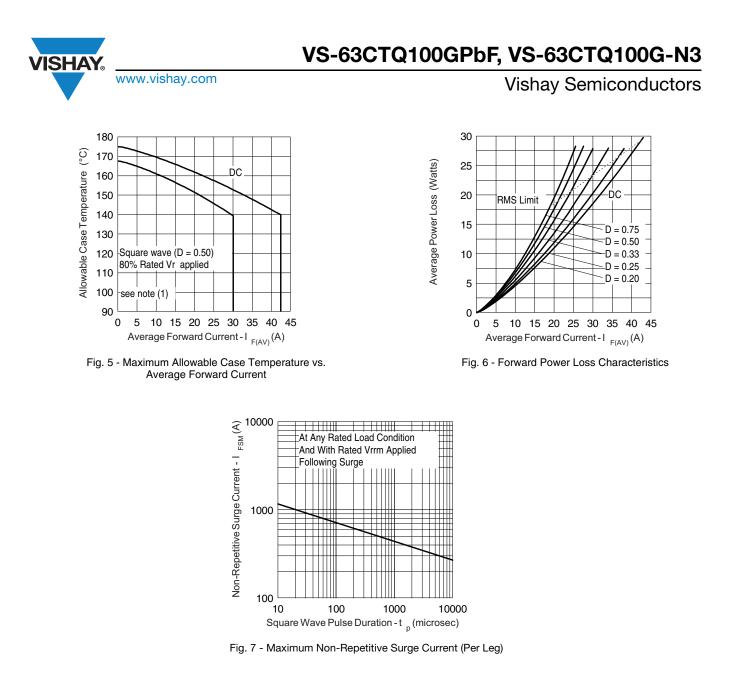


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage





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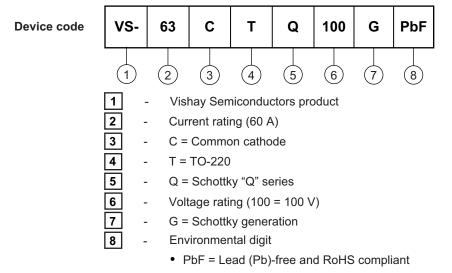
Note



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ORDERING INFORMATION TABLE



• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-63CTQ100GPbF	50	1000	Antistatic plastic tube			
VS-63CTQ100G-N3	50	1000	Antistatic plastic tube			

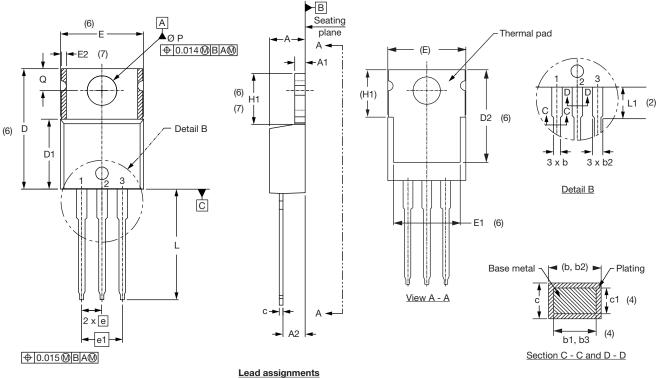
LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95222					
	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			

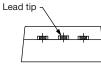


Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches





ead	assignments	

Diodes

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- ⁽³⁾ Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left(4\right) }$ Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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