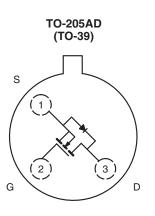
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2N6661, 2N6661-2, 2N6661JANTX, 2N6661JANTXV

Vishay Siliconix

N-Channel 90 V (D-S) MOSFET

PRODUCT SUMMARY	
V _{DS} (V)	90
$R_{DS(on)} (\Omega)$ at $V_{GS} = 10 V$	4
Configuration	Single



Top View

FEATURES

- Military Qualified
- Low On-Resistence: 3.6 Ω
- Low Threshold: 1.6 V
- Low Input Capacitance: 35 pF
- Fast Switching Speed: 6 ns
- Low Input and Output Leakage

BENEFITS

- Guaranteed Reliability
- Low Offset Voltage
- Low-Voltage Operation
- Easily Driven Without Buffer
- High-Speed Circuits
- Low Error Voltage

APPLICATIONS

- Hi-Rel Systems
- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- Battery Operated Systems
- Solid-State Relays

ORDERING INFORMATION				
PART	PACKAGE	DESCRIPTION/DSCC PART NUMBER	VISHAY ORDERING PART NUMBER	
2N6661		Commercial	2N6661	
		Commercial, Lead (Pb)-free	2N6661-E3	
2N6661-2		See -2 Flow Document	2N6661-2	
	TO-205AD	JANTX2N6661 (std Au leads)	2N6661JTX02	
2N6661JANTX (TO-39)	(TO-39)	JANTX2N6661 (with solder)	2N6661JTXL02	
	JANTX2N6661P (with PIND)	2N6661JTXP02		
2N6661JANTXV		JANTXV2N6661 (std Au leads)	2N6661JTXV02	
		JANTXV2N6661P (with PIND)	2N6661JTVP02	

ABSOLUTE MAXIMUM RATINGS	(T _A = 25 °C, unless other	rwise noted)		
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V _{DS}	90	V
Gate-Source Voltage	V _{GS} :		± 20	v
Continuous Drain Current (T 150 °C)	$T_{\rm C} = 25 ^{\circ}{\rm C}$ 0.86	0.86		
Continuous Drain Current (T _J = 150 °C)	T _C = 100 °C	ID	0.54	А
Pulsed Drain Current ^a		I _{DM}	3	
Maximum Dawar Disaination	T _C = 25 °C	D	6.25	10/
Maximum Power Dissipation	T _A = 25 °C	- P _D -	0.725	- W
Thermal Resistance, Junction-to-Ambient ^b		R _{thJA}	170	°C/W
Thermal Resistance, Junction-to-Case		R _{thJC}	20	-0/00
Operating Junction and Storage Temperature R	lange	T _J , T _{stg}	- 55 to 150	°C

Notes

a. Pulse width limited by maximum junction temperature.

b. Not required by military spec.





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SPECIFICATIONS (T _A = 25 °C	C, unless o	otherwise not	ed)					
						LIMITS		
PARAMETER	SYMBOL	TES	T CONDITIC	DNS	MIN.	TYP. ^b	MAX.	UNIT
Static								
Drain-Source Breakdown Voltage	V _{DS}	V _{DS}	= 0 V, I _D = 10) μΑ	90	125	-	
		$V_{DS} = V_{GS}$, $I_D = 1 \text{ mA}$ $T_A = -55 \text{ °C}$		0.8	1.6	2	v	
Gate-Source Threshold Voltage	V _{GS(th)}			T _A = - 55 °C	-	1.8	2.5	v
				T _A = 125 °C	0.3	1.3	-	
Onto Dantu Laghana		$V_{GS} = \pm 20 V$	V _{DS}	s = 0 V	-	-	± 100	
Gate-Body Leakage	I _{GSS}	$v_{GS} = \pm 20 v$		T _A = 125 °C	-	-	± 500	nA
Zava Cata Valtaga Drain Current		V _{GS} = 0 V	V _{DS}	= 72 V	-	-	- 1 μA	
Zero Gate Voltage Drain Current	I _{DSS}	$v_{GS} = 0 v$		T _A = 125 °C	-	-		μΑ
On-State Drain Current ^b	I _{D(on)}	$V_{GS} = 10 V$	V _{DS}	= 10 V	-	1.8	-	mA
		$V_{GS} = 5 V$	I _D =	= 0.3 A	-	3.8	5.3	
Drain-Source On-State Resistance ^b	R _{DS(on)}	$R_{DS(on)}$ $V_{GS} = 10 V$	I _D = 1 A		-	3.6	4	Ω
				$T_A = 125 \ ^\circ C^d$	-	6.7	7.5	
Forward Transconductance ^b	9 _{fs}	V _{DS} =	7.5 V, I _D = 0.	475 A	170	340	-	mS
Diode Forward Voltage	V _{SD}	$V_{GS} = 0 V$	I _S =	0.86 A	0.7	0.9	1.4	V
Dynamic			•			•	•	
Input Capacitance	C _{iss}				-	35	50	
Output Capacitance	C _{oss}	<u> </u>	$V_{DS} = 25 \text{ V}, \text{ f} = 1 \text{ MHz} \qquad \frac{-335}{-15} \\ \frac{-15}{-2} \\ \frac{-30}{-30} \\ -$		40	pF		
Reverse Transfer Capacitance	C _{rss}	$V_{GS} = 0 V$			10			
Drain-Source Capacitance	C _{ds}				-	30	-	1
Switching ^c	•		•			•	*	
Turn-On Time	t _{ON}	V _{DD} =	= 25 V, R _L = 2	23 Ω	-	6	10	
Turn-Off Time	t _{OFF}	I _D ≅ 1 A, V	/ _{GEN} = 10 V, I	R _g = 23 Ω	-	8	10	ns

Notes

a. FOR DESIGN AID ONLY, not subject to production testing.

b. Pulse test: PW \leq 300 μs duty cycle \leq 2 %.

c. Switching time is essentially independent of operating temperature.

d. This parameter not registered with JEDEC.

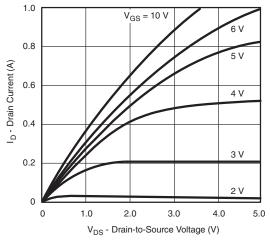
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

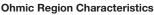
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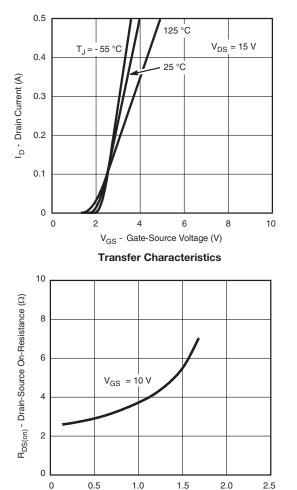
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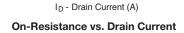
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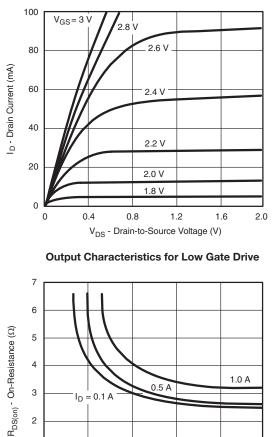
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

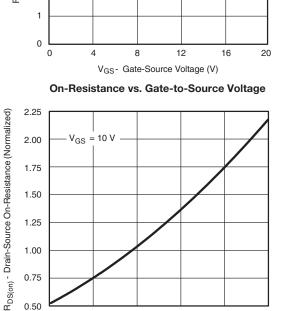






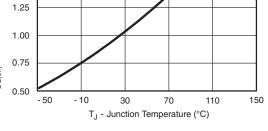






 I_D

2



Normalized On-Resistance vs. Junction Temperature

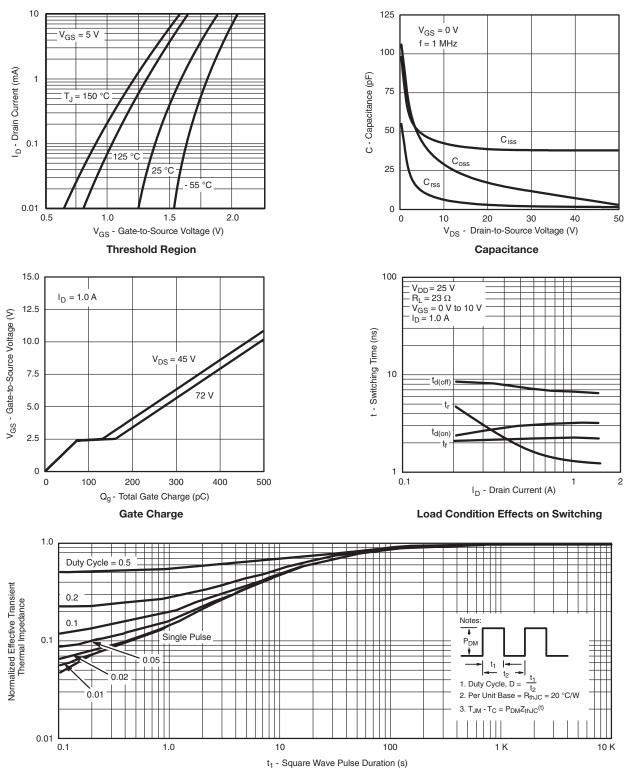
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?70225.

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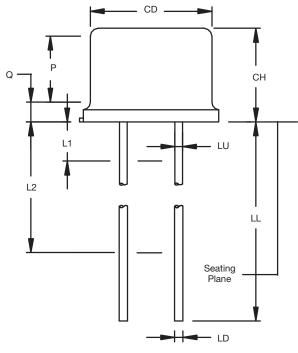
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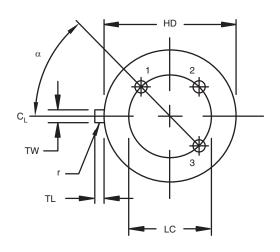
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Vishay Siliconix

TO-205AD (TO-39 TALL LID)





DIM.	INC	HES	MILLIN	IETERS
	MIN.	MAX.	MIN.	MAX.
CD	0.305	0.335	7.75	8.51
СН	0.240	0.260	6.10	6.60
HD	0.335	0.370	8.51	9.40
LC ⁽⁶⁾	0.20	0 TP	5.08 TP	
LD ⁽⁷⁾⁽⁸⁾	0.016	0.021	0.41	0.53
LL (7)(8)	0.500	0.750	12.70	19.05
LU ⁽⁷⁾⁽⁸⁾	0.016	0.019	0.41	0.48
L1 ⁽⁷⁾⁽⁸⁾		0.050		1.27
L2 ⁽⁷⁾⁽⁸⁾	0.250		6.35	_
P ⁽⁵⁾	0.100		2.54	_
Q (4)		0.050		1.27
r ⁽⁹⁾		0.010		0.25
TL ⁽³⁾	0.029	0.045	0.74	1.14
TW ⁽²⁾	0.028	0.034	0.71	0.86
α (6)	45° TP		45° TP	

Notes

⁽¹⁾ Dimensions are in inches. Metric equivalents are given for general information only.

⁽²⁾ Beyond radius (r) maximum, TW shall be held for a minimum length of 0.011" (0.028 mm).

⁽³⁾ Dimension TL measured from maximum HD.

⁽⁴⁾ Outline in this zone is not controlled.

⁽⁵⁾ Dimension CD shall not vary more than 0.010 (0.25 mm) in zone P. This zone is controlled for automatic handling.

(6) Leads at guage plane 0.054ⁱⁱ + 0.001ⁱⁱ, - 0.000ⁱⁱ (1.37 mm + 0.03 mm, - 0.00 mm) below seating plane shall be within 0.007ⁱⁱ (0.18 mm) radius of true position (TP) at maximum material condition (MMC) relative to tab at MMC.

⁽⁷⁾ LU applies between L1 and L2, LD applies between L2 and L maximum.

Diameter is uncontrolled in L1 and beyond LL minimum.

(8) All three leads.

- ⁽⁹⁾ Radius (r) applies to both inside corners of tab.
- ⁽¹⁰⁾ Drain is electrically connected to the case.

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