IGBT

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on-state voltage and minimal switching loss. The IGBT is well suited for resonant or soft switching applications. Incorporated into the device is a rugged co-packaged free wheeling diode with a low forward voltage.

Features

- Low Saturation Voltage using Trench with Fieldstop Technology
- Low Switching Loss Reduces System Power Dissipation
- Optimized for Low Case Temperature in IH Cooker Application
- Low Gate Charge
- These are Pb-Free Devices

Typical Applications

- Inductive Heating
- Consumer Appliances
- Soft Switching

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-emitter voltage	V_{CES}	1200	V
Collector current @ Tc = 25°C @ Tc = 100°C	I _C	50 25	A
Pulsed collector current, T _{pulse} limited by T _{Jmax}	I _{CM}	200	Α
Diode forward current @ Tc = 25°C @ Tc = 100°C	I _F	50 25	Α
Diode pulsed current, T _{pulse} limited by T _{Jmax}	I _{FM}	200	Α
Gate-emitter voltage	V_{GE}	±20	V
Power Dissipation @ Tc = 25°C @ Tc = 100°C	P _D	192 77	W
Operating junction temperature range	TJ	-55 to +150	°C
Storage temperature range	T _{stg}	-55 to +150	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T _{SLD}	260	°C

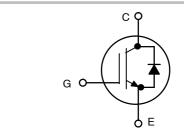
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

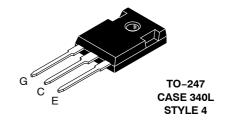


ON Semiconductor®

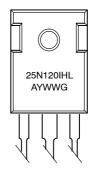
http://onsemi.com

25 A, 1200 V V_{CEsat} = 1.85 V E_{off} = 0.8 mJ





MARKING DIAGRAM



A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
NGTB25N120IHLWG	TO-247 (Pb-Free)	30 Units / Rail

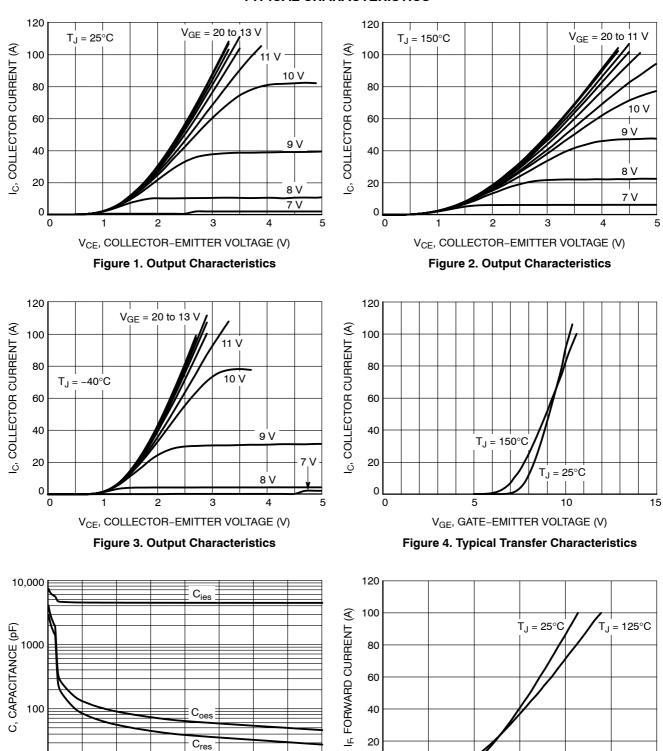
THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ hetaJC}$	0.65	°C/W
Thermal resistance junction-to-case, for Diode	$R_{ hetaJC}$	2.0	°C/W
Thermal resistance junction-to-ambient	$R_{ hetaJA}$	40	°C/W

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC	•	•		•		
Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE} = 0 \text{ V}, I_{C} = 500 \mu\text{A}$	V _{(BR)CES}	1200	_	-	V
Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 25 A V _{GE} = 15 V, I _C = 25 A, T _J = 150°C	V _{CEsat}	-	1.85 2.1	2.3	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_{C} = 250 \mu A$	V _{GE(th)}	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	$V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}$ $V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}, T_{J=} 150^{\circ}\text{C}$	I _{CES}	- -	_ _	0.5 2.0	mA
Gate leakage current, collector-emitter short-circuited	V _{GE} = 20 V, V _{CE} = 0 V	I _{GES}	-	-	100	nA
DYNAMIC CHARACTERISTIC		•				•
Input capacitance		C _{ies}	-	4700	-	pF
Output capacitance	V _{CE} = 20 V, V _{GE} = 0 V, f = 1 MHz	C _{oes}	-	155	-	
Reverse transfer capacitance	1	C _{res}	-	100	-	
Gate charge total		Q_g		200		nC
Gate to emitter charge	V _{CE} = 600 V, I _C = 25 A, V _{GE} = 15 V	Q_{ge}		38		
Gate to collector charge	1	Q _{gc}		100		
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD					
Turn-off delay time	T _J = 25°C	t _{d(off)}		235		ns
Fall time	$V_{CC} = 600 \text{ V}, I_{C} = 25 \text{ A}$ $R_{q} = 10 \Omega$	t _f		160		
Turn-off switching loss	V _{GE} = 0 V/ 15V	E _{off}		0.8		mJ
Turn-off delay time	T _J = 125°C	t _{d(off)}		250		ns
Fall time	$V_{CC} = 600 \text{ V, I}_{C} = 25 \text{ A}$ $R_{g} = 10 \Omega$	t _f		225		
Turn-off switching loss	V _{GE} = 0 V/ 15V	E _{off}		1.9		mJ
DIODE CHARACTERISTIC						
Forward voltage	V _{GE} = 0 V, I _F = 25 A V _{GE} = 0 V, I _F = 25 A, T _J = 150°C	V _F		1.7 1.8	1.8	V

TYPICAL CHARACTERISTICS



V_{CE}, COLLECTOR-EMITTER VOLTAGE (V) Figure 5. Typical Capacitance

100

125

150

50

75

10

 $\label{eq:VF} V_F, \, \text{FORWARD VOLTAGE (V)}$ Figure 6. Diode Forward Characteristics

1.5

2.0

2.5

3.0

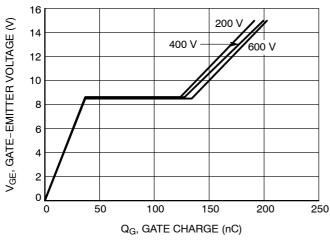
0.5

1.0

0

200

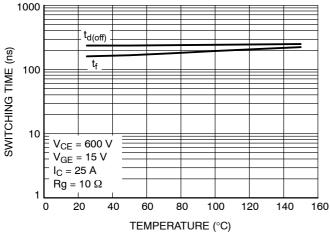
TYPICAL CHARACTERISTICS



(m $V_{CE} = 600 \text{ V}$ 1.8 Eoff, TURN-OFF SWITCHING LOSS $V_{GE} = 15 V$ - I_C = 25 A 1.6 $Rg = 10 \Omega$ 1.4 1.2 1.0 8.0 0.6 0.4 0.2 0 160 0 20 40 80 100 120 140 TEMPERATURE (°C)

Figure 7. Typical Gate Charge

Figure 8. Energy Loss vs. Temperature



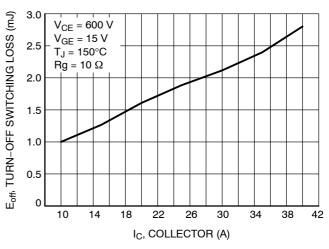
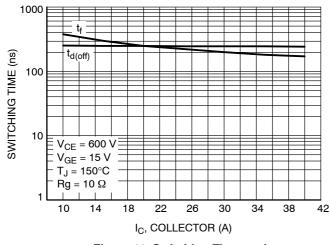


Figure 9. Switching Time vs. Temperature

Figure 10. Energy Loss vs. I_C



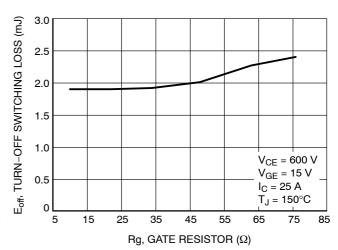
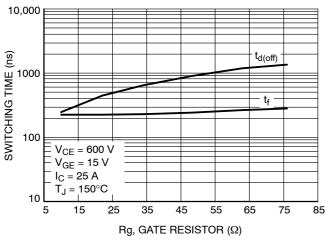


Figure 11. Switching Time vs. I_C

Figure 12. Energy Loss vs. Rg

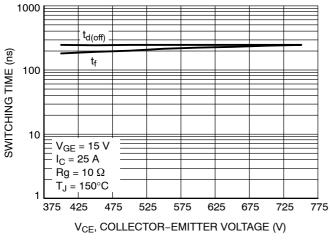
TYPICAL CHARACTERISTICS



2.5 2.0 ENERGY (mJ) 1.5 1.0 V_{GE} = 15 V I_C = 25 A 0.5 $Rg = 10 \Omega$ T_J = 150°C 725 775 375 425 525 575 625 675 V_{CE}, COLLECTOR-EMITTER VOLTAGE (V)

Figure 13. Switching Time vs. Rg

Figure 14. Energy Loss vs. V_{CE}



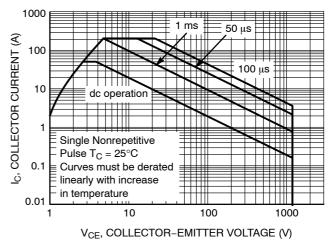


Figure 15. Switching Time vs. V_{CE}

Figure 16. Safe Operating Area

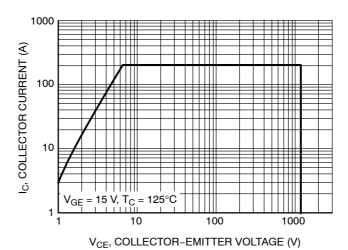


Figure 17. Reverse Bias Safe Operating Area

TYPICAL CHARACTERISTICS

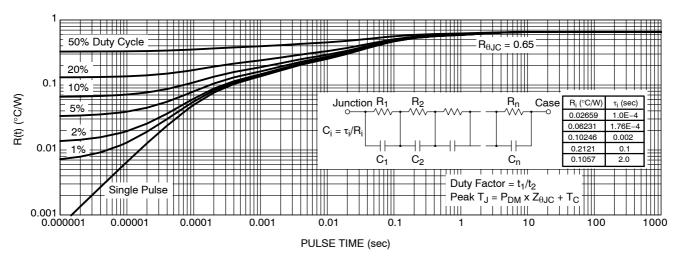


Figure 18. IGBT Transient Thermal Impedance

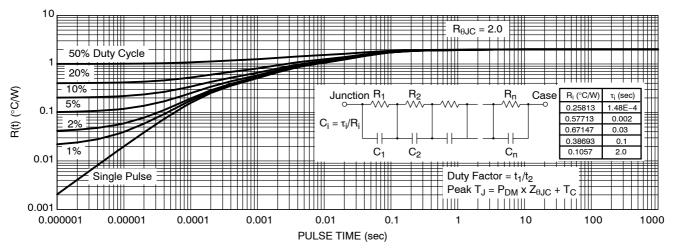


Figure 19. Diode Transient Thermal Impedance

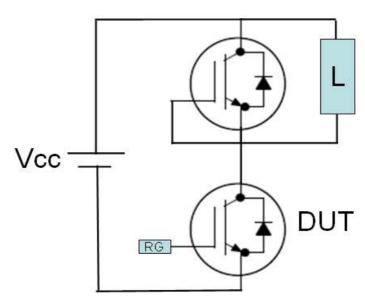


Figure 20. Test Circuit for Switching Characteristics

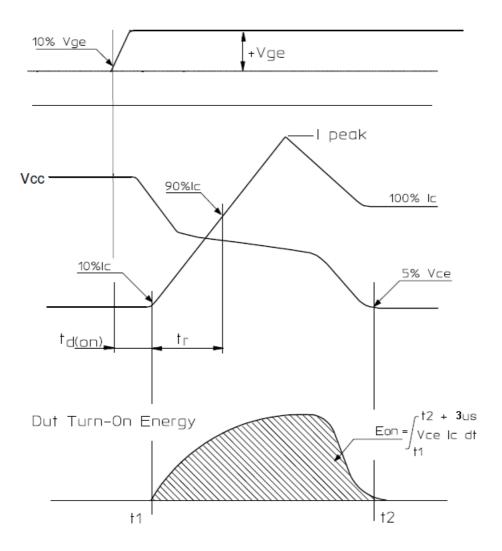


Figure 21. Definition of Turn On Waveform

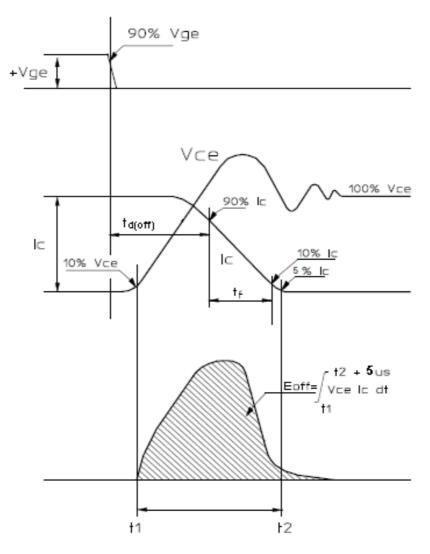
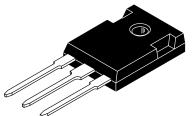


Figure 22. Definition of Turn Off Waveform





TO-247 CASE 340L ISSUE G

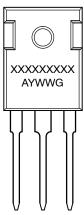
DATE 06 OCT 2021

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETER

	MILLIMETERS		INC	HES
DIM	MIN.	MAX.	MIN.	MAX.
Α	20.32	21.08	0.800	0.830
В	15.75	16.26	0.620	0.640
С	4.70	5.30	0.185	0.209
D	1.00	1.40	0.040	0.055
Ε	1.90	2.60	0.075	0.102
F	1.65	2.13	0.065	0.084
G	5.45 BSC		0.215 BSC	
Н	1.50	2.49	0.059	0.098
J	0.40	0.80	0.016	0.031
К	19.81	20.83	0.780	0.820
L	5.40	6.20	0.212	0.244
N	4.32	5.49	0.170	0.216
Р		4.50		0.177
Q	3.55	3.65	0.140	0.144
U	6.15	6.15 BSC		BSC
W	2.87	3.12	0.113	0.123

GENERIC MARKING DIAGRAM*



STYLE 1:	STYLE 2:	STYLE 3:
PIN 1. GATE	PIN 1. ANODE	PIN 1. BASE
2. DRAIN	2. CATHODE (S)	2. COLLECTOR
3. SOURCE	3. ANODE 2	3. EMITTER
4. DRAIN	4. CATHODES (S)	4. COLLECTOR

PIN 1. MAIN TERMINAL 1 2. MAIN TERMINAL 2

3. GATE 4. MAIN TERMINAL 2

⊕ 0.25 (0.010)**W** Y AS

STYLE 4:
PIN 1. GATE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

XXXXX = Specific Device Code
A = Assembly Location
Y = Year

Y = Year

WW = Work Week

G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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STYLE 5: PIN 1. CATHODE

2. ANODE

3. GATE 4. ANODE

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