





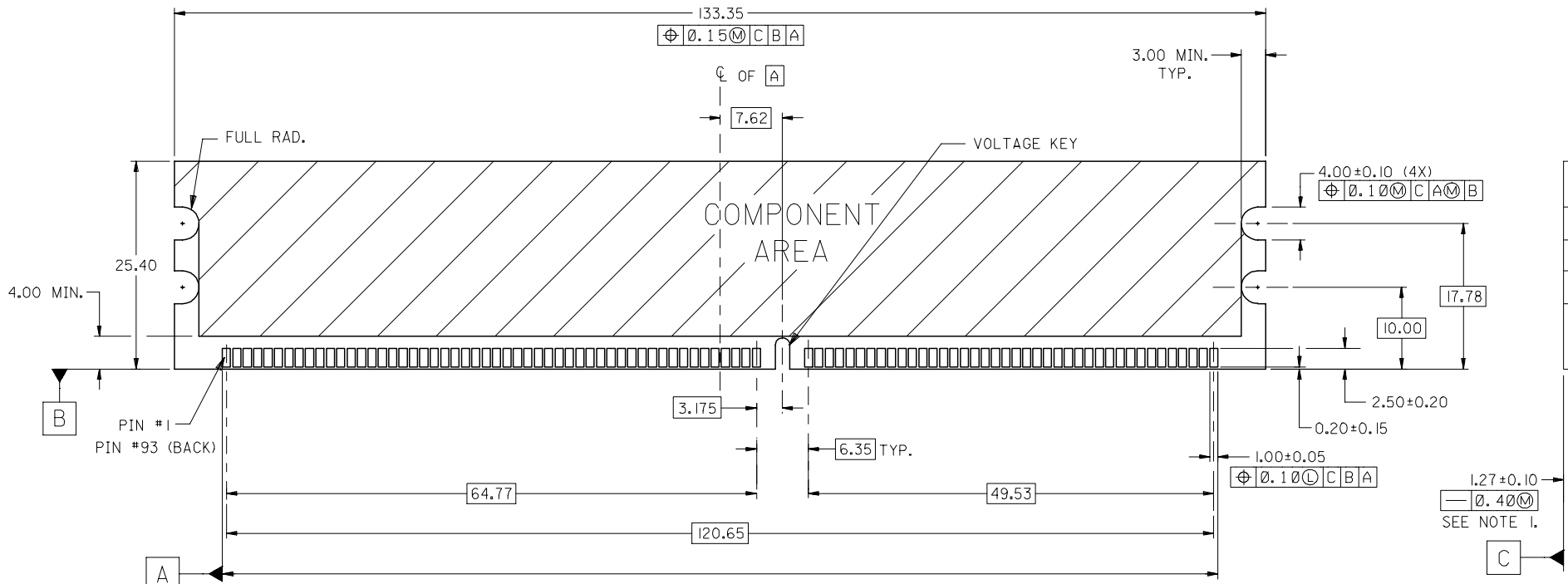
RECOMMENDED P.C. BOARD HOLE PATTERN
(CONNECTOR SIDE)

ADD DIMENSION EC NO: S2007-0163 DRWN:CMTEO CHKD:SHLENI APPR:GGLLEE	2006/08/17 2006/08/17 2006/08/18	DESCRIPTION REV	QUALITY SYMBOLS ▽=0 ◻=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION			
				4 PLACES	± ---	± ---	DRAWN BY	DATE	TITLE				
				3 PLACES	± ---	± ---	MLONG	2000/05/10	DDR DIMM, 1.27MM PITCH 184 CKTS, SINGLE KEY				
2 PLACES	± 0.15	± ---	2 PLACES	± 0.15	± ---	CHECKED BY	DATE	MOLEX INCORPORATED					
1 PLACE	± ---	± ---	1 PLACE	± ---	± ---	SQUEK	2000/05/11	SD-87636-001					
ANGULAR ± 3 °				DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		APPROVED BY		DATE	SHEET NO.				
						SKTOH		2000/05/12	2 OF 4				
						MATERIAL NO.		DOCUMENT NO.					
						SEE TABLE		SD-87636-001					
						SIZE		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					

PART NUMBER	DATUM VOLTAGE KEY	PACKAGING TYPE	DIM "T"	RECOMMENDED PCB THICKNESS	LATCH COLOR	CONTACT AREA PLATING
87636-2001	CENTER (1.8V)	TRAY	3.23	2.36	BEIGE	0.38uM/15uIN MIN. GOLD
87636-2002	LEFT (2.5V)					
87636-2011	CENTER (1.8V)					
87636-2012	LEFT (2.5V)					
87636-3001	CENTER (1.8V)		3.23	2.36	BLACK	0.76uM/30uIN MIN. GOLD
87636-3002	LEFT (2.5V)					
87636-3011	CENTER (1.8V)					
87636-3012	LEFT (2.5V)					
87636-2102	LEFT (2.5V)		3.23	2.36	BLACK	0.38uM/15uIN MIN. GOLD

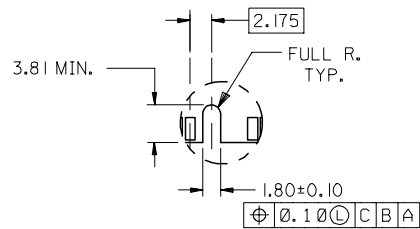
ADD DIMENSION EC NO: S2007-0163 DRWN:CMTEO 2006/08/17 CHKD:SHLENI 2006/08/17 APPR:GGLLEE 2006/08/18	QUALITY SYMBOLS  = 0  = 0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	 THIRD ANGLE PROJECTION		
			mm	INCH	DRAWN BY	DATE	DDR DIMM, 1.27MM PITCH 184 CKTS, SINGLE KEY			
		4 PLACES	± ---	± ---	MLONG	2000/05/10				
		3 PLACES	± ---	± ---	CHECKED BY	DATE	 MOLEX INCORPORATED			
		2 PLACES	± 0.15	± ---	SQUEK	2000/05/11				
1 PLACE	± ---	± ---	APPROVED BY	DATE	MATERIAL NO. SKT0H 2000/05/12		DOCUMENT NO. SD-87636-001		SHEET NO. 3 OF 4	
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS			SEE TABLE		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					

10 9 8 7 6 5 4 3 2 1

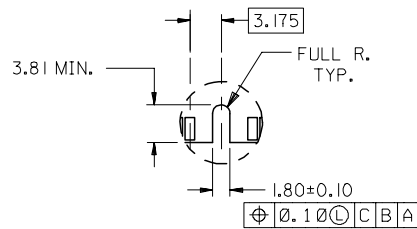


(OUTSIDE EDGE OF END PADS)

RECOMMENDED MODULE LAYOUT
184 CKT 1.8V SHOWN



OFFSET LEFT KEY



CENTERED KEY

NOTES:

1. STRAIGHTNESS OF MODULE APPLIES TO THE AREA FROM THE BOTTOM OF THE CARD UP 4.00 MM.
2. IF TIE BARS ARE ATTACHED TO PADS, THE TIE BAR SHOULD BE ON AN INTERNAL LAYER, SO THAT THE REMNANT CANNOT CAUSE DAMAGE TO THE CONTACTS.

ADD DIMENSION EC NO: S2007-0163 2006/08/17 DRWN:CMTEO 2006/08/17 CHKD:SHLENI 2006/08/18 APPR:GGLLEE	DESCRIPTION REV	QUALITY SYMBOLS ▽=0 ∇=0	GENERAL TOLERANCES (UNLESS SPECIFIED)	DIMENSION STYLE MM ONLY	SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	
				DRAWN BY MLONG	DATE 2000/05/10	TITLE DDR DIMM, 1.27MM PITCH 184 CKTS, SINGLE KEY		
				4 PLACES ± --- ± --- 3 PLACES ± --- ± --- 2 PLACES ± 0.15 ± --- 1 PLACE ± --- ± ---	CHECKED BY SQUEK	DATE 2000/05/11		
				ANGULAR ± 3 °	APPROVED BY SKTOH	DATE 2000/05/12	MOLEX INCORPORATED	
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	SEE TABLE	MATERIAL NO.	DOCUMENT NO. SD-87636-001	SHEET NO. 4 OF 4		

9 8 7 6 5 4 3 2 1