

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Not recommended
for new design

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N-CHANNEL MOS FIELD EFFECT TRANSISTOR
 FOR SWITCHING

DESCRIPTION

The μ PA622TT is a switching device which can be driven directly by a 4.0 V power source.

This device features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- 4.0 V drive available
- Low on-state resistance
- $R_{DS(on)1} = 82 \text{ m}\Omega \text{ MAX.}$ ($V_{GS} = 10 \text{ V}$, $I_D = 1.5 \text{ A}$)
- $R_{DS(on)2} = 120 \text{ m}\Omega \text{ MAX.}$ ($V_{GS} = 4.5 \text{ V}$, $I_D = 1.0 \text{ A}$)
- $R_{DS(on)3} = 139 \text{ m}\Omega \text{ MAX.}$ ($V_{GS} = 4.0 \text{ V}$, $I_D = 1.0 \text{ A}$)

★ ORDERING INFORMATION

PART NUMBER	PACKAGE
μ PA622TT-E1-A	6 pin WSOF (1620)
μ PA622TT-E2-A	

Remark "-A" indicates Pb-free (This product does not contain Pb in external electrode and other parts.).
 "-E1" or "-E2" indicates the unit orientation.
 (8 mm embossed carrier tape, 3000 pcs/reel)

Marking: WC

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

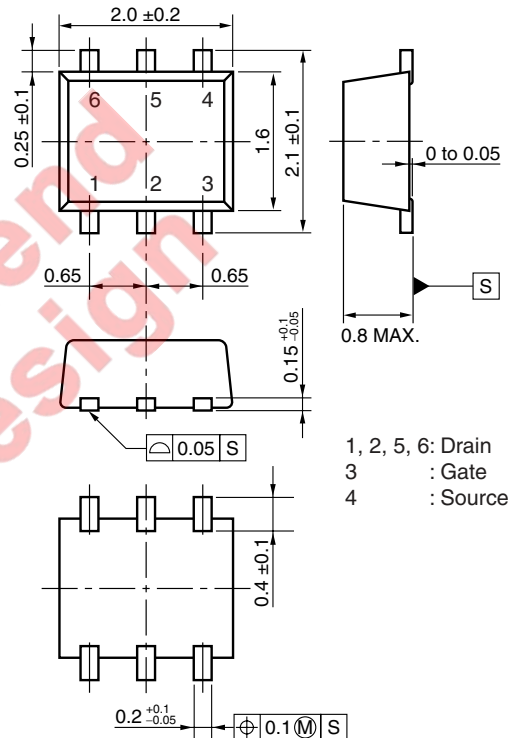
Drain to Source Voltage ($V_{GS} = 0 \text{ V}$)	V_{DSS}	30	V
Gate to Source Voltage ($V_{DS} = 0 \text{ V}$)	V_{GSS}	± 20	V
Drain Current (DC)	$I_{D(DC)}$	± 3.0	A
Drain Current (pulse)	$I_{D(pulse)}$	± 12	A
Total Power Dissipation	P_{T1}	0.2	W
Total Power Dissipation	P_{T2}	1.3	W
Channel Temperature	T_{ch}	150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

- Notes** 1. Mounted on FR-4 board of $5000 \text{ mm}^2 \times 1.1 \text{ mm}$, $t \leq 5 \text{ sec.}$
 2. $PW \leq 10 \mu\text{s}$, Duty Cycle $\leq 1\%$

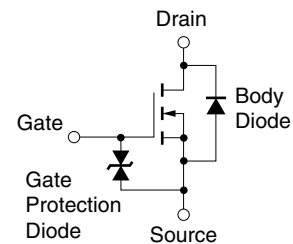
Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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PACKAGE DRAWING (Unit: mm)



EQUIVALENT CIRCUIT

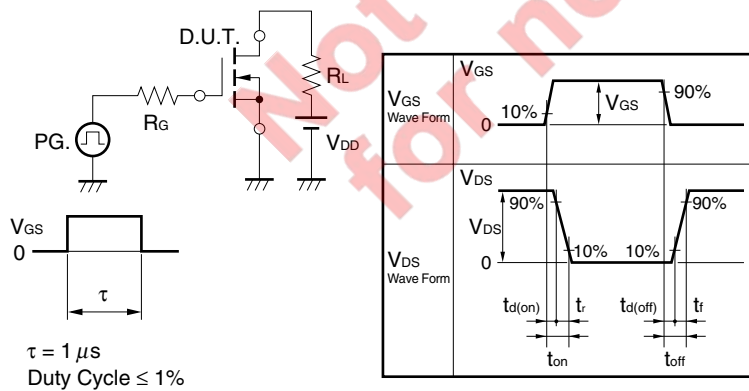


ELECTRICAL CHARACTERISTICS (T_A = 25°C)

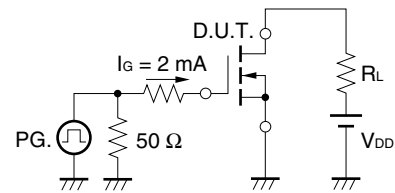
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			10	μA
★ Gate Leakage Current	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V			±10	μA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1.0 mA	1.5	2.0	2.5	V
Forward Transfer Admittance ^{Note}	y _{fs}	V _{DS} = 10 V, I _D = 1.5 A	0.5	2.1		S
Drain to Source On-state Resistance ^{Note}	R _{DS(on)1}	V _{GS} = 10 V, I _D = 1.5 A		65	82	mΩ
	R _{DS(on)2}	V _{GS} = 4.5 V, I _D = 1.0 A		90	120	mΩ
	R _{DS(on)3}	V _{GS} = 4.0 V, I _D = 1.0 A		104	139	mΩ
Input Capacitance	C _{iss}	V _{DS} = 10 V		155		pF
Output Capacitance	C _{oss}	V _{GS} = 0 V		45		pF
Reverse Transfer Capacitance	C _{rss}	f = 1.0 MHz		27		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 15 V, I _D = 1.5 A		10		ns
Rise Time	t _r	V _{GS} = 10 V		28		ns
Turn-off Delay Time	t _{d(off)}	R _G = 10 Ω		75		ns
Fall Time	t _f			50		ns
Total Gate Charge	Q _G	V _{DD} = 24 V		3.8		nC
Gate to Source Charge	Q _{GS}	V _{GS} = 10 V		0.7		nC
Gate to Drain Charge	Q _{GD}	I _D = 3.0 A		1.3		nC
Body Diode Forward Voltage ^{Note}	V _{F(S-D)}	I _F = 3.0 A, V _{GS} = 0 V		0.90		V

Note Pulsed

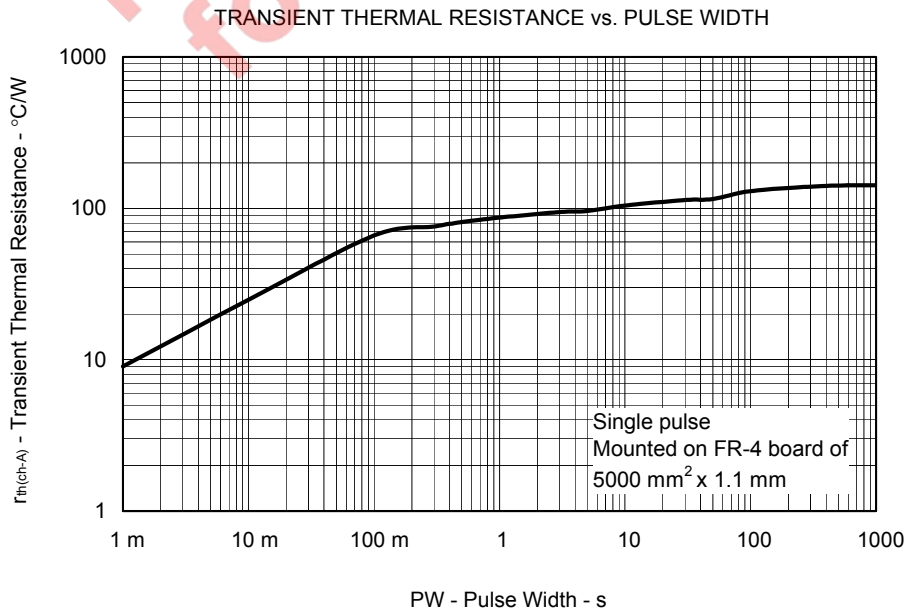
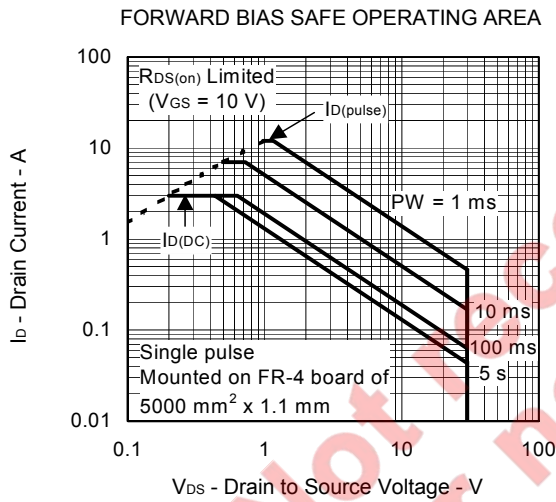
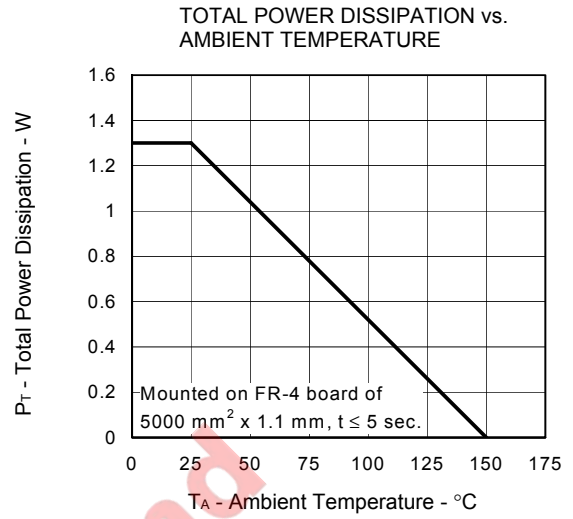
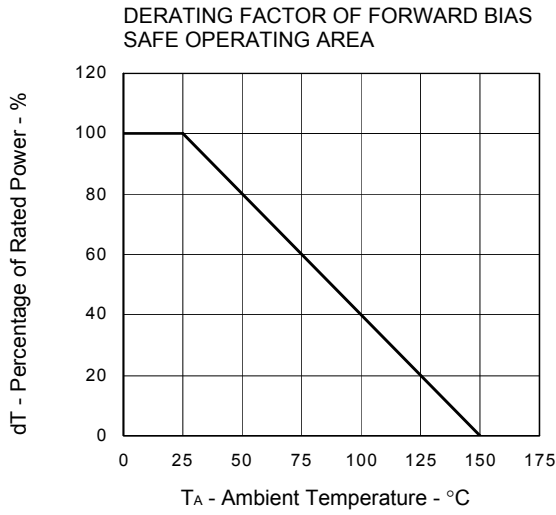
TEST CIRCUIT 1 SWITCHING TIME



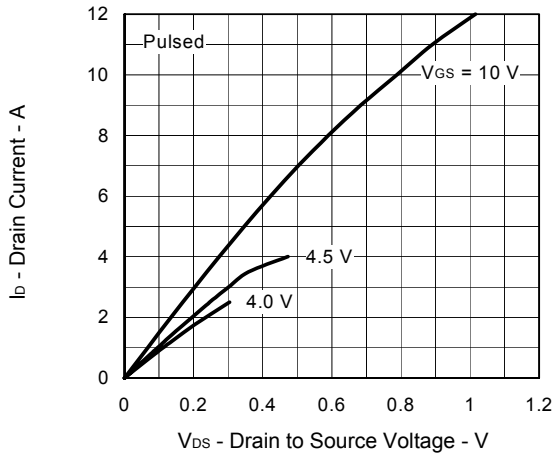
TEST CIRCUIT 2 GATE CHARGE



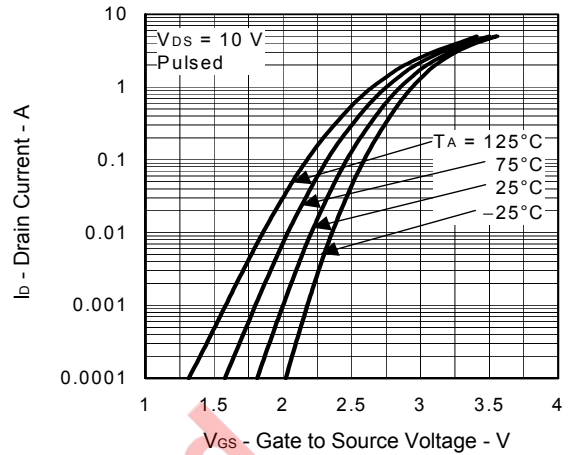
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$)



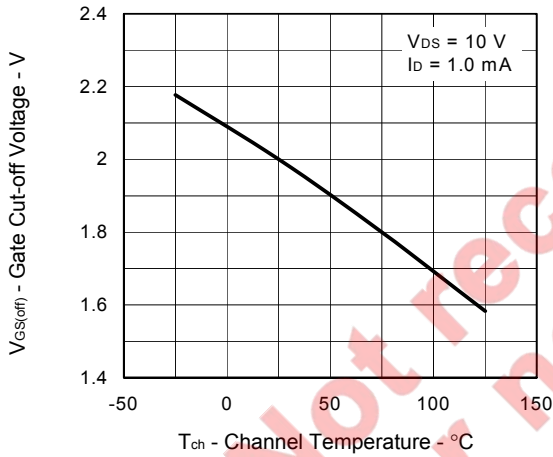
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



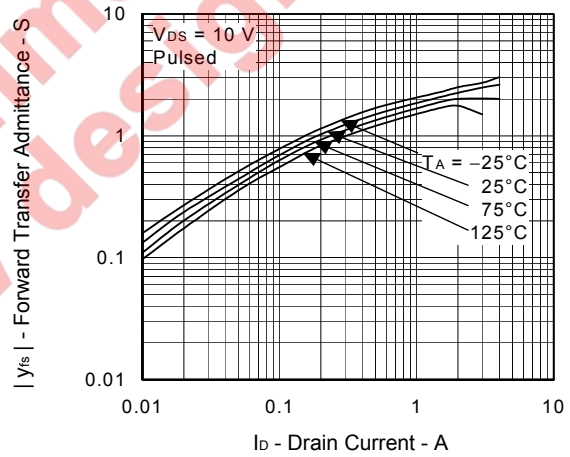
FORWARD TRANSFER CHARACTERISTICS



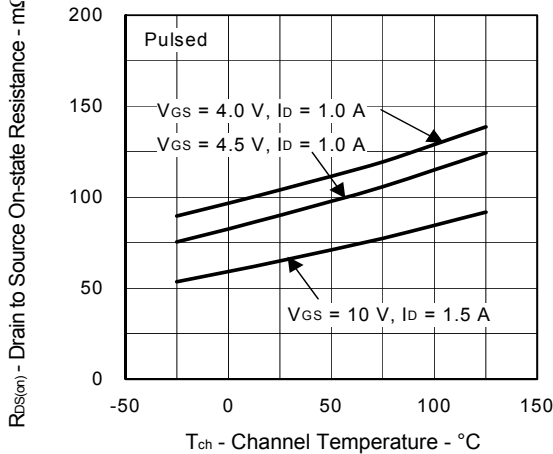
GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



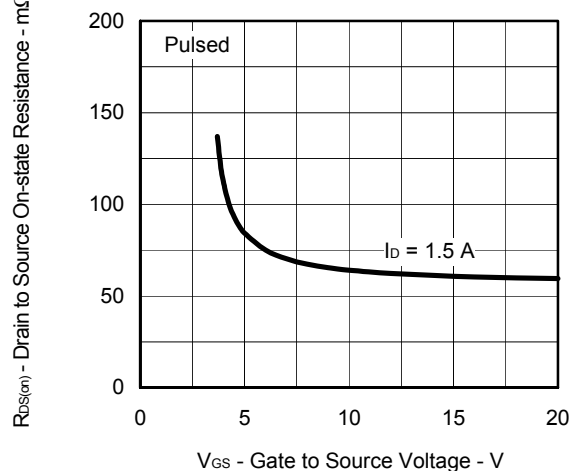
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

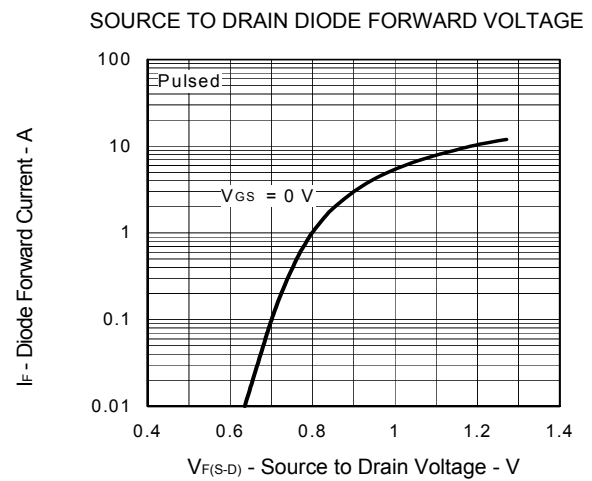
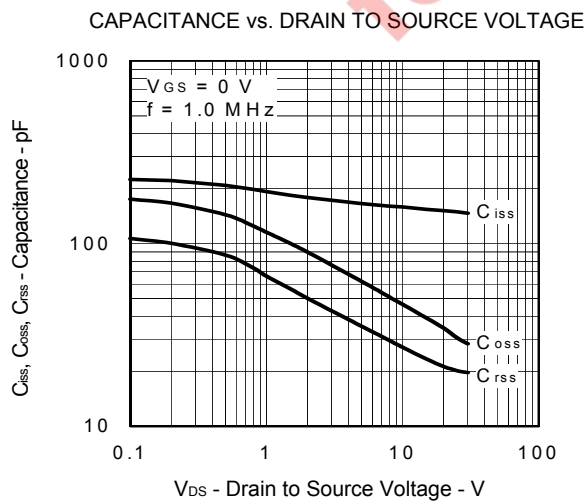
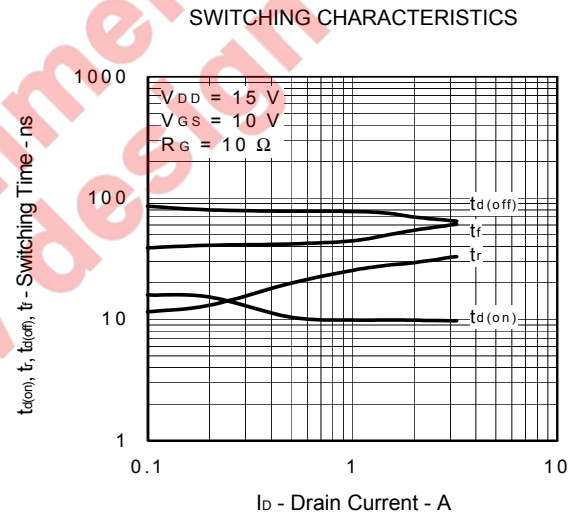
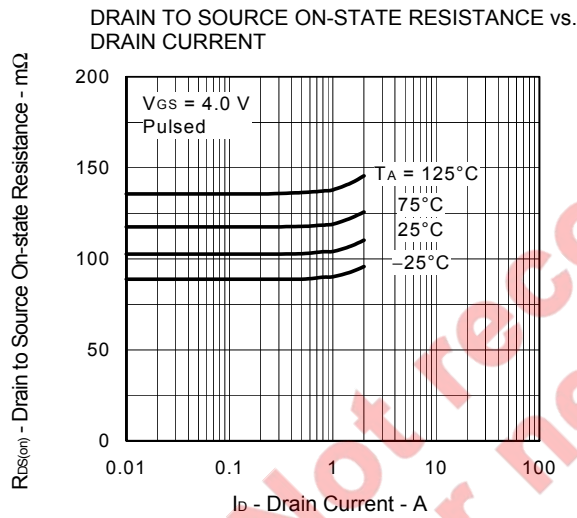
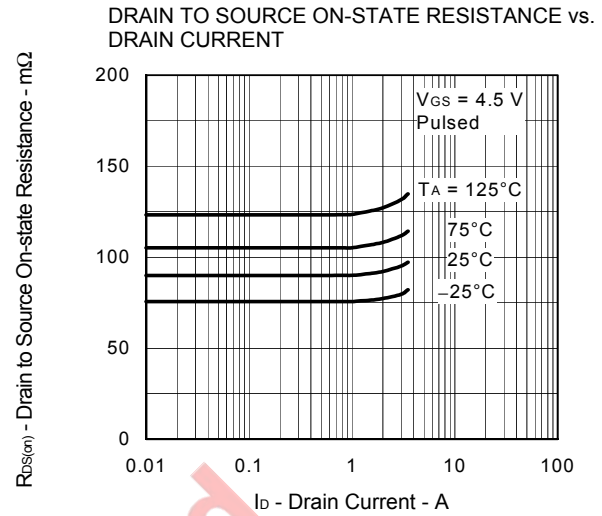
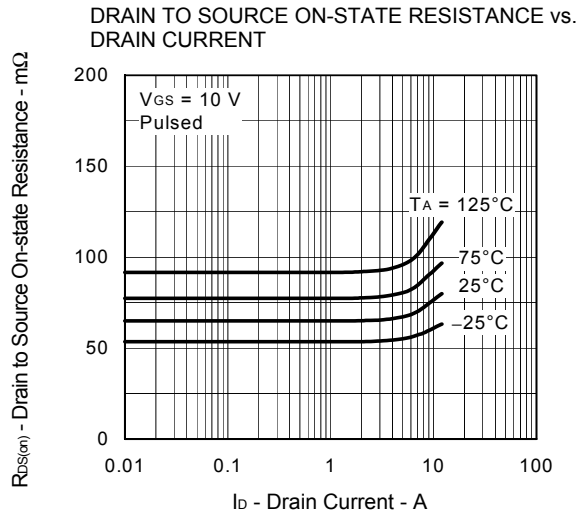


DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE

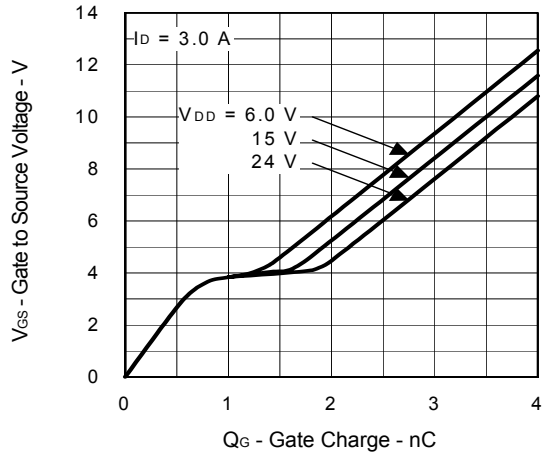


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE





DYNAMIC INPUT/OUTPUT CHARACTERISTICS



Not recommend
for new design

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