- Inputs Are TTL-Voltage Compatible
- Center-Pin V_{CC} and GND Pin Configurations Minimize High-Speed Switching Noise
- EPIC[™] (Enhanced-Performance Implanted CMOS) 1-µm Process
- 500-mA Typical Latch-Up Immunity at 125°C
- Package Options Include Plastic Small-Outline Packages and Standard Plastic 300-mil DIPs

(TOP VIEW) **1** 1B 1A 1Y 🛮 15 7 2A 2 2Y 🛮 3 14 T 2B 13 V_{CC} GND □ 4 GND [5 12 V_{CC} 3Y 🛮 11 3A 6 10 3B 4Υ **Π** 7 4В П 8 9 1 4A

D OR N PACKAGE

description

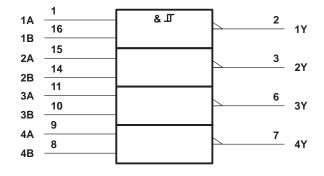
This device contains four independent 2-input NAND gates with Schmitt-trigger inputs. Because of the Schmitt action, they have different input threshold levels for positive- and negative-going signals. Each gate performs the Boolean function $Y = \overline{A \bullet B}$ or $Y = \overline{A \bullet B}$ in positive logic.

The 74ACT11132 is characterized for operation from -40°C to 85°C.

FUNCTION TABLE

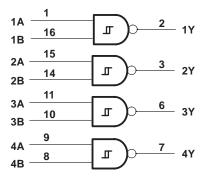
INP	OUTPUT				
Α	В	Υ			
Н	Н	L			
L	Χ	Н			
Х	L	Н			

logic symbol†



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

logic diagram (positive logic)



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74ACT11132 QUADRUPLE POSITIVE-NAND GATE WITH SCHMITT-TRIGGER INPUTS

SCAS177 - D3974, JANUARY 1992 - REVISED APRIL 1993

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage range, V _{CC}	0.5 V to 7 V
Input voltage range, V _I (see Note 1)	$-0.5 \text{ V to V}_{CC} + 0.5 \text{ V}$
Output voltage range, V _O (see Note 1)	$-0.5 \text{ V to V}_{CC} + 0.5 \text{ V}$
Input clamp current, $I_{ K }$ ($V_{ C }$ or $V_{ C }$ $V_{ C }$	±20 mA
Output clamp current, I _{OK} (V _O < 0 or V _O > V _{CC})	± 50 mA
Continuous output current, $I_O(V_O = 0 \text{ to } V_{CC})$	± 50 mA
Continuous current through V _{CC} or GND	±100 mA
Storage temperature range	–65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions (see Note 2)

		MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	V
VIH	High-level input voltage	2			V
V_{IL}	Low-level input voltage			8.0	V
VI	Input voltage	0		VCC	V
VO	Output voltage	0		VCC	V
IOH	High-level output current			-24	mA
lOL	Low-level output current			24	mA
$\Delta t/\Delta v$	Input transition rise or fall rate	0		10	ns/V
TA	Operating free-air temperature	-40		85	°C

NOTE 2: Unused or floating inputs must be held high or low.

NOTE 1: The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	\ ,_	T _A = 25°C			BAINI	MAY	LINUT
		VCC	MIN	TYP	MAX	MIN	MAX	UNIT
\/_		4.5 V			2		2	V
V _{T+}		5.5 V			2		2	V
\/-		4.5 V			0.8		0.8	V
V _T _		5.5 V			0.8		0.8	V
V _{Hys} (V _{T+} - V _{T-})		4.5 V	0.4		1.2	0.4	1.2	V
(VT+-VT-)		5.5 V	0.4		1.2	0.4	1.2	V
	I _{OH} = -50 μA	4.5 V	4.4			4.4		٧
		5.5 V	5.4			5.4		
Voн	$I_{OH} = -24 \text{ mA}$	4.5 V	3.94			3.8		
		5.5 V	4.94			4.8		
	$I_{OH} = -75 \text{ mA}^{\dagger}$	5.5 V				3.85		
	I _{OL} = 50 μA	4.5 V			0.1		0.1	V
		5.5 V			0.1		0.1	
VOL		4.5 V			0.36		0.44	
		5.5 V			0.36		0.44	
	$I_{OL} = 75 \text{ mA}^{\dagger}$	5.5 V					1.65	<u> </u>
lį	$V_I = V_{CC}$ or GND	5.5 V			±0.1		±1	μΑ
Icc	$V_I = V_{CC}$ or GND, $I_O = 0$	5.5 V			8		80	μΑ
∆l _{CC} ‡	One input at 3.4 V, Other inputs at V _{CC} or GND	5.5 V			0.9		1	mA
Ci	$V_I = V_{CC}$ or GND	5 V		3.5				pF

[†] Not more than one output should be tested at a time, and the duration of the test should not exceed 10 ms.

switching characteristics over recommended operating free-air temperature range, V_{CC} = 5 V \pm 0.5 V (unless otherwise noted) (see Figure 1)

DADAMETED	FROM	то	T _A = 25°C				MAY	LINUT
PARAMETER	(INPUT)	(OUTPUT)	MIN	TYP	MAX	MIN	MAX	UNIT
^t PLH	A or B	V	2.6	5.3	8	2.6	8.8	20
^t PHL		Ť	3.7	6.4	8.1	3.7	9.3	ns

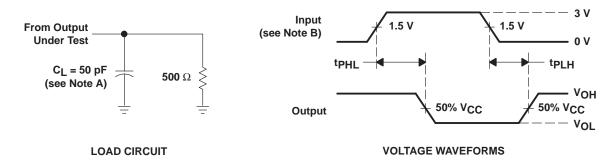
operating characteristics, V_{CC} = 5 V, T_A = 25°C

PARAMETER		TEST CONDITIONS	TYP	UNIT
C _{pd}	Power dissipation capacitance	$C_L = 50 \text{ pF}, f = 1 \text{ MHz}$	29	pF

[‡] This is the increase in supply current for each input that is at one of the specified TTL voltage levels rather than 0 V or VCC.

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PARAMETER MEASUREMENT INFORMATION



NOTES: A. C_L includes probe and jig capacitance.

- B. Input pulses are supplied by generators having the following characteristics: PRR \leq 10 MHz, $Z_O = 50 \Omega$, $t_f = 3 \text{ ns}$, $t_f = 3 \text{ ns}$.
- C. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms

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