



30V N-Channel Enhancement Mode MOSFET

Voltage

30 V

Current

8 A

Features

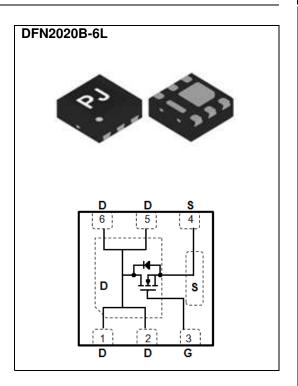
- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@8A<18m\Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_D@6A$ < $28m\Omega$
- High switching speed
- Improved dv/dt capability
- Low gate charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

• Case: DFN2020B-6L Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0003 ounces, 0.0086 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAME	SYMBOL	LIMIT	UNITS		
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	<u>+</u> 20	V	
Continuous Drain Current	T _A =25°C	I _D	8.0	_	
Pulsed Drain Current		I _{DM}	32	A	
Power Dissipation	T _A =25°C	D	2.0	W	
	Derate above 25°C	P _D	16	mW/°C	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance					
- Junction to Ambient (Note 4)	R _θ ЈА	62.5	°C/W		





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS		
Static								
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =250uA	30	-	-	V		
Gate Threshold Voltage	$V_{\text{GS(th)}}$	V _{DS} =V _{GS} ,I _D =250uA	1.0	1.7	2.5	V		
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V,I _D =8A	-	16	18	mΩ		
		V _{GS} =4.5V,I _D =6A	-	23	28			
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V	-	-	1.0	uA		
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA		
Dynamic (Note 5)								
Total Gate Charge	Qg	V _{DS} =15V, I _D =8A, V _{GS} =4.5V (Note 2,3)	-	4.3	-	nC		
Gate-Source Charge	Qgs		-	1.3	-			
Gate-Drain Charge	Q_{gd}		-	1.6	-			
Input Capacitance	Ciss	V _{DS} =25V, V _{GS} =0V, f=1.0MHZ	-	392	-	pF		
Output Capacitance	Coss		-	76	-			
Reverse Transfer Capacitance	Crss		-	54	-			
Turn-On Delay Time	td _(on)	V _{DS} =15V, I _D =1A,	-	5.9	-	ns		
Turn-On Rise Time	tr	$\begin{array}{c} V_{GS}{=}10V,\ R_{G}{=}6\Omega \\ \text{(Note 2,3)} \end{array}$	-	11	-			
Turn-Off Delay Time	td _(off)		-	17	-			
Turn-Off Fall Time	tf		-	3.8	-			
Drain-Source Diode								
Maximum Continuous Drain-Source				-	1.5	Α		
Diode Forward Current	Is							
Diode Forward Voltage	V _{SD}	I _S =1.0A, V _{GS} =0V	-	0.73	1.0	V		

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Reja is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 5. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

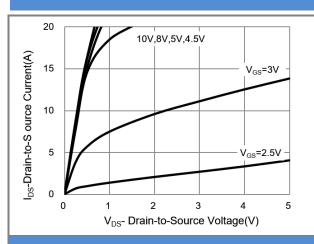


Fig.1 On-Region Characteristics

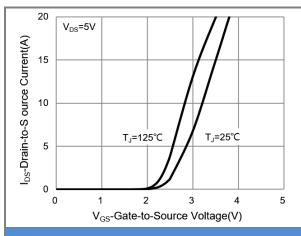


Fig.2 Transfer Characteristics

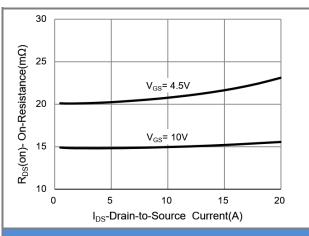


Fig.3 On-Resistance vs. Drain Current

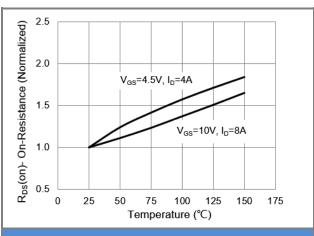


Fig.4 On-Resistance vs. Junction temperature

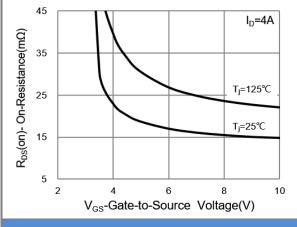
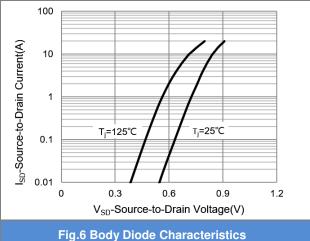


Fig.5 On-Resistance Variation with VGS.







TYPICAL CHARACTERISTIC CURVES

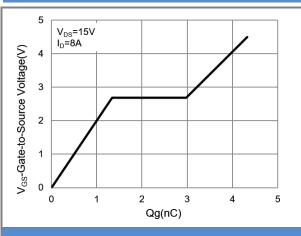


Fig.7 Gate-Charge Characteristics

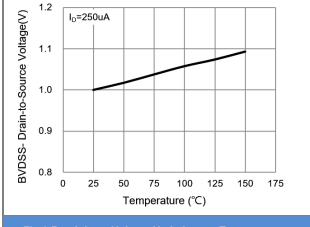


Fig.8 Breakdown Voltage Variation vs. Temperature.

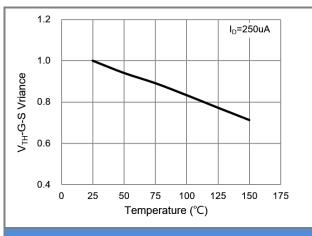


Fig.9 Threshold Voltage Variation with Temperature

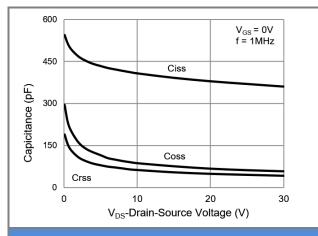


Fig.10 Capacitance vs. Drain-Source Voltage.

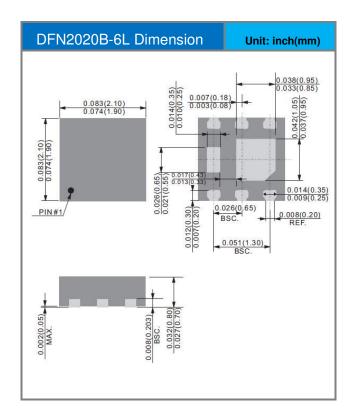


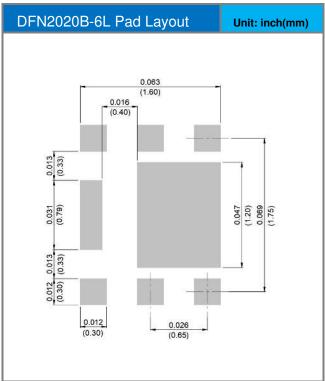


Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ2422_R1_00001	DFN2020B-6L	3K pcs / 7" reel	422	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout









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