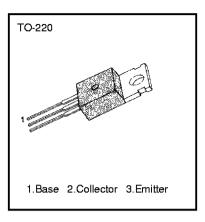
# PNP SILICON TRANSISTOR

## GENERAL PURPOSE AND SWITCHING APPLICATIONS DC CURRENT GAIN SPECIFIED TO 10 AMPERES

• High Current Gain-Bandwidth Product ( $f_T = 2MHz (MIN)$ )

### **ABSOLUTE MAXIMUM RATINGS**

Characteristic	Symbol	Rating	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-70	٧
Collector-Emitter Voltage	V <sub>CEO</sub>	-60	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5	V
Collector Current	l <sub>C</sub>	-10	Α
Base Current	I <sub>B</sub>	-6	Α
Collector Dissipation (T <sub>C</sub> =25°C)	Pc	75	w
Collector Dissipation (T <sub>A</sub> =25°C)	Pc	0.6	W
Junction Temperature	TJ	150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ 150	°C

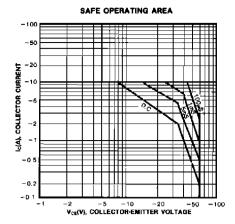


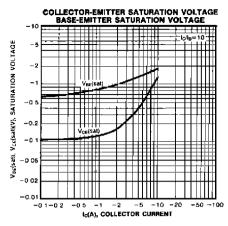
# ELECTRICAL CHARACTERISTICS (T<sub>c</sub> =25°C)

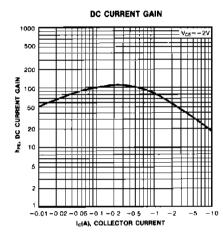
Characteristic	Symbol	Test Conditions	Min	Max	Unit
Collector Emitter Sustaining Voltage	V <sub>CEO</sub> (sus)	I <sub>C</sub> = - 200mA, I <sub>B</sub> = 0	-60		٧
Collector Cutoff Current	I <sub>CEO</sub>	V <sub>CE</sub> = - 30V, I <sub>B</sub> = 0		-700	μА
Collector Cutoff Current	I <sub>CEX</sub>	$V_{CE} = -70V, V_{BE}(off) = 1.5V$		-1	mA
		V <sub>CE</sub> = - 70V, V <sub>BE</sub> (off) = 1.5V		-5	mA
		T <sub>C</sub> = 150°C			
Emitter Cutoff Current	I <sub>EBO</sub>	V <sub>EB</sub> = - 5V, I <sub>C</sub> = 0		-5	mA
* DC Current Gain	h <sub>FE</sub>	$V_{CE} = -4V, I_{C} = -4A$	20	100	
		V <sub>CE</sub> = -4V, I <sub>C</sub> = -10A	5		
* Collector-Emitter Saturation Voltage	V <sub>CE</sub> (sat)	$I_C = -4A$ , $I_B = -0.4A$		-1.1	٧
		I <sub>C</sub> = - 10A, I <sub>B</sub> = - 3.3A		-8	٧
* Base Emitter On Voltage	V <sub>BE</sub> (on)	$V_{CE} = -4V, I_{C} = -4A$		-1.8	٧
Current Gain Bandwidth Product	f⊤	$V_{CE} = -10V$ , $I_{C} = -500$ mA,	2		MHz
		f = 500KHz			

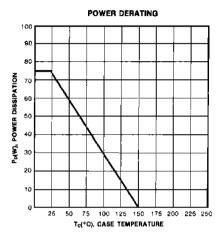
<sup>\*</sup> Pulse test: PW≤300µs, duty cycle≤2% Pulse













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