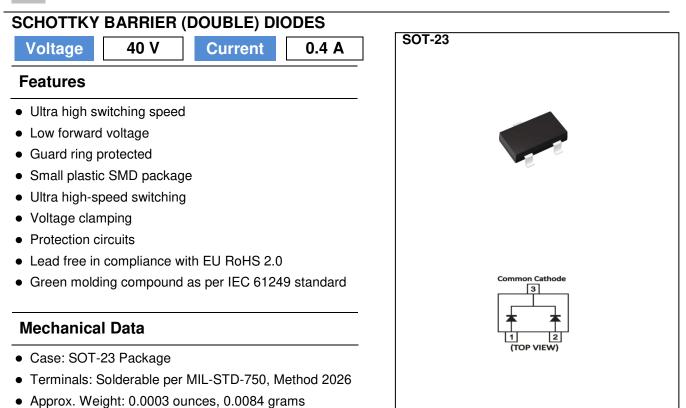
# BAT721AC



#### **Maximum Ratings and Thermal Characteristics** ( $T_A = 25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	40	V
Maximum Rms Voltage	V <sub>RMS</sub>	28	V
Maximum Dc Blocking Voltage	V <sub>DC</sub>	40	V
Maximum Average Forward Current	I <sub>F(AV)</sub>	0.4	А
Peak Forward Surge Current : 8.3 ms Single Half Sine- Wave Superimposed On Rated Load Per Diode	I <sub>FSM</sub>	1	A
Typical Junction Capacitance Measured at 1 MHZ And Applied $V_R = 4 V$	CJ	7	pF
Typical Thermal Resistance Per Diode	R <sub>0JA</sub> <sup>(1)</sup>	350	°C/W
Operating Junction Temperature Range	TJ	-55~125	°C
Storage Temperature Range	T <sub>STG</sub>	-55~150	°C





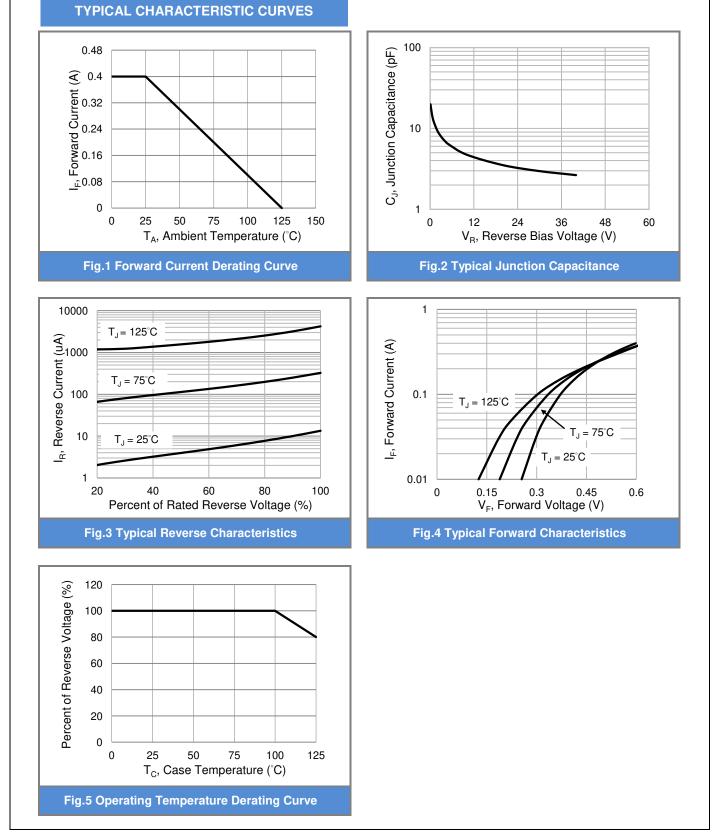
## BAT721AC

### **Electrical Characteristics** ( $T_A = 25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Forward Voltage	V <sub>F</sub>	$I_F = 0.2 \text{ A},  T_J = 25 \ ^{\circ}\text{C}$	-	0.45	-	V
		$I_F = 0.4 \text{ A}, T_J = 25 ^{\circ}\text{C}$	-	-	0.65	
		$I_F = 0.2 \text{ A},  T_J = 125 ^{\circ}\text{C}$	-	0.43	-	
		$I_F = 0.4 \text{ A}, T_J = 125 \degree \text{C}$	-	0.62	-	
Reverse Current	$I_{R}^{(2)}$	$V_{R} = 32 \text{ V}, \text{ T}_{J} = 25 ^{\circ}\text{C}$	-	8	-	uA
		$V_{R} = 40 \text{ V},  \text{T}_{J} = 25 ^{\circ}\text{C}$	-	-	50	uA
		$V_{R} = 40 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	2.5	-	mA

NOTES:

- 1. Mounted on a FR4 PCB, single-sided copper, mini pad.
- 2. Short duration pulse test used to minimize self-heating effect.



## 

BAT721AC

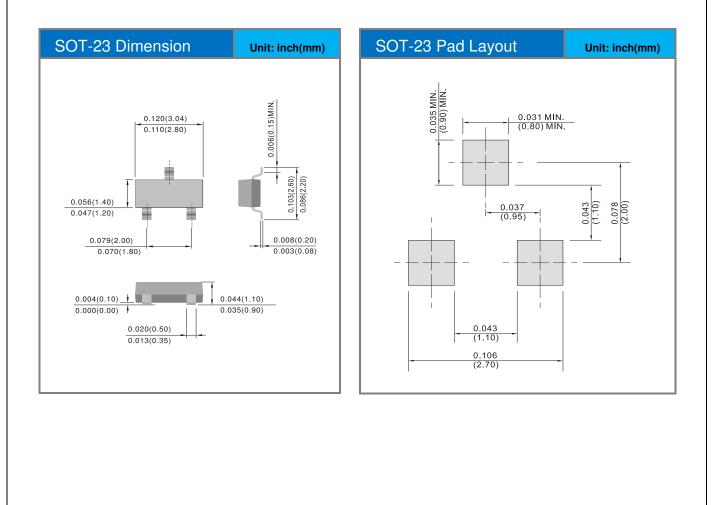




#### Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
BAT721AC_R1_00001	SOT-23	3K pcs / 7" reel	TAC	Halogen free

#### Packaging Information & Mounting Pad Layout





## BAT721AC

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