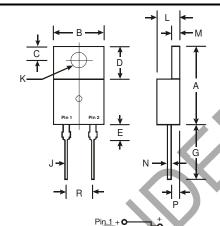
# SBL1630 - SBL1660

# **Features**

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications
- Lead Free Finish, RoHS Compliant (Note 3)

## **Mechanical Data**

- Case: TO-220AC
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Polarity: See Diagram
- Terminals: Finish Tin. Solderable per MIL-STD-202, Method 208 @3
- Mounting Position: Any Marking: Type Number
- Weight: 2.24 grams (approximate)



TO-220AC					
Dim	Min	Max			
Α	14.48	15.75			
В	10.00	10.40			
C	2.54	3.43			
D	5.90	6.40			
щ	2.80	3.93			
G	12.70	14.27			
7	0.69	0.93			
K	3.54	3.78			
7	4.07	4.82			
M	1.15	1.39			
Z	0.30	0.50			
Р	2.04	2.79			
R 🦠	4.83	5.33			
All Dimensions in mm					

### **Maximum Ratings and Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	SBL 1630	SBL 1635	SBL 1640	SBL 1645	SBL 1650	SBL 1660	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	30	35	40	45	50	60	٧
RMS Reverse Voltage	V <sub>R(RMS)</sub>	21	24.5	28	31.5	35	42	V
Average Rectified Output Current (Note 1) @ T <sub>C</sub> = 95°C lo						Α		
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	275					Α	
Forward Voltage Drop @ I <sub>F</sub> =16A, T <sub>C</sub> = 25°C	V <sub>FM</sub>	0.57 0.75				V		
Peak Reverse Current $@T_C = 25^{\circ}C$ at Rated DC Blocking Voltage $@T_C = 100^{\circ}C$ 1.0					mA			
Typical Junction Capacitance (Note 2)	C <sub>j</sub>	700				pF		
Thermal Resistance Junction to Case (Note 1)	$R_{ heta JC}$	3.5				°C/W		
Operating and Storage Temperature Range	$T_{j}$ , $T_{STG}$	-65 to +150					°C	

Notes:

- 1. Thermal resistance junction to case mounted on heatsink.
- 2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.
- 3, RoHS revision 13.2.2003. Glass and high temperature solder exemptions applied, see EU Directive Annex Notes 5 and 7.



# NOT RECOMMENDED FOR NEW DESIGN

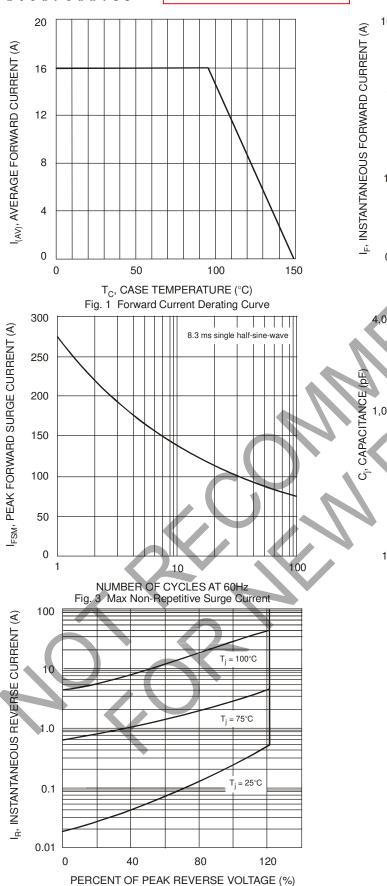


Fig. 5 Typical Reverse Characteristics

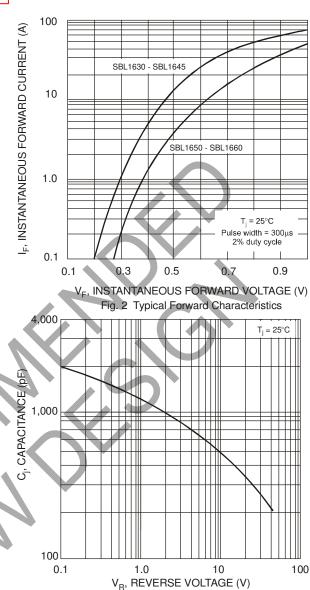


Fig. 4 Typical Junction Capacitance



### NOT RECOMMENDED FOR NEW DESIGN

#### Ordering Information (Note 4)

Device	Packaging	Shipping
SBL16xx*	TO-220AC	50/Tube

<sup>\*</sup> xx = Device type, e.g. SBL1645

4. For packaging details, visit our website at http://www.diodes.com/datasheets/ap02008.pdf. Notes:

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