



#### 20V P-Channel Enhancement Mode MOSFET

Voltage -20 V Current -0.9A

#### **Features**

- RDS(ON), VGS@-4.5V, ID@-0.9A<130mΩ
- RDS(ON) , VGS@-2.5V, ID@-0.6A<160mΩ</li>
- RDS(ON) , VGS@-1.8V, ID@-0.4A<210m $\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

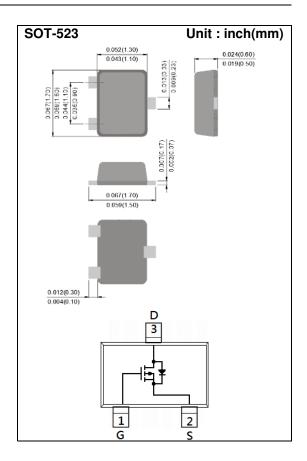
#### **Mechanical Data**

• Case: SOT-523 Package

• Terminals: Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.002 grams

Marking: E01



## Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAME	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	-20	V
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 12	V
Continuous Drain Current		I <sub>D</sub>	-0.9	Α
Pulsed Drain Current		I <sub>DM</sub>	-3.6	Α
Power Dissipation	T <sub>a</sub> =25°C		300	mW
	Derate above 25°C	P <sub>D</sub>	2.4	mW/°C
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C
Typical Thermal Resistance - Junction to Ambient <sup>(Note 3)</sup>		R <sub>0JA</sub>	417	°C/W





## **Electrical Characteristics** (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-0.4	-0.69	-1.2	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-0.9A	-	110	130	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-0.6A	-	130	160	
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-0.4A	-	160	210	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	Igss	V <sub>GS=+</sub> 12V, V <sub>DS</sub> =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	$Q_g$	V <sub>DS</sub> =-10V, I <sub>D</sub> =-0.9A, V <sub>GS</sub> =-4.5V <sup>(Note 1,2)</sup>	-	5.4	-	nC
Gate-Source Charge	$Q_{gs}$		-	0.7	-	
Gate-Drain Charge	$Q_{gd}$		-	1.4	-	
Input Capacitance	Ciss	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V, f=1.0MHZ	-	416	-	pF
Output Capacitance	Coss		-	43	-	
Reverse Transfer Capacitance	Crss	I=I.UIVIMZ	-	32	-	
Switching						
Turn-On Delay Time	td <sub>(on)</sub>	\/ 10\/   0.0A	-	4	-	ns
Turn-On Rise Time	tr	$V_{DD}$ =-10V, $I_{D}$ =-0.9A, $V_{GS}$ =-4.5V, $R_{G}$ =6 $\Omega$ (Note 1,2)	-	27	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	78	-	
Turn-Off Fall Time	tf	MG=012(Note 1,2)	-	45	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	Is		-	-	-0.4	Α
Diode Forward Voltage	V <sub>SD</sub>	Is=-1A, V <sub>GS</sub> =0V	-	-0.8	-1.2	V

#### NOTES:

- 1. Pulse width<a></a>300us, Duty cycle<a></a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





#### **TYPICAL CHARACTERISTIC CURVES**

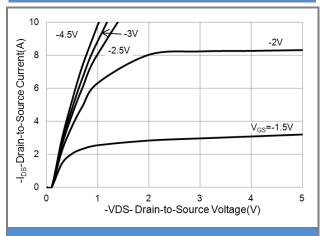


Fig.1 On-Region Characteristics

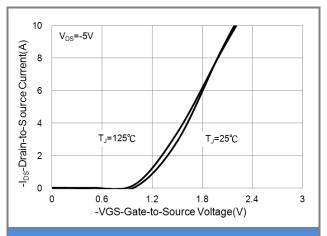


Fig.2 Transfer Characteristics

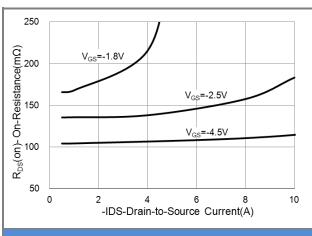


Fig.3 On-Resistance vs. Drain Current

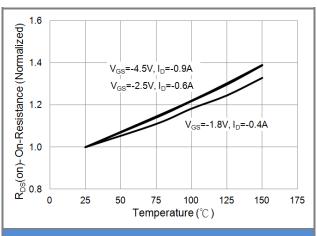


Fig.4 On-Resistance vs. Junction temperature

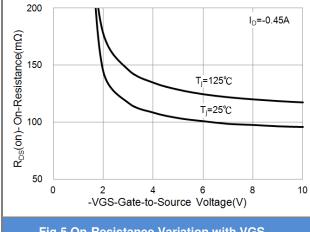
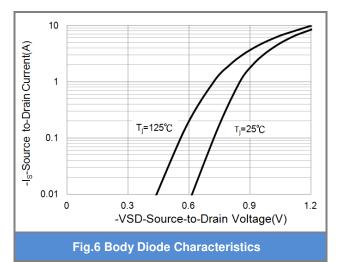


Fig.5 On-Resistance Variation with VGS.







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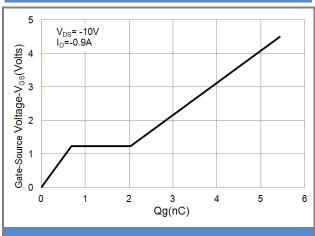


Fig.7 Gate-Charge Characteristics

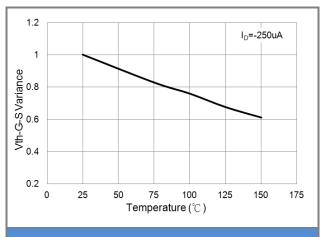


Fig.8 Threshold Voltage Variation with Temperature

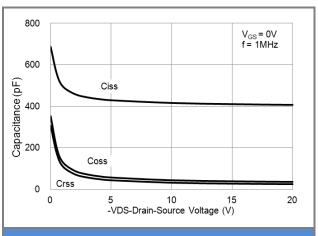


Fig.9 Capacitance vs. Drain-Source Voltage

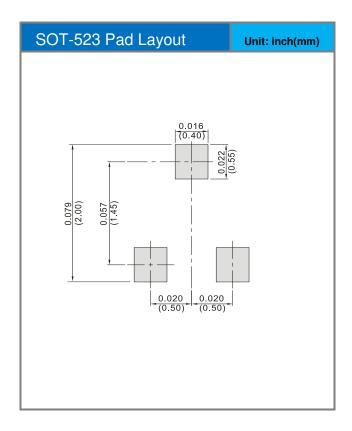




### PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJE8401_R1_00001	SOT-523	4K pcs / 7" reel	E01	Halogen free RoHS compliant

### **MOUNTING PAD LAYOUT**







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