

## 1A, 60V Trench Schottky Surface Mount Rectifier

### FEATURES

- AEC-Q101 qualified
- Patented Trench Schottky technology
- Very low profile - typical height of 0.68mm
- Low power loss, high efficiency
- Ideal for automated placement
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

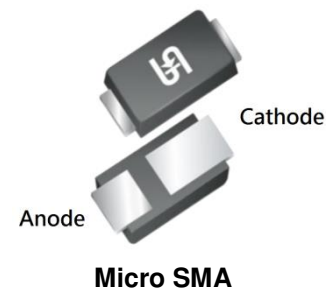
### APPLICATIONS

- Switching mode power supply (SMPS)
- Adapters
- Lighting application
- On-board DC/DC converter
- Automotive

### MECHANICAL DATA

- Case: Micro SMA
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.006g (approximately)

KEY PARAMETERS		
PARAMETER	VALUE	UNIT
$I_F$	1	A
$V_{RRM}$	60	V
$I_{FSM}$	25	A
$T_{J\ MAX}$	175	°C
Package	Micro SMA	
Configuration	Single die	



ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)			
PARAMETER	SYMBOL	TSU1M60H	UNIT
Marking code on the device		Z2	
Repetitive peak reverse voltage	$V_{RRM}$	60	V
Reverse voltage, total rms value	$V_{R(RMS)}$	42	V
Forward current	$I_F$	1	A
Surge peak forward current single half sine-wave superimposed on rated load	$t = 8.3\text{ms}$	25	A
	$t = 1.0\text{ms}$	80	A
Junction temperature	$T_J$	- 55 to +175	°C
Storage temperature	$T_{STG}$	- 55 to +175	°C

<b>THERMAL PERFORMANCE</b>			
<b>PARAMETER</b>	<b>SYMBOL</b>	<b>TYP</b>	<b>UNIT</b>
Junction-to-lead thermal resistance	$R_{\theta JL}$	20	$^{\circ}\text{C/W}$
Junction-to-ambient thermal resistance	$R_{\theta JA}$	73	$^{\circ}\text{C/W}$
Junction-to-case thermal resistance	$R_{\theta JC}$	30	$^{\circ}\text{C/W}$

**Thermal Performance Note:** Units mounted on PCB (5mm x 5mm Cu pad test board)

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_A = 25^{\circ}\text{C}$ unless otherwise noted)					
<b>PARAMETER</b>	<b>CONDITIONS</b>	<b>SYMBOL</b>	<b>TYP</b>	<b>MAX</b>	<b>UNIT</b>
Forward voltage <sup>(1)</sup>	$I_F = 0.5\text{A}, T_J = 25^{\circ}\text{C}$	$V_F$	0.52	-	V
	$I_F = 1.0\text{A}, T_J = 25^{\circ}\text{C}$		0.59	0.65	V
	$I_F = 0.5\text{A}, T_J = 125^{\circ}\text{C}$		0.42	-	V
	$I_F = 1.0\text{A}, T_J = 125^{\circ}\text{C}$		0.54	0.60	V
Reverse current @ rated $V_R$ <sup>(2)</sup>	$T_J = 25^{\circ}\text{C}$	$I_R$	-	80	$\mu\text{A}$
	$T_J = 125^{\circ}\text{C}$		-	3	mA
Junction capacitance	1MHz, $V_R = 4.0\text{V}$	$C_J$	95	-	pF

**Notes:**

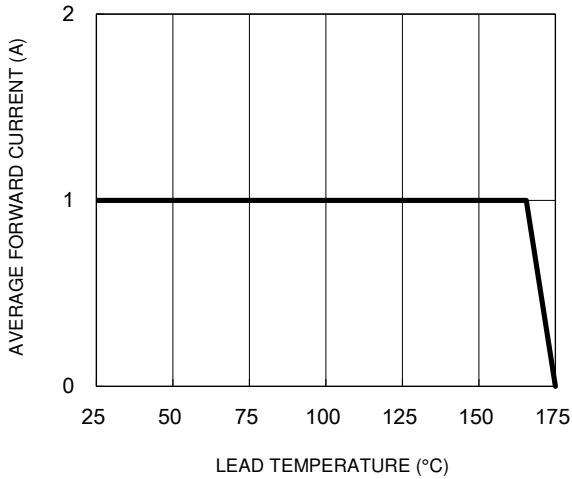
1. Pulse test with PW = 0.3ms
2. Pulse test with PW = 30ms

<b>ORDERING INFORMATION</b>		
<b>ORDERING CODE</b>	<b>PACKAGE</b>	<b>PACKING</b>
TSU1M60H	Micro SMA	12,000 / Tape & Reel

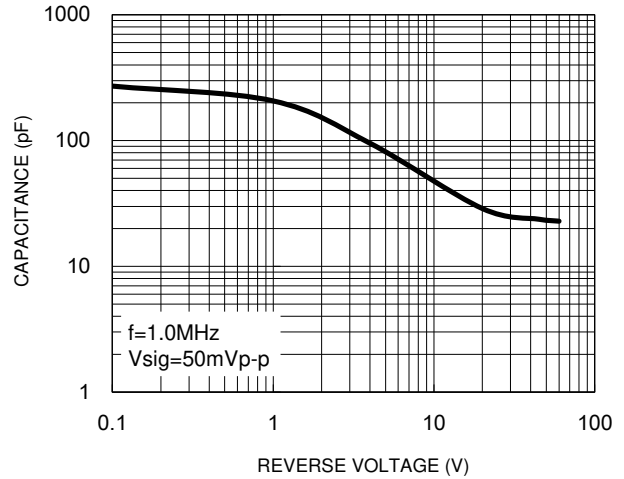
**CHARACTERISTICS CURVES**

( $T_A = 25^\circ\text{C}$  unless otherwise noted)

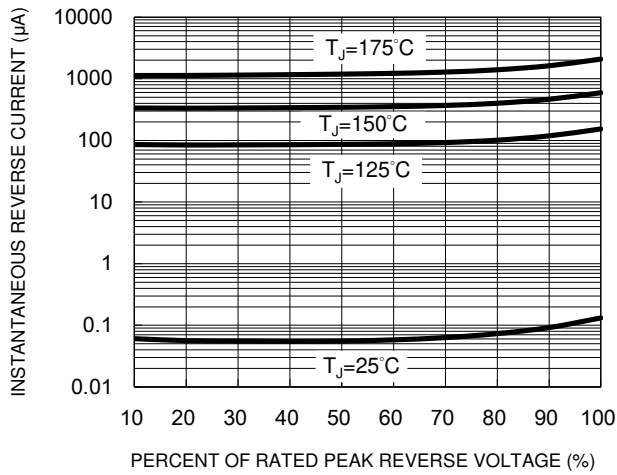
**Fig.1 Forward Current Derating Curve**



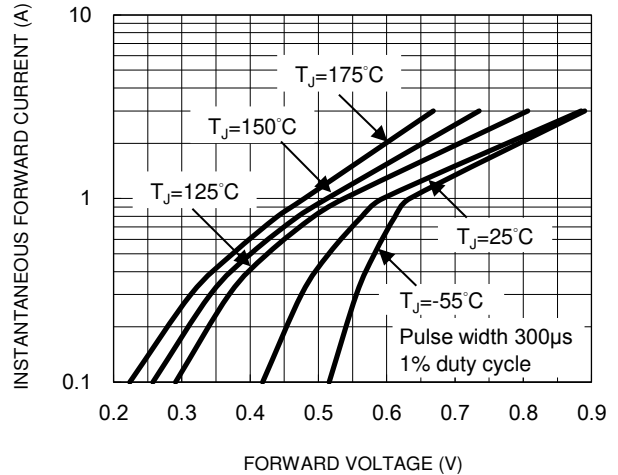
**Fig.2 Typical Junction Capacitance**



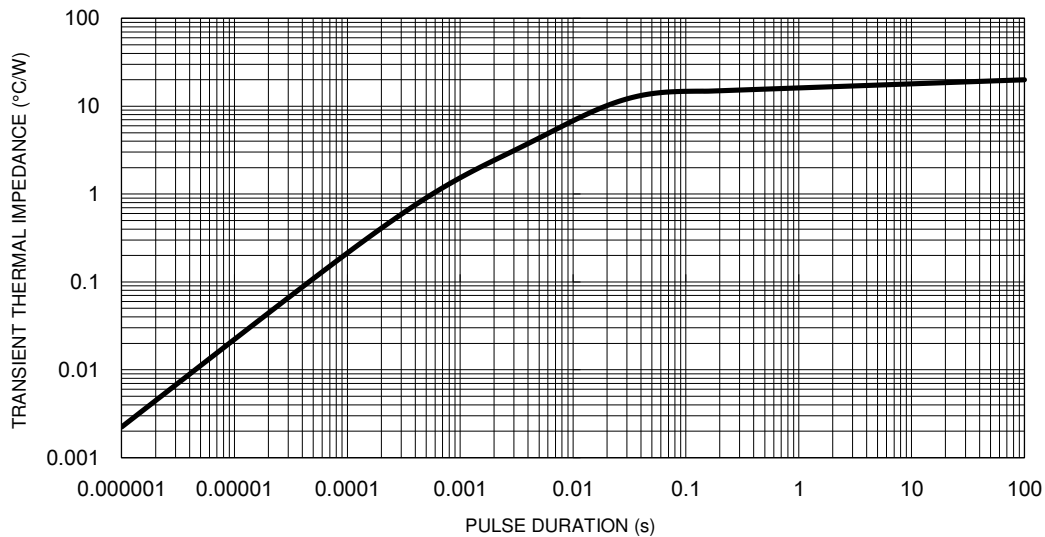
**Fig.3 Typical Reverse Characteristics**



**Fig.4 Typical Forward Characteristics**

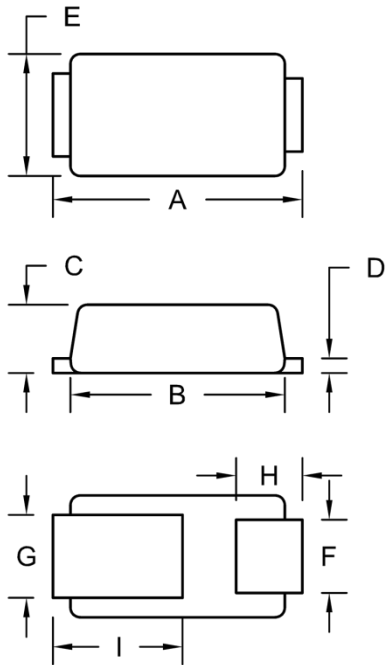


**Fig.5 Typical Transient Thermal Impedance**



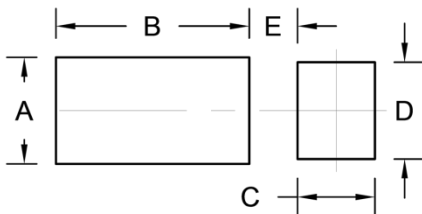
**PACKAGE OUTLINE DIMENSIONS**

Micro SMA



DIM.	Unit (mm)		Unit (inch)	
	Min.	Max.	Min.	Max.
A	2.30	2.70	0.091	0.106
B	2.10	2.30	0.083	0.091
C	0.63	0.73	0.025	0.029
D	0.10	0.20	0.004	0.008
E	1.15	1.35	0.045	0.053
F	0.65	0.85	0.026	0.034
G	0.75	0.95	0.030	0.037
H	0.55	0.75	0.022	0.030
I	1.10	1.50	0.043	0.059

**SUGGESTED PAD LAYOUT**



Symbol	Unit (mm)	Unit (inch)
A	1.10	0.043
B	2.00	0.079
C	0.80	0.031
D	1.00	0.039
E	0.50	0.020

**Notes:**

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

**MARKING DIAGRAM**



P/N = Marking Code  
YW = Date Code

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