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November 2013

FQPF16N25C N-Channel QFET[®] MOSFET 250 V, 15.6 A, 270 mΩ

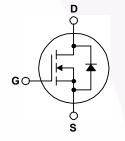
Features

- + 15.6 A, 250 V, $R_{DS(on)}$ = 270 m Ω (Max) @ V_{GS} = 10 V, I_{D} = 7.8 A
- Low Gate Charge (Typ. 41 nC)
- Low Crss (Typ. 68 pF)
- 100% Avalanche Tested

Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol		Parameter		FQPF16N25C	Unit
V _{DSS}	Drain to Source Voltage			250	V
ID	Drain Current	- Continuous (T _C = 25 ^o C)		15.6 *	A
	Drain Current	- Continuous (T _C = 100 ^o C)		9.8 *	A
DM	Drain Current	- Pulsed	(Note 1)	62.4 *	A
V _{GSS}	Gate to Source Voltage			± 30	V
AS	Single Pulsed Avalanche Energy		(Note 2)	410	mJ
AR	Avalanche Current		(Note 1)	15.6	A
AR	Repetitive Avalanche Energy		(Note 1)	13.9	mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	5.5	V/ns
P _D	Devues Dissinction	$(T_{C} = 25^{\circ}C)$		43	W
	Power Dissipation	- Derate Above 25°C		0.34	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C
ΓL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			300	°C

*Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	FQPF16N25C	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case, Max	2.89	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max	62.5	°C/W

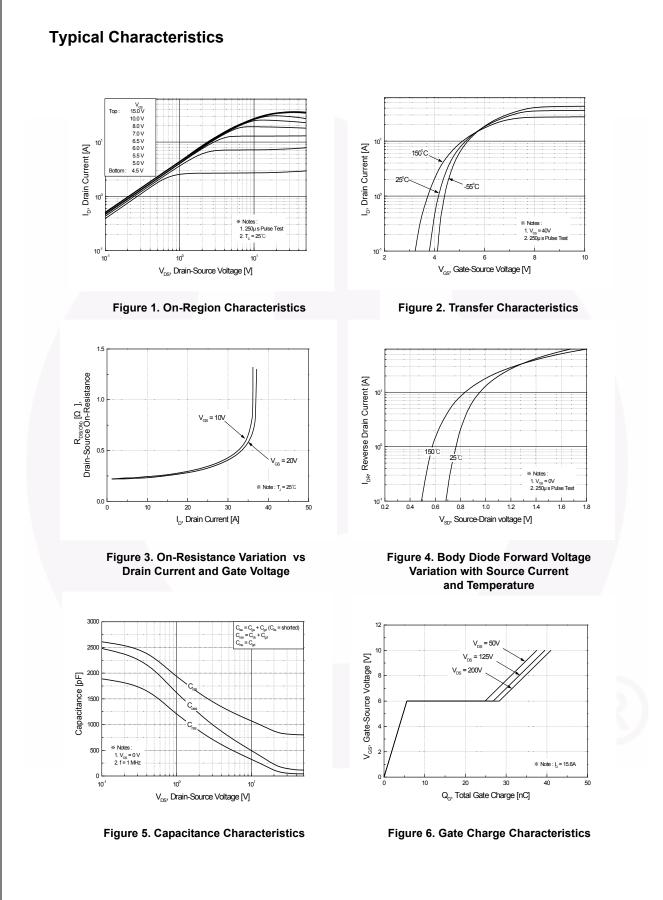
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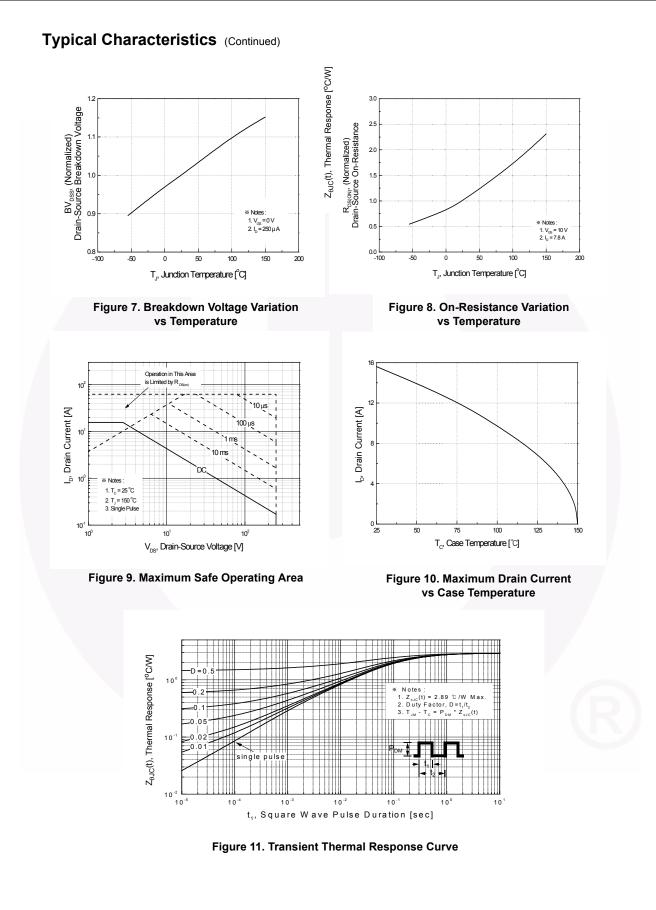
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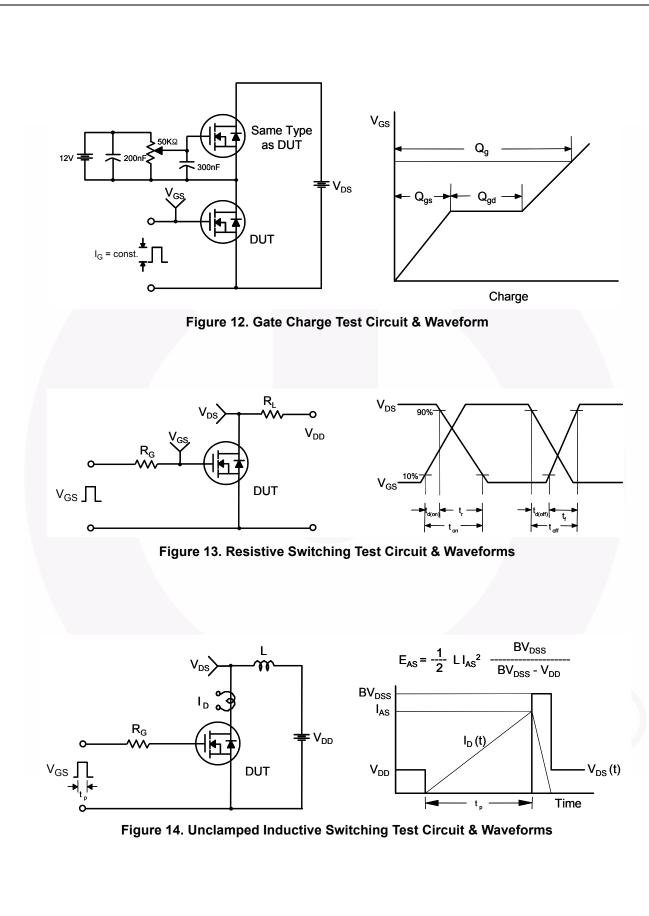
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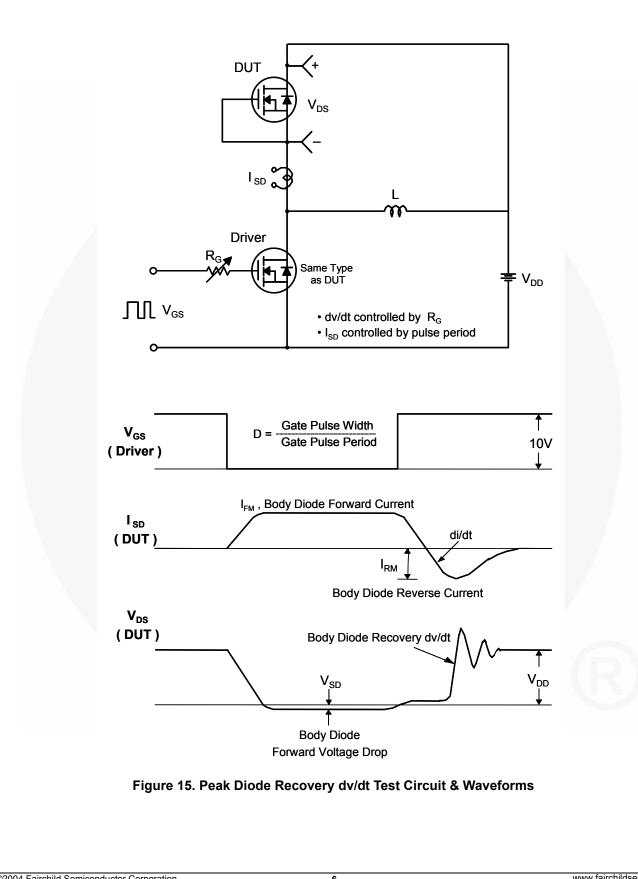
Device MarkingDeviceFQPF16N25CFQPF16N25C		Package	Ree	l Size	Tape Widt	h Qu	uantity	
		TO-220F T		ube	N/A	50	50 units	
Electri	cal Charact	eristics T _C = 25°C ui	nless otherwise noted.					
Symbol	Р	arameter	Test Condition	IS	Min.	Тур.	Max.	Uni
Off Cha	aracteristics							
BV _{DSS}	1	eakdown Voltage	V _{GS} = 0 V, I _D = 250 μA		250			V
ΔBV_{DSS}		age Temperature Coeffi-	$I_D = 250 \ \mu$ A, Referenced to 25°C			0.31		V/°C
$/ \Delta T_J$	cient					0.31		V/°C
I _{DSS}	Zero Gate Volta	ge Drain Current	V _{DS} = 250 V, V _{GS} = 0 V				10	μA
			$V_{DS} = 200 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$				100	μA
I _{GSSF}		age Current, Forward	$V_{GS} = 30 V, V_{DS} = 0 V$				100	nA
I _{GSSR}	Gate-Body Leak	age Current, Reverse	$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$	_			-100	nA
On Cha	racteristics							
V _{GS(th)}	Gate Threshold	Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$		2.0		4.0	V
R _{DS(on)}	Static Drain-Sou On-Resistance	rce	V _{GS} = 10 V, I _D = 7.8 A			0.22	0.27	Ω
9 _{FS}	Forward Transco	onductance	V _{DS} = 40 V, I _D = 7.8 A			10.5		S
C _{iss}	Input Capacitan	ce	V _{DS} = 25 V, V _{GS} = 0 V,			830	1080	pF
C _{oss}	Output Capacita		f = 1.0 MHz			170	220	pF
C _{rss}	Reverse Transfer Capacitance					68	89	pF
Switchi	ing Character	istics						
t _{d(on)}	Turn-On Delay	lime .	V _{DD} = 125 V, I _D = 15.6 A			15	40	ns
t _r	Turn-On Rise Ti	me	$V_{DD} = 125 \text{ V}, \text{ I}_{D} = 15.0 \text{ A}$ $V_{GS} = 10 \text{ V}, \text{ R}_{G} = 25 \Omega$,		130	270	ns
t _{d(off)}	Turn-Off Delay 1	Time	VGS 10 V, NG 20 32			135	280	ns
t _f	Turn-Off Fall Tin	ne		(Note 4)		105	220	ns
Qg	Total Gate Char	ge	V _{DS} = 200 V, I _D = 15.6 A	,		41	53.5	nC
Q _{gs}	Gate-Source Ch	arge	$V_{GS} = 10 V$			5.6		nC
Q _{gd}	Gate-Drain Char	rge		(Note 4)		22.7		nC
Droin S	ouroo Diodo	Characteristics and	I Maximum Ratings					
I _S			•				15.6	A
I _{SM}	Maximum Continuous Drain-Source Diode Forward Current Maximum Pulsed Drain-Source Diode Forward Current					62.4	A	
V _{SD}		ode Forward Voltage	$V_{GS} = 0 V, I_S = 15.6 A$				1.5	V
t _{rr}	Reverse Recover	0	$V_{GS} = 0 V, I_S = 15.6 A$ $V_{GS} = 0 V, I_S = 15.6 A,$			260		ns
Q _{rr}	Reverse Recover	· ·	$V_{GS} = 0$ V, $I_{S} = 15.6$ A, dI _F / dt = 100 A/µs			2.47		μC
otes:			1			- . , , , , , ,, ,,,,,,,,,,,,,,,		μυ

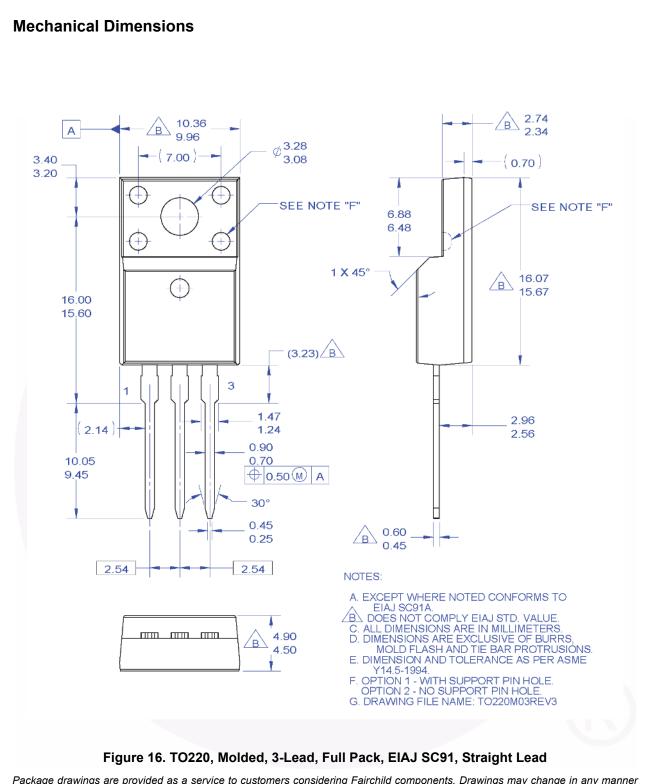
4. Essentially independent of operating temperature











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