

Intel® Xeon® Processor E5540

Processor Specifications:

sSpec Number: SLBF6	Package Type: LGA1366
CPU Speed: 2.53 GHz	Manufacturing Technology: 45 nm
PCG:	Core Stepping: D0
Bus Speed: 5.86 GT/s	CPUID String: 106A5h
Bus/Core Ratio: 19	Thermal Design Power: 80W
L3 Cache Size: 8 MB	Thermal Specification: 76°C
L3 Cache Speed: 2.53 GHz	VID Voltage Range: 0.75V -1.35V

Product Documentation:

<http://support.intel.com/support/processors/sb/CS-028967.htm>

Product Order Codes:

Box Order Code: BX80602E5540

OEM Order Code: AT80602000789AA

Supported Features:

- Quad Core
- Enhanced Intel Speedstep® Technology
- Hyper-Threading Technology ¹
- Intel® EM64T ²
- Intel® Virtualization Technology
- Enhanced Halt State (C1E)
- Execute Disable Bit ³

Notes:

1/1/1/2 are the available frequency bins (+133 MHz) of Intel® Turbo Boost Technology that are available for 4, 3, 2, 1 active cores respectively.

Legal Disclaimers:

1 Hyper-Threading Technology requires a computer system with an Intel® Pentium® 4 processor or Intel® Core™ i7 processor supporting HT Technology and a Hyper-Threading Technology enabled chipset, BIOS and operating system. Performance will vary depending on the specific hardware and software you use. See <http://www.intel.com/info/hyperthreading/> for more information including details on which processors support HT Technology.

2 Intel® EM64T requires a computer system with a processor, chipset, BIOS, operating system, device drivers and applications enabled for Intel EM64T. Processor will not operate (including 32-bit operation) without an Intel EM64T-enabled BIOS. Performance will vary depending on your hardware and software configurations. See <http://www.intel.com/info/em64t> for more information including details on which processors support Intel® EM64T or consult with your system vendor for more information.

3 Enabling Execute Disable Bit functionality requires a PC with a processor with Execute Disable Bit capability and a supporting operating system. Check with your PC manufacturer on whether your system delivers Execute Disable Bit functionality.