



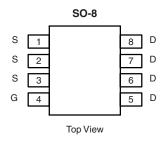
# N-Channel 80-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)		
80	0.0165 at V <sub>GS</sub> = 10 V	9.5		
	0.022 at V <sub>GS</sub> = 6.0 V	8.3		

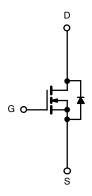
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si4896DY-T1-E3 (Lead (Pb)-free) Si4896DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

Davamatav	$T_A = 25  ^{\circ}C$ , unles		10 s	Ctoody Ctoto	Unit
Parameter		Symbol	10 S	Steady State	Unit
Drain-Source Voltage		$V_{DS}$	80		V
Gate-Source Voltage		$V_{GS}$	± 20		v
O	T <sub>A</sub> = 25 °C	ı	9.5	6.7	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C	l <sub>D</sub>	7.6	5.4	
Pulsed Drain Current		I <sub>DM</sub>	50		Α
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	40		
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	2.8	1.4	
Manifestore David Disability and	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3.1	1.56	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	' D	2.0	1.0	VV
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 t	o 150	°C

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Mariana kanting ta Andria 18	t ≤ 10 s	R <sub>thJA</sub>	33	40	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	' ¹thJA	65	80	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	17	21	

a. Surface Mounted on 1" x 1" FR4 board.

# Vishay Siliconix



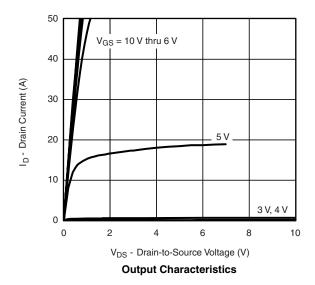
<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0			V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Cata Valtana Busin Comunit		V <sub>DS</sub> = 64 V, V <sub>GS</sub> = 0 V	<sub>S</sub> = 0 V		1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 64 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			5	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α	
	В	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A		0.0135	0.0165	0	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 6.0 \text{ V}, I_D = 8.0 \text{ A}$		0.0175	0.022	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 A		25		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = 2.8 A, V <sub>GS</sub> = 0 V		0.75	1.1	V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			34	41		
Gate-Source Charge	$Q_{gs}$ $V_{DS} = 40 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		7.5		nC		
Gate-Drain Charge	$Q_{gd}$			11.0			
Gate Resistance	$R_g$		0.2	0.85	1.2	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			17	25		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 40 V, $R_L$ = 40 $\Omega$		11	17		
Turn-Off Delay Time	$t_{d(off)}$ $I_D \cong 1.0 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$			40	60	ns	
Fall Time	t <sub>f</sub>			31	45		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	$I_F = 2.8 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$		45	75		

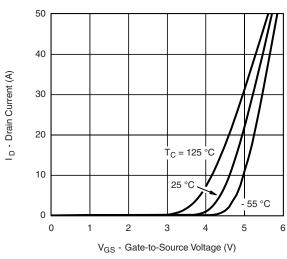
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

# TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

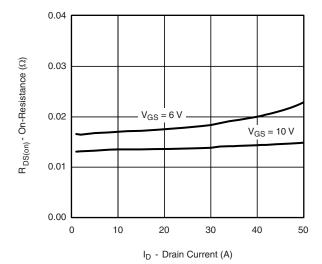




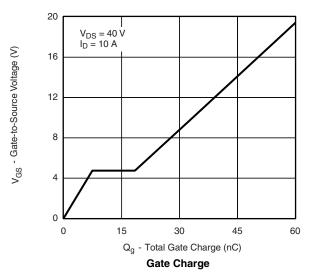
**Transfer Characteristics** 

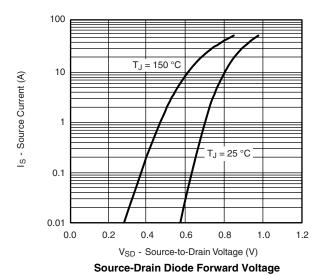


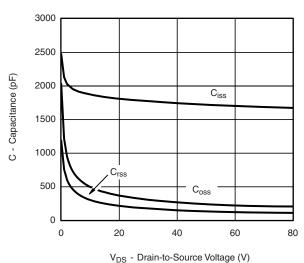
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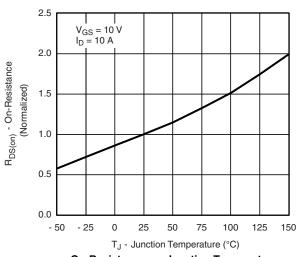
#### On-Resistance vs. Drain Current



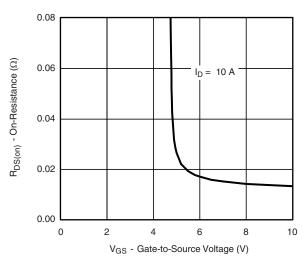




Capacitance



On-Resistance vs. Junction Temperature

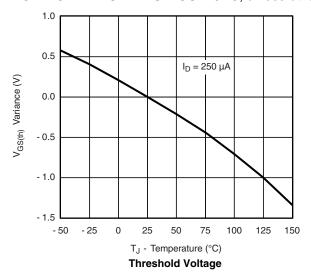


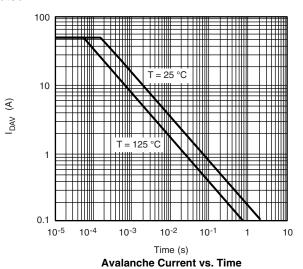
On-Resistance vs. Gate-to-Source Voltage

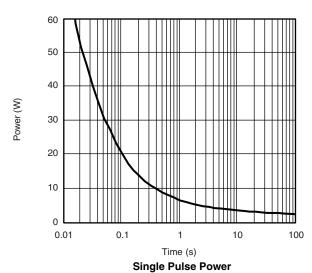
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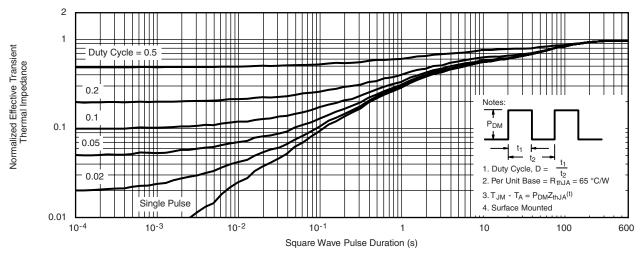
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# TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





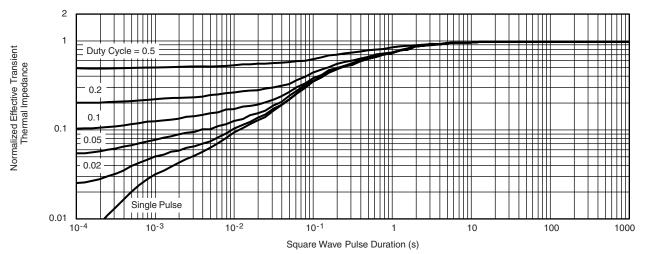




Normalized Thermal Transient Impedance, Junction-to-Ambient



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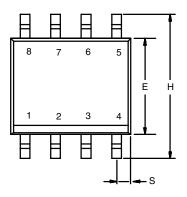


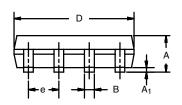
Normalized Thermal Transient Impedance, Junction-to-Foot

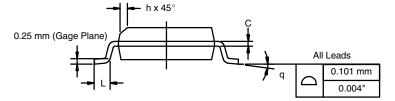
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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INCHES			
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050 BSC			
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I. 11-Sep-06						

DWG: 5498

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# APPLICATION NOTE



### **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)

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