LINEAR SYSTEMS

Improved Standard Products[®]

FEATURES							
REVERSE BREAKDOWN VOLTAGE	BV _R ≥ -30V						
REVERSE CAPACITANCE	C _{rss} ≤ 2.0pF						
ABSOLUTE MAXIMUM RATINGS ¹							
@ 25 °C (unless otherwise stated)							
Maximum Temperatures							
Storage Temperature	-55 to +150 °C						
Operating Junction Temperature -55 to -							
Maximum Power Dissipation ²							
Continuous Power Dissipation	300mW						
Continuous Power Dissipation Maximum Currents	300mW						
Continuous Power Dissipation Maximum Currents Forward Current	300mW 10mA						

PAD-DFN SERIES

MINIATURE/NON MAGNETIC 8-PIN DFN PACKAGE LOW LEAKAGE DIODE



COMMON ELECTRICAL CHARACTERISTICS @ 25 °C (unless otherwise stated)

SYMBOL	CHARACTERISTIC	MIN	TYP	MAX	UNITS	CONDITIONS
BV _R	Reverse Breakdown Voltage	-30			V	I _R = -1μΑ
VF	Forward Voltage		0.8	1.5		$I_F = 5mA$
Crss	Total Reverse Capacitance		1.5		pF	$V_{\rm R} = -5V, f = 1$ MHz

SPECIFIC ELECTRICAL CHARACTERISTICS @ 25 °C (unless otherwise stated)

SYMBOL	CHARACTERISTIC			UNITS	CONDITIONS
I _R	Maximum Reverse Leakage Current	PAD5DFN	-5	pА	V _R = -20V
		PAD50DFN	-50		

Figure 1. Operational Amplifier Protection

Input Differential Voltage limited to 0.8V (typ) by DFNs D_1 and D_2 . Common Mode Input voltage limited by DFNs D_3 and D_4 to ±15V.

Figure 2. Sample and Hold Circuit

Typical Sample and Hold circuit with clipping. DFN diodes reduce offset voltages fed capacitively from the JFET switch gate.



FIGURE 2





<u>NOTES</u>

- 1. Absolute maximum ratings are limiting values above which serviceability may be impaired.
- 2. Derate 2.8 mW/ºC above 25ºC
- The PAD type number denotes its maximum reverse current value in pico amperes. Devices with I_R values intermediate to those shown are available upon request.

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