





N-CHANNEL ENHANCEMENT MODE MOSFET

Features

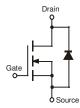
- Low Gate Charge
- Low R_{DS(ON)}:
 - 30mΩ @V_{GS} = 10V
 - $40m\Omega$ @V_{GS} = 4.5V
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

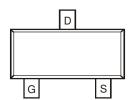
- Case: SC59
- Case Material Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (e3)
- Terminal Connections: See Diagram
- Weight: 0.014 grams (Approximate)







Equivalent Circuit



Pin Configuration

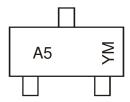
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3033LSN-7	SC59	3000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- ${\it 4.} \ {\it For packaging details, go to our website at http://www.diodes.com/products/packages.html.}$

Marking Information



A5 = Product Type Marking Code

YM = Date Code Marking Y = Year (ex: D = 2016)

M = Month (ex: 9 = September)

Date Code Kev

Year	2007	^		2016	2017	20	18	2019	2020	20	21	2022
Code	U	^	-	D	Е	F	=	G	Н		I	J
Month	Jan	Feb	Mar	Apr	Mav	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	30	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current (Note 5)	$T_A = +25$ °C $T_A = +70$ °C	I _D	6 5	Α
Pulsed Drain Current (Note 6)		I _{DM}	24	Α
Body-Diode Continuous Current (Note 5)		Is	2.25	Α

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_{D}	1.4	W
Thermal Resistance, Junction to Ambient (Note 5) t ≤10s	$R_{ heta JA}$	90	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

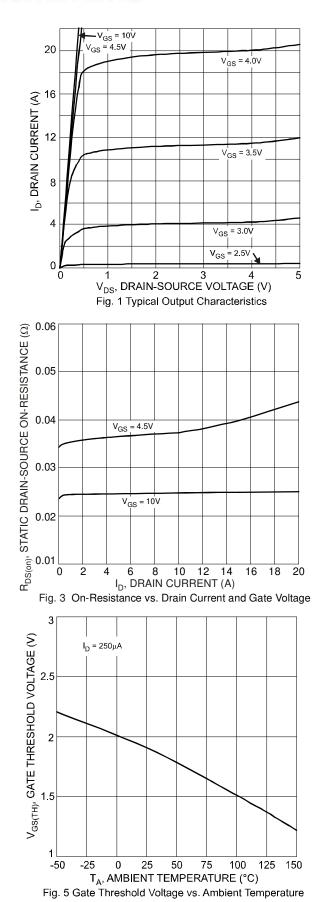
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

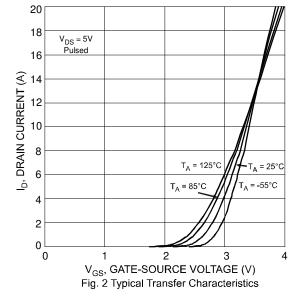
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	BV _{DSS}	30	_		V	$I_D = 250 \mu A, V_{GS} = 0 V$
Zero Gate Voltage Drain Current $T_J = +7$ $T_J = +7$	Ince	_	_	1 5	μА	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Body Leakage Current	I _{GSS}	_	_	±100	nA	$V_{DS} = 0V, V_{GS} = \pm 20V$
Gate Threshold Voltage	V _{GS(TH)}	1.0	_	2.1	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance (Note 7)	R _{DS(ON)}	_	25 36	30 40	mΩ	$V_{GS} = 10V$, $I_D = 6A$ $V_{GS} = 4.5V$, $I_D = 5A$
Forward Transconductance (Note 7)	g _{FS}	_	5	_	S	$V_{DS} = 10V, I_D = 8A$
Diode Forward Voltage (Note 7)	V _{SD}	_	0.7	1.1	V	I _S = 2.25A, V _{GS} = 0V
DYNAMIC PARAMETERS (Note 8)						
Total Gate Charge	Q_{g}	_	10.5		nC	$V_{GS} = 5V, V_{DS} = 15V, I_D = 6A$
Gate-Source Charge	Q_{gs}	_	3.8		nC	$V_{GS} = 10V, V_{DS} = 15V, I_D = 6A$
Gate-Drain Charge	Q_{gd}	_	2.9	_	nC	$V_{GS} = 10V, V_{DS} = 15V, I_D = 6A$
Turn-On Delay Time	t _{D(ON)}	_	11	_	ns	
Turn-On Rise Time	t _R	_	7	_	ns	$V_{DD} = 15V, V_{GS} = 10V,$
Turn-Off Delay Time	t _{D(OFF)}	_	63	_	ns	$R_D = 1.8\Omega$, $R_G = 6\Omega$
Turn-Off Fall Time	t _F	_	30	_	ns	
Input Capacitance	C _{iss}	_	755	_	pF	
Output Capacitance	Coss	_	136	_	pF	V _{DS} = 10V, V _{GS} = 0V -f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	108	_	pF	71 – 1.0IVII IZ

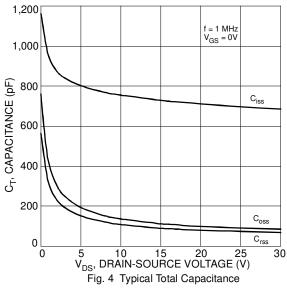
Notes:

- 5. Device mounted on 1"x1", FR-4 PC board with 2 oz. Copper and test pulse width t ≤10s.
- 6. Repetitive Rating, pulse width limited by junction temperature.
- 7. Test pulse width t = 300ms.
 8. Guaranteed by design. Not subject to production testing.









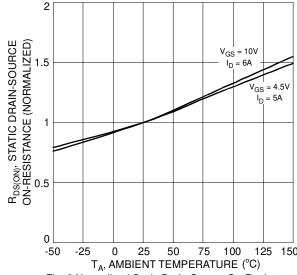
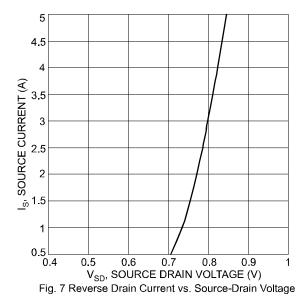


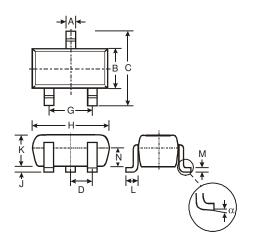
Fig. 6 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature





Package Outline Dimensions

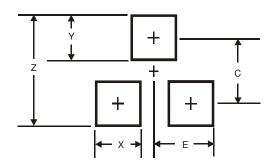
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SC59						
Dim	Min	Max	Тур				
Α	0.35	0.50	0.38				
В	1.50	1.70	1.60				
C	2.70	3.00	2.80				
D	-	-	0.95				
G	-	-	1.90				
Н	2.90	3.10	3.00				
J	0.013	0.10	0.05				
K	1.00	1.30	1.10				
L	0.35	0.55	0.40				
M	0.10	0.20	0.15				
N	0.70	0.80	0.75				
α	0°	8°	-				
All Dimensions in mm							

Suggested Pad Layout

 $\label{please} Please see \ http://www.diodes.com/package-outlines.html for the latest version.$



Dimensions	Value (in mm)
Z	3.4
Х	0.8
Υ	1.0
С	2.4
E	1.35



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