# General Purpose Transistor Array One Differentially Connected Pair and Three Isolated Transistor Arrays

The MC3346 is designed for general purpose, low power applications for consumer and industrial designs.

Symbol

VCEO

VCBO

 $V_{\mathsf{EB}}$ 

Vcio

 $I_{C}$ 

PD

ΤA

T<sub>sta</sub>

Value

15

20

5.0

20

50

1.2

10

-40 to +85

-65 to +150

Unit

Vdc

Vdc

Vdc

Vdc

mAdc

W mW/°C

> °C ℃

• Guaranteed Base-Emitter Voltage Matching

**MAXIMUM RATINGS** 

Collector-Emitter Voltage

Collector-Substrate Voltage

Collector Current - Continuous

**Operating Temperature Range** 

Storage Temperature Range

Total Power Dissipation @ T<sub>A</sub> = 25°C

Collector-Base Voltage

Derate above 25°C

Emitter-Base Voltage

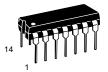
Rating

- Operating Current Range Specified: 10 µA to 10 mA
- Five General Purpose Transistors in One Package



## GENERAL PURPOSE TRANSISTOR ARRAY

SEMICONDUCTOR TECHNICAL DATA



P SUFFIX PLASTIC PACKAGE CASE 646

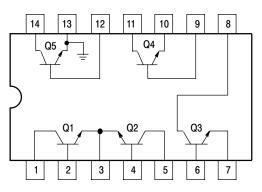


D SUFFIX PLASTIC PACKAGE CASE 751A (SO–14)

#### ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC3346D	T <sub>A</sub> = –40° to +85°C	SO-14
MC3356P		Plastic DIP

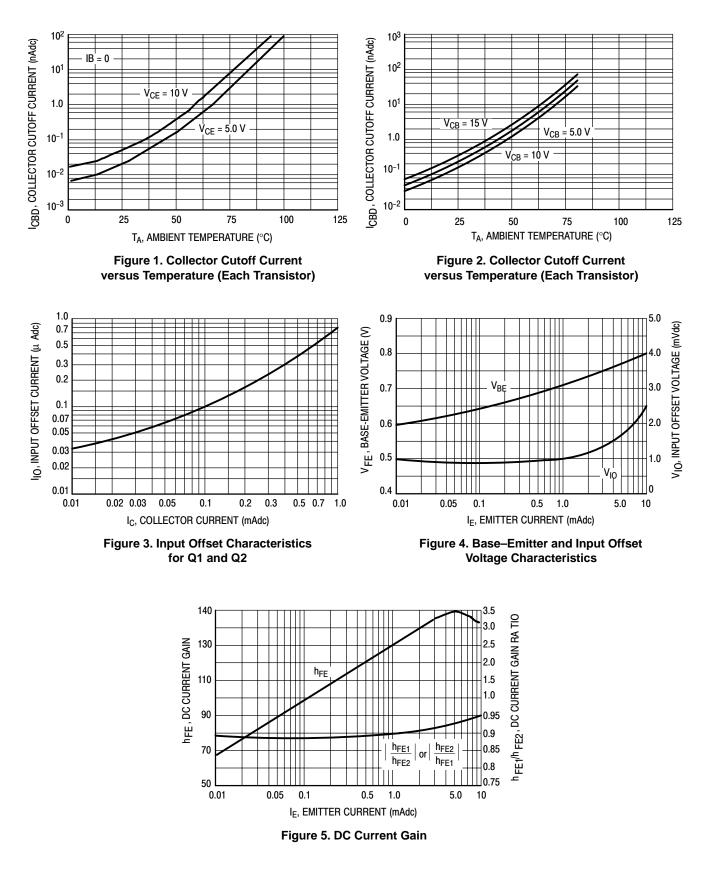
#### **PIN CONNECTIONS**



Pin 13 is connected to substrate and must remain at the lowest circuit potential.

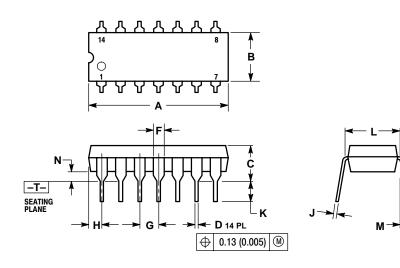
## Semiconductor Components Industries, LLC, 2001 May, 2001 – Rev. 1

Characteristics	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTICS		T			1
Collector–Base Breakdown Voltage $(I_C = 10 \ \mu Adc)$	V <sub>(BR)CBO</sub>	20	60	-	Vdc
Collector–Emitter Breakdown Voltage $(I_C = 1.0 \text{ mAdc})$	V <sub>(BR)CEO</sub>	15	-	-	Vdc
Collector–Substrate Breakdown Voltage $(I_{C} = 10 \ \mu A)$	V <sub>(BR)CIO</sub>	20	60	_	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 10 μAdc)	V <sub>(BR)EBO</sub>	5.0	7.0	_	Vdc
Collector–Base Cutoff Current ( $V_{CB} = 10 Vdc, I_E = 0$ )	I <sub>CBO</sub>	-	-	40	nAdc
DC Current Gain $(I_C = 10 \text{ mAdc}, V_{CE} = 3.0 \text{ Vdc})$ $(I_C = 1.0 \text{ mAdc}, V_{CE} = 3.0 \text{ Vdc})$ $(I_C = 10 \mu\text{Adc}, V_{CE} = 3.0 \text{ Vdc})$	h <sub>FE</sub>	- 40 -	140 130 60		-
Base-Emitter Voltage ( $V_{CE} = 3.0 \text{ Vdc}, I_E = 1.0 \text{ mAdc}$ ) ( $V_{CE} = 3.0 \text{ Vdc}, I_E = 10 \text{ mAdc}$ )	V <sub>BE</sub>		0.72 0.8	-	Vdc
Input Offset Current for Matched Pair Q1 and Q2 $(V_{CE} = 3.0 \text{ Vdc}, I_C = 1.0 \text{ mAdc})$	$ I_{1O1} - I_{1O2} $	-	0.3	2.0	μAdo
Magnitude of Input Offset Voltage ( $V_{CE} = 3.0 \text{ Vdc}, I_C = 1.0 \text{ mAdc}$ )	-	-	0.5	5.0	mVdd
Temperature Coefficient of Base–Emitter Voltage $(V_{CE} = 3.0 \text{ Vdc}, I_C = 1.0 \text{ mAdc})$	ΔV <sub>BE</sub> DT	-	-1.9	-	mV/°0
Temperature Coefficient	$\frac{ \Delta V_{IO} }{D_{T}}$	-	1.0	-	μV/° <b>(</b>
Collector–Emitter Cutoff Current ( $V_{CE} = 10 \text{ Vdc}, I_B = 0$ )	ICEO	-	-	0.5	μAdo
DYNAMIC CHARACTERISTICS					
Low Frequency Noise Figure ( $V_{CE} = 3.0 \text{ Vdc}, I_C = 100 \mu \text{Adc}, R_S = 1.0 \text{ k}\Omega, f = 1.0 \text{ kHz}$ )	NF	-	3.25	_	dB
Forward Current Transfer Ratio ( $V_{CE} = 3.0 \text{ Vdc}, I_C = 1.0 \text{ mAdc}, f = 1.0 \text{ kHz}$ )	h <sub>FE</sub>	-	110	-	-
Short Circuit Input Impedance ( $V_{CE} = 3.0 \text{ Vdc}, I_C = 1.0 \text{ mAdc}$ )	h <sub>ie</sub>	-	3.5	-	kΩ
Open Circuit Output Impedance ( $V_{CE} = 3.0 \text{ Vdc}, I_C = 1.0 \text{ mAdc}$ )	h <sub>oe</sub>	-	15.6	-	μmho
Reverse Voltage Transfer Ratio $(V_{CE} = 3.0 \text{ Vdc}, I_C = 1.0 \text{ mAdc})$	h <sub>re</sub>	-	1.8	-	x10 <sup></sup>
Forward Transfer Admittance $(V_{CE} = 3.0 \text{ Vdc}, I_C = 1.0 \text{ mAdc}, f = 1.0 \text{ MHz})$	Уfe	-	31–j1.5	-	-
Input Admittance ( $V_{CE} = 3.0 \text{ Vdc}, I_C = 1.0 \text{ mAdc}, f = 1.0 \text{ MHz}$ )	У <sub>іе</sub>	-	0.3 + j0.04	-	-
Output Admittance ( $V_{CE}$ = 3.0 Vdc, I <sub>C</sub> = 1.0 mAdc, f = 1.0 MHz)	У <sub>ое</sub>	-	0.001 + j0.03	-	_
Current–Gain – Bandwidth Product ( $V_{CE}$ = 3.0 Vdc, I <sub>C</sub> = 3.0 mAdc)	f <sub>T</sub>	300	550	-	MHz
Emitter–Base Capacitance ( $V_{EB} = 3.0 \text{ Vdc}, I_E = 0$ )	C <sub>eb</sub>	-	0.6	-	pF
Collector–Base Capacitance $(V_{CB} = 3.0 \text{ Vdc}, I_C = 0)$	C <sub>cb</sub>	-	0.58	-	pF
Collector–Substrate Capacitance ( $V_{CS} = 3.0 \text{ Vdc}, I_C = 0$ )	C <sub>CI</sub>	_	2.8	-	pF



### PACKAGE DIMENSIONS

**P SUFFIX** PLASTIC PACKAGE CASE 646-06 **ISSUE M** 

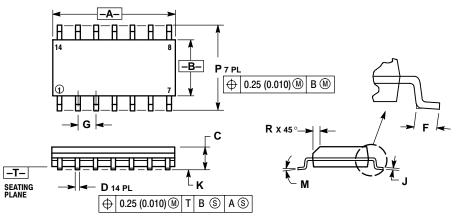


NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL. 4. DIMENSION B DOES NOT INCLUDE MOLD FLASH. 5. ROUNDED CORNERS OPTIONAL. INCHES MILLIMETERS TOTAL MAY MIN MAX

		INC	HES	MILLIMETERS		
	DIM	MIN	MAX	MIN	MAX	
	Α	0.715	0.770	18.16	18.80	
	В	0.240	0.260	6.10	6.60	
	С	0.145	0.185	3.69	4.69	
	D	0.015	0.021	0.38	0.53	
	F	0.040	0.070	1.02	1.78	
	G	0.100 BSC		2.54 BSC		
	Н	0.052	0.095	1.32	2.41	
[	J	0.008	0.015	0.20	0.38	
	Κ	0.115	0.135	2.92	3.43	
	L	0.290	0.310	7.37	7.87	
	Μ		10°		10°	
	Ν	0.015	0.039	0.38	1.01	

### PACKAGE DIMENSIONS

**D SUFFIX** PLASTIC PACKAGE CASE 751A-03 (SO-8) ISSUE F



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: MILLIMETER. 3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION. 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE. 5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION. SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIN	IETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	8.55	8.75	0.337	0.344	
В	3.80	4.00	0.150	0.157	
С	1.35	1.75	0.054	0.068	
D	0.35	0.49	0.014	0.019	
F	0.40	1.25	0.016	0.049	
G	1.27 BSC		0.050 BSC		
J	0.19	0.25	0.008	0.009	
Κ	0.10	0.25	0.004	0.009	
Μ	0 °	7°	0 °	7°	
Р	5.80	6.20	0.228	0.244	
R	0.25	0.50	0.010	0.019	

# Notes

# Notes

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