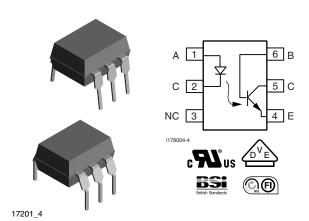




## Optocoupler, Phototransistor Output, with Base Connection



#### **DESCRIPTION**

The CNY17 is an optically coupled pair consisting of a gallium arsenide infrared emitting diode optically coupled to a silicon NPN phototransitor.

Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output.

The CNY17 can be used to replace relays and transformers in many digital interface applications, as well as analog applications such as CRT modulation.

#### **FEATURES**

- Isolation test voltage 5000 V<sub>RMS</sub>
- · Long term stability
- Industry standard dual-in-line package
- V<sub>IORM</sub> = 850 V
- Material categorization:
   For definitions of compliance please see www.vishay.com/doc?99912







#### **AGENCY APPROVALS**

- UL file no. E52744
- cUL tested to CSA 22.2 bulletin 5A
- DIN EN 60747-5-5 (0884-5) available with option 1
- BSI IEC 60950-1:2006 IEC 60065
- FIMKO
- CQC

ORDERING I	NFORMATI	ON						
С	N	Y	1 PART NUMBER	7 -	#			2-6, 400 mil
AGENCY CERTIF	TED/PACKAGE	:		(	CTR (%)			
cUL, VDE, BSI, FIMKO, CQC		40 to 80	63 to 125		100 to 200	160 to 320		
DIP-6			CNY17-1.	CNY17-2.		CNY17-3.	CNY17	'-4.
DIP-6, 400 mil			CNY17G-1	CNY17G-2	(	CNY17G-3	CNY17	G-4

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT					
INPUT									
Reverse voltage		V <sub>R</sub>	5	V					
Forward current		I <sub>F</sub>	60	mA					
Surge current	t ≤ 10 μs	I <sub>FSM</sub>	3	Α					
Power dissipation		P <sub>diss</sub>	100	mW					
OUTPUT	·								
Collector emitter breakdown voltage		BV <sub>CEO</sub>	70	V					
Emitter base breakdown voltage		BV <sub>EBO</sub>	7	V					
Collector current		Ic	50	mA					
Collector current	t < 1 ms	Ic	100	mA					
Power dissipation		P <sub>diss</sub>	150	mW					



# **CNY17., CNY17G**

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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
COUPLER								
Isolation test voltage between emitter and detector referred to climate DIN 50014, part 2, Nov. 74	t = 1 min	V <sub>ISO</sub>	5000	$V_{RMS}$				
Creepage distance (CNY17.)			≥ 7	mm				
Clearance distance (CNY17.)			≥ 7	mm				
Creepage distance (CNY17G)			≥ 8	mm				
Clearance distance (CNY17G)			≥ 8	mm				
Isolation thickness between emitter and detector			≥ 0.4	mm				
Comparative tracking index per DIN IEC 112/VDE 0303, part 1			250					
Isolation resistance	$V_{IO} = 500 \text{ V}, T_{amb} = 25 ^{\circ}\text{C}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω				
isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω				
Storage temperature		T <sub>stg</sub>	- 55 to + 125	°C				
Operating temperature		T <sub>amb</sub>	- 55 to + 100	°C				
Soldering temperature (1)	max. 10 s, dip soldering: distance to seating plane ≥ 1.5 mm	T <sub>sld</sub>	260	°C				

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to wave profile for soldering conditions for through hole devices.

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT									
Forward voltage	$I_F = 60 \text{ mA}$		V <sub>F</sub>		1.25	1.65	V		
Breakdown voltage	I <sub>R</sub> = 10 μA		$V_{BR}$	6			V		
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>		0.01	10	μA		
Capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		Co		25		pF		
Thermal resistance			R <sub>th</sub>		750		K/W		
ОИТРИТ									
Collector emitter capacitance	V <sub>CE</sub> = 5 V, f = 1 MHz		C <sub>CE</sub>		5.2		pF		
Collector base capacitance	V <sub>CB</sub> = 5 V, f = 1 MHz		C <sub>CB</sub>		6.5		pF		
Emitter base capacitance	V <sub>EB</sub> = 5 V, f = 1 MHz		C <sub>EB</sub>		7.5		pF		
Thermal resistance			R <sub>th</sub>		500		K/W		
COUPLER									
Collector emitter, saturation voltage	$I_F = 10 \text{ mA}, I_C = 2.5 \text{ mA}$		V <sub>CEsat</sub>		0.25	0.4	V		
Coupling capacitance			C <sub>C</sub>		0.6		pF		
	V <sub>CE</sub> = 10 V	CNY17-1	I <sub>CEO</sub>		2	50	nA		
Collector emitter legicage gurrent		CNY17-2	I <sub>CEO</sub>		2	50	nA		
Collector emitter, leakage current		CNY17-3	I <sub>CEO</sub>		5	100	nA		
		CNY17-4	I <sub>CEO</sub>		5	100	nA		

#### Note

Minimum and maximum values were tested requierements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.



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# **CNY17., CNY17G**

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# Vishay Semiconductors

CURRENT TRANSFER RATIO									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
I <sub>C</sub> /I <sub>F</sub>		CNY17-1	CTR	40		80	%		
	V - 5 V I - 10 mA	CNY17-2	CTR	63		125	%		
	$V_{CE} = 5 \text{ V}, I_{F} = 10 \text{ mA}$	CNY17-3	CTR	100		200	%		
		CNY17-4	CTR	160		320	%		
		CNY17-1	CTR	13	30		%		
	V 5V 1 4 m A	CNY17-2	CTR	22	45		%		
	$V_{CE} = 5 \text{ V}, I_{F} = 1 \text{ mA}$	CNV17-3	CTR	3/1	70		0/6		

CNY17-3

CNY17-4

**CTR** 

CTR

34

70

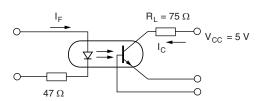
90

#### Note

• Current transfer ratio and collector-emitter leakage current by dash number (T<sub>amb</sub> °C).

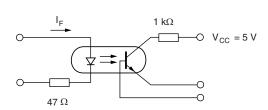
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
LINEAR OPERATION (WIT	THOUT SATURATION)	_			1	•	
Turn-on time	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 75 \Omega$		t <sub>on</sub>		3		μs
Rise time	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 75 \Omega$		t <sub>r</sub>		2		μs
Turn-off time	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 75 \Omega$		t <sub>off</sub>		2.3		μs
Fall time	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 75 \Omega$		t <sub>f</sub>		2		μs
Cut-off frequency	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 75 \Omega$		f <sub>CO</sub>		250		kHz
SWITCHING OPERATION	(WITH SATURATION)	•				•	
Turn-on time	I <sub>F</sub> = 20 mA	CNY17-1	t <sub>on</sub>		3		μs
	1. 101	CNY17-2	t <sub>on</sub>		4.2		μs
	$I_F = 10 \text{ mA}$	CNY17-3	t <sub>on</sub>		4.2		μs
	I <sub>F</sub> = 5 mA	CNY17-4	t <sub>on</sub>		6		μs
	I <sub>F</sub> = 20 mA	CNY17-1	t <sub>r</sub>		2		μs
Discouling a		CNY17-2	t <sub>r</sub>		3		μs
Rise time	$I_F = 10 \text{ mA}$	CNY17-3	t <sub>r</sub>		3		μs
	I <sub>F</sub> = 5 mA	CNY17-4	t <sub>r</sub>		4.6		μs
	I <sub>F</sub> = 20 mA	CNY17-1	t <sub>off</sub>		18		μs
T (1) (1)	1. 101	CNY17-2	t <sub>off</sub>		23		μs
Turn-off time	$I_F = 10 \text{ mA}$	CNY17-3	t <sub>off</sub>		23		μs
	I <sub>F</sub> = 5 mA	CNY17-4	t <sub>off</sub>		25		μs
Fall time	I <sub>F</sub> = 20 mA	CNY17-1	t <sub>f</sub>		11		μs
	1 10 1	CNY17-2	t <sub>f</sub>		14		μs
	$I_F = 10 \text{ mA}$	CNY17-3	t <sub>f</sub>		14		μs
	I <sub>E</sub> = 5 mA	CNY17-4	t <sub>f</sub>		15		μs

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)



icny17\_01

Fig. 1 - Linear Operation (without Saturation)



icny17\_02

Fig. 1 - Switching Operation (with Saturation)

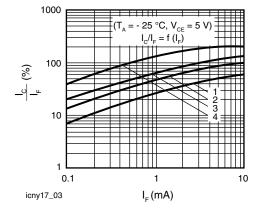


Fig. 2 - Current Transfer Ratio vs. Diode Current

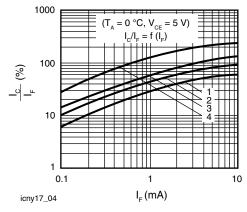


Fig. 3 - Current Transfer Ratio vs. Diode Current

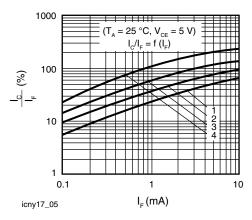


Fig. 4 - Current Transfer Ratio vs. Diode Current

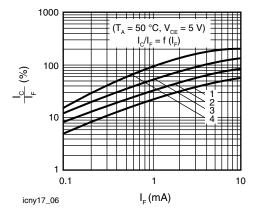


Fig. 5 - Current Transfer Ratio vs. Diode Current



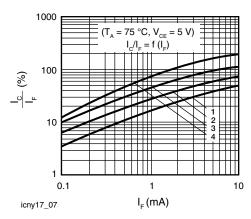


Fig. 6 - Current Transfer Ratio vs. Diode Current

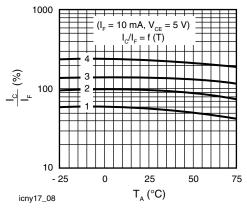


Fig. 7 - Current Transfer Ratio (CTR) vs. Temperature

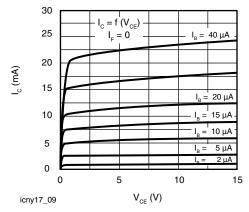


Fig. 8 - Transistor Characteristics

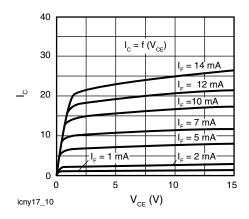


Fig. 9 - Output Characteristics

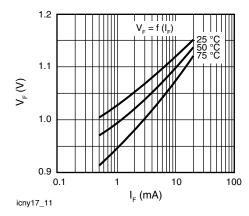


Fig. 10 - Forward Voltage vs. Forward Current

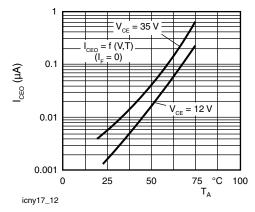


Fig. 11 - Leakage Current vs. Ambient Temperature



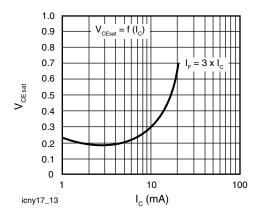


Fig. 12 - Saturation Voltage vs. Collector Current and Modulation Depth CNY17-1

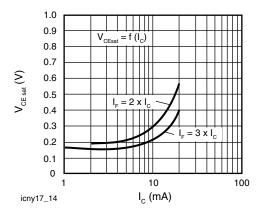


Fig. 13 - Saturation Voltage vs. Collector Current and Modulation Depth CNY17-2

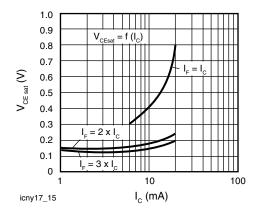


Fig. 14 - Saturation Voltage vs.
Collector Current and Modulation Depth CNY17-3

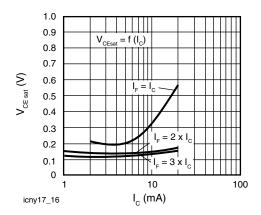


Fig. 15 - Saturation Voltage vs. Collector Current and Modulation Depth CNY17-4

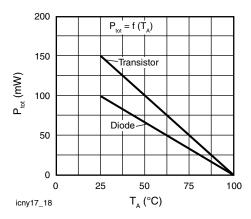
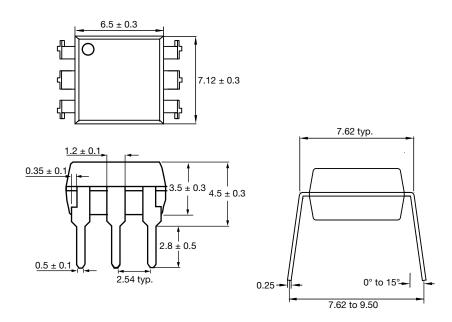


Fig. 16 - Permissible Power Dissipation for Transistor and Diode

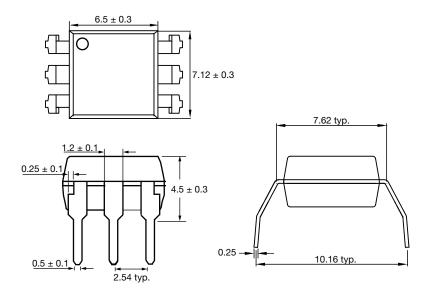


### **PACKAGE DIMENSIONS** in millimeters

DIP-6



### DIP-6, 400 mil



### **PACKAGE MARKING**





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