



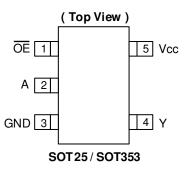


SINGLE BUFFER GATE WITH 3-STATE OUTPUT

Description

The 74LVC1G125Q is an automotive-compliant single, non-inverting buffer/bus driver with a 3-state output. The output enters a high-impedance state when a HIGH level is applied to the output enable $\overline{(OE)}$ pin. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V, allowing this device to be used in a mixed-voltage environment. The device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output preventing damaging current backflow when the device is powered down.

Pin Assignments



Features

- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Wide Supply Voltage Range from 1.65V to 5.5V
- ±24mA Output Drive at 3.3V
- CMOS Low Power Consumption
- Ioff Supports Partial Power-Down Mode Operation
- Inputs Accept up to 5.5V Regardless of Vcc Level
- ESD Protection Tested per AEC-Q100
- Exceeds 2000V Human Body Model (AEC-Q100-002)
- Exceeds 1000V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74LVC1G125Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Applications

- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide Array of Products such as:
 - Automotive Applications within Grade 1 Temperature Range
 - Industrial Computing/Controls/Automation
 - High Reliability Networking/Communications
 - Industrial/Agricultural Equipment

Notes:

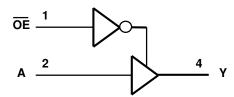
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Pin Descriptions

Pin Name	Description	
ŌĒ	Output Enable Active LOW	
Α	Data Input	
GND	Ground	
Υ	Data Output	
Vcc	Supply Voltage	

Logic Diagram



Function Table

Inp	Output	
ŌĒ	Α	Υ
L	Н	Н
L	L	L
Н	Х	Z

Absolute Maximum Ratings (Notes 4 & 5)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
V _{CC}	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to V _{CC} + 0.5	V
I _{IK}	Input Clamp Current V _I < 0	-50	mA
Іок	Output Clamp Current	-50	mA
lo	Continuous Output Current	±50	mA
Icc, Ign	Continuous Current Through Vcc or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C

Notes:

^{4.} Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should be

within recommend values.

5. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.



Recommended Operating Conditions (Note 6)

Symbol		Parameter	Min	Max	Unit
V/	Operating Voltage	Operating	1.65	5.5	V
Vcc	Operating voltage	Data Retention Only	1.5	_	V
		Vcc = 1.65V to 1.95V	$0.65 \times V_{CC}$	_	
V/	High Loyal Input Valtage	Vcc = 2.3V to 2.7V	1.7	_	V
VIH	High-Level Input Voltage	V _{CC} = 3V to 3.6V	2	_	V
		V _{CC} = 4.5V to 5.5V	$0.7 \times V_{CC}$	_	
		V _{CC} = 1.65V to 1.95V	_	$0.35 \times V_{CC}$	
.,	Law Law Law A Waltana	V _{CC} = 2.3V to 2.7V	_	0.7	.,
VIL	Low-Level Input Voltage	V _{CC} = 3V to 3.6V	_	0.8	V
		Vcc = 4.5V to 5.5V	_	0.3 × Vcc	
Vı	Input Voltage		0	5.5	V
Vo	Output Voltage	0	Vcc	V	
		Vcc = 1.65V	_	-4	
		Vcc = 2.3V	_	-8	mA
la	High-Level Output Current	Vcc = 2.7V	_	-12	
I _{OH}	High-Level Output Current	V 0V	_	-16	IIIA
		V _{CC} = 3V	_	-24	
		Vcc = 4.5V	_	-32	
		$V_{CC} = 1.65V$	_	4	
		V _{CC} = 2.3V	_	8	
lol	Low-Level Output Current	Vcc = 2.7V	_	12	mA
IOL	Low-Level Output Current	V 0V	_	16	IIIA
		Vcc = 3V	_	24	
		Vcc = 4.5V	_	32	
		$V_{CC} = 1.8V \pm 0.15V, 2.5V \pm 0.2V$	_	20	
$\Delta t/\Delta V$	Input Transition Rise or Fall Rate	$V_{CC} = 3.3V \pm 0.3V$	_	10	ns/V
		$V_{CC} = 5V \pm 0.5V$	_	5	
TA	Operating Free-Air Temperature	_	-40	+125	°C

Note: 6. Unused inputs should be held at V_{CC} or Ground.



Electrical Characteristics (All typical values are at $V_{CC} = 3.3V$, $T_A = +25$ °C)

Symbol	Parameter	er Test Conditions		Voc	-40°	C to +125°	°C	Unit		
Symbol	Parameter			Vcc	Min	Тур	Max	Unit		
			Іон = -100μΑ	1.65V to 5.5V	Vcc - 0.1	_	_			
			I _{OH} = -4mA	1.65V	0.95	_	_			
Vон	High Level Output Voltage	VI = VIH OR VIL	Iон = -8mA	2.3V	17	_	_	V		
VOH	Inigit Level Output voltage	VI = VIH OF VIL	I _{OH} = -12mA	2.7V	1.9	_	_	v		
			I _{OH} = -24mA	3V	2.0	_	_			
			$I_{OH} = -32mA$	4.5V	3.4	_	_			
					I _{OL} = 100μA	1.65V to 5.5V	_	_	0.1	
		ow Level Output Voltage $V_I = V_{IH}$ or V_{IL}	I _{OL} = 4mA	1.65V	_	_	0.7	V		
.,			IoL = 8mA	2.3V	_	_	0.45			
Vol	Low Level Output Voltage		I _{OL} = 12mA	2.7V	_	_	0.6			
			I _{OL} = 24mA	3V	_	_	0.8			
			I _{OL} = 32mA	4.5V	_	_	0.8			
lı	Input Current	Vı = 5.5V or GN	ID	0 to 5.5V	_	±0.1	±1	μΑ		
loff	Power Down Leakage Current	$V_1 \text{ or } V_0 = 5.5V$	V_I or $V_O = 5.5V$		_	_	<u>+</u> 2	μA		
loz	Z-State Leakage Current	V _O = Ground to 5.5V		3.6V	_	_	<u>+</u> 2	μA		
Icc	Supply Current	V _I = 5.5V or GND, I _O = 0		5.5V	_	0.1	4	μA		
ΔΙα	Additional Supply Current	One input at V _{CC} – 0.6V Other inputs at V _{CC} or GND		3V to 5.5V	_	_	500	μA		
Cı	Input Capacitance	VI = VCC or GN	D	3.3V	_	3.5	_	pF		

Package Characteristics

Symbol	Parameter	Package	Test Conditions	Min	Тур	Max	Unit
0	Thermal Resistance	SOT25	Note 7	_	184	_	0044
θја	Junction-to-Ambient	SOT353	Note 7	_	385	_	°C/W
0	Thermal Resistance	SOT25	Note 7	_	62	_	0044
θJC	Junction-to-Case	SOT353	Note 7	_	164	_	°C/W

Note: 7. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



Switching Characteristics

Figure 1 Typical Values at $T_A = +25^{\circ}C$ and nominal voltages 1.8V, 2.5V, 2.7V, 3.3V, and 5.0V.

Davameter	From	То		7	T _A = -40°C to +125°C		Unit							
Parameter Input Ou	Output	V cc	Min	Тур	Max	Unit								
			1.8V ± 0.15V	1.0	3.3	10.5								
			2.5V ± 0.2V	0.5	2.2	7.0								
t _{PD}	Α	Y	2.7V	0.5	2.5	7.0	ns							
		-	3.3V ± 0.3V	0.5	2.1	6.0								
			5.0V ± 0.5V	0.5	1.7	5.5								
	ŌĒ		1.8V ± 0.15V	1.0	4.1	12.0								
			2.5V ± 0.2V	0.5	2.8	8.5								
ten		ŌE	ŌĒ	ŌE	ŌE	ŌĒ	ŌĒ	ŌĒ	ŌĒ	Y	2.7V	2.7V 0.5 3.3	8.5	ns
			3.3V ± 0.3V	0.5	2.4	7.0								
			5.0V ± 0.5V	0.5	2.1	6.5								
			1.8V ± 0.15V	1.0	4.3	12.0								
	tois OE							2.5V ± 0.2V	0.5	2.7	6.5			
tois		Υ	2.7V	0.5	3.0	6.5	ns							
			3.3V ± 0.3V	0.5	3.1	6.5								
			5.0V ± 0.5V	0.5	2.2	5.5								

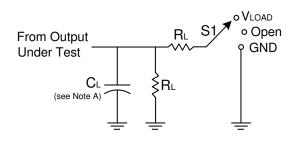
Operating Characteristics

 $T_A = +25$ °C

	Parameter		Test Conditions	Vcc = 1.8V Typ	Vcc = 2.5V Typ	Vcc = 3.3V Typ	Vcc = 5V Typ	Unit
Coo	Power Dissipation	Outputs Enabled	f = 10MHz	19	19	19	21	ρF
GPD	Capacitance Outputs Disabled	I = IUIVIMZ	2	2	3	4	рΓ	

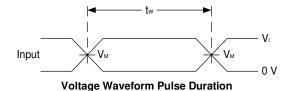


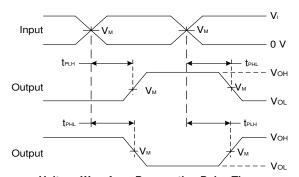
Parameter Measurement Information



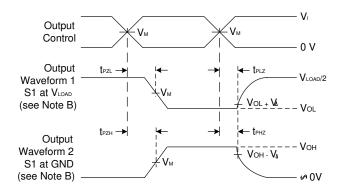
TEST	S1
tplH/tpHL	Open
tplz/tpzl	VLOAD
t _{PHZ} /t _{PZH}	GND

V	Inputs		V V		0	D.	V Δ
V _{CC}	Vı	t _R /t _F	V _M	V _{LOAD}	CL	RL	VΔ
1.8V±0.15V	Vcc	≤2ns	Vcc/2	2 × Vcc	30pF	1kΩ	0.15V
2.5V±0.2V	Vcc	≤2ns	Vcc/2	2 × Vcc	30pF	500Ω	0.15V
2.7V	2.7V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
3.3V±0.3V	3V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
5V±0.5V	V _{CC}	≤2.5ns	V _{CC} /2	$2\times V_{CC}$	50pF	500Ω	0.3V





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs



Voltage Waveform Enable and Disable Times Low and High Level Enabling

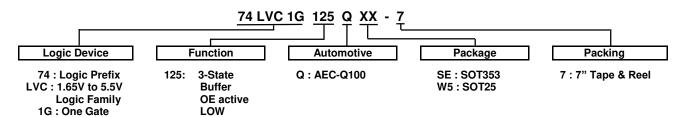
Figure 1. Load Circuit and Voltage Waveforms

Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 10MHz.
- C. Inputs are measured separately one transition per measurement.
- D. t_{PLZ} and t_{PHZ} are the same as t_{DIS} .
- E. t_{PZL} and t_{PZH} are the same as t_{EN}.
- F. t_{PLH} and t_{PHL} are the same as t_{PD}.



Ordering Information (Note 8)



Part Number	Package	Package	Package	7" Tape and Reel		
Fait Number	Code	(Notes 9 & 10)	Size	Quantity	Part Number Suffix	
74LVC1G125QSE-7	SE	SOT353	2.15mm × 2.1mm × 1.1mm 0.65mm lead pitch	3000/Tape & Reel	-7	
74LVC1G125QW5-7	W5	SOT25	3.0 mm \times 2.8 mm \times 1.2 mm 0.95 mm lead pitch	3000/Tape & Reel	-7	

Notes: 8. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

9. Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.

10. The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

Marking Information

(Top View)

YWX

XXX: Identification Code Year 0~9

Week: A~Z 1~26 week

a~z 27~52 week z represents week 52 and 53

X : A~ Z: Internal Code

SOT25 / SOT353

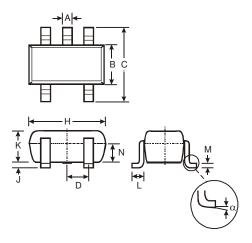
Part Number	Package	Identification Code
74LVC1G125QW5-7	SOT25	UYQ
74LVC1G125QSE-7	SOT353	UYQ



Package Outline Dimensions

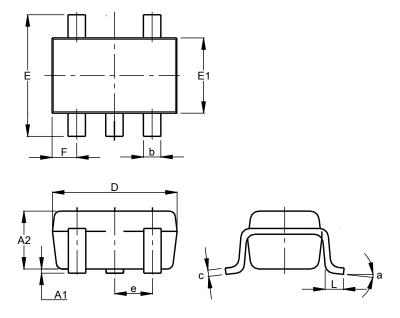
 $Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

(1) Package Type: SOT25



SOT25					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
C	2.70	3.00	2.80		
D	-	-	0.95		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
М	0.10	0.20	0.15		
N	0.70	0.80	0.75		
α	0°	8°	-		
All Dimensions in mm					

(2) Package Type: SOT353



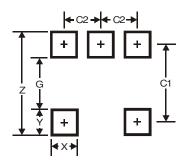
SOT353					
Dim	Min	Max	Тур		
A 1	0.00	0.10	0.05		
A2	0.90	1.00	0.95		
b	0.10	0.30	0.25		
С	0.10	0.22	0.11		
D	1.80	2.20	2.15		
Е	2.00	2.20	2.10		
E1	1.15	1.35	1.30		
е	0.650 BSC				
F	0.40	0.45	0.425		
L	0.25	0.40	0.30		
а	0°	8°			
All Dimensions in mm					



Suggested Pad Layout

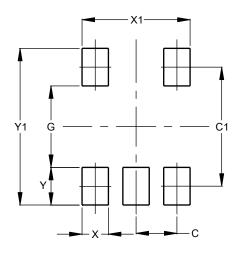
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



Dimensions	Value	
Z	3.20	
G	1.60	
Х	0.55	
Υ	0.80	
C1	2.40	
C2	0.95	

(2) Package Type: SOT353



Dimensions	Value (in mm)	
С	0.650	
C1	1.900	
G	1.300	
Х	0.420	
X1	1.720	
Υ	0.600	
V1	2 500	

Mechanical Data

SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208
- Weight: 0.0158 grams (Approximate)

SOT353

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.0064 grams (Approximate)



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