N-Channel Power MOSFET

600V, 38A, 99mΩ

FEATURES

- Super-Junction technology
- High performance, small $R_{DS(ON)}$*Q_g figure of merit (FOM)
- High ruggedness performance
- 100% UIS and R_g tested
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21

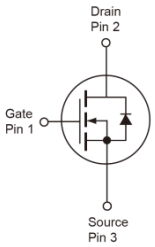
KEY PERFORMANCE PARAMETERS		
PARAMETER	**VALUE**	**UNIT**
V_{DS}	600	V
$R_{DS(on)}$ (max)	99	mΩ
Q_g	62	nC

APPLICATIONS

- PFC stage
- Server/Telecom Power
- Charging Station
- Inverter
- Power Supply

Pb-Free RoHS COMPLIANT HALOGEN FREE

ITO-220S



ABSOLUTE MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)				
PARAMETER		**SYMBOL**	**LIMIT**	**UNIT**
Drain-Source Voltage		V_{DS}	600	V
Gate-Source Voltage		V_{GS}	±30	V
Continuous Drain Current (Note 1)	T_C = 25°C	I_D	38	A
	T_C = 100°C		24	A
Pulsed Drain Current (Note 2)		I_{DM}	114	A
Total Power Dissipation @ T_C = 25°C		P_D	69	W
Single Pulse Avalanche Energy (Note 3)		E_{AS}	784	mJ
Single Pulse Avalanche Current (Note 3)		I_{AS}	5.6	A
Operating Junction and Storage Temperature Range		T_J, T_{STG}	- 55 to +150	°C

THERMAL PERFORMANCE			
PARAMETER	**SYMBOL**	**LIMIT**	**UNIT**
Junction to Case Thermal Resistance	$R_{\Theta JC}$	1.8	°C/W
Junction to Ambient Thermal Resistance	$R_{\Theta JA}$	62	°C/W

Thermal Performance Note: $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistances. The case-thermal reference is defined at the solder mounting surface of the drain pins. $R_{\Theta JA}$ is guaranteed by design while $R_{\Theta CA}$ is determined by the user's board design.

ELECTRICAL SPECIFICATIONS (T_A = 25°C unless otherwise noted)						
PARAMETER	**CONDITIONS**	**SYMBOL**	**MIN**	**TYP**	**MAX**	**UNIT**
Static						
Drain-Source Breakdown Voltage	V_{GS} = 0V, I_D = 250μA	BV_{DSS}	600	--	--	V
Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	$V_{GS(TH)}$	2	3	4	V
Gate Body Leakage	V_{GS} = ±30V, V_{DS} = 0V	I_{GSS}	--	--	±100	nA
Zero Gate Voltage Drain Current	V_{DS} = 600V, V_{GS} = 0V	I_{DSS}	--	--	1	μA
Drain-Source On-State Resistance **(Note 4)**	V_{GS} = 10V, I_D = 5.3A	$R_{DS(on)}$	--	81	99	mΩ
Dynamic **(Note 5)**						
Total Gate Charge	V_{DS} = 480V, I_D = 16A, V_{GS} = 10V	Q_g	--	62	--	nC
Gate-Source Charge		Q_{gs}	--	15	--	
Gate-Drain Charge		Q_{gd}	--	26	--	
Input Capacitance	V_{DS} = 100V,V_{GS} = 0V, f = 1.0MHz	C_{iss}	--	2587	--	pF
Output Capacitance		C_{oss}	--	123	--	
Reverse Transfer Capacitance		C_{rss}	--	20	--	
Gate Resistance	f = 1.0MHz	R_g	--	3.3	6.6	Ω
Switching **(Note 6)**						
Turn-On Delay Time	V_{DD} = 300V, R_{GEN} = 5Ω, I_D = 8A, V_{GS} = 10V,	$t_{d(on)}$	--	16	--	ns
Turn-On Rise Time		t_r	--	21	--	
Turn-Off Delay Time		$t_{d(off)}$	--	84	--	
Turn-Off Fall Time		t_f	--	21	--	
Source-Drain Diode						
Body-Diode Continuous Forward Current		I_S	--	--	38	A
Body-Diode Pulsed Current		I_{SM}	--	--	114	A
Forward Voltage **(Note 4)**	I_S = 16A, V_{GS} = 0V	V_{SD}	--	--	1.4	V
Reverse Recovery Time	I_S = 8A dI$_F$/dt = 100A/μs	t_{rr}	--	265	--	ns
Reverse Recovery Charge		Q_{rr}	--	3.3	--	μC

Notes:

1. Current limited by package.
2. Pulse width limited by the maximum junction temperature.
3. L = 50mH, I_{AS} = 5.6A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C
4. Pulse test: PW ≤ 300μs, duty cycle ≤ 2%.
5. For DESIGN AID ONLY, not subject to production testing.
6. Switching time is essentially independent of operating temperature.

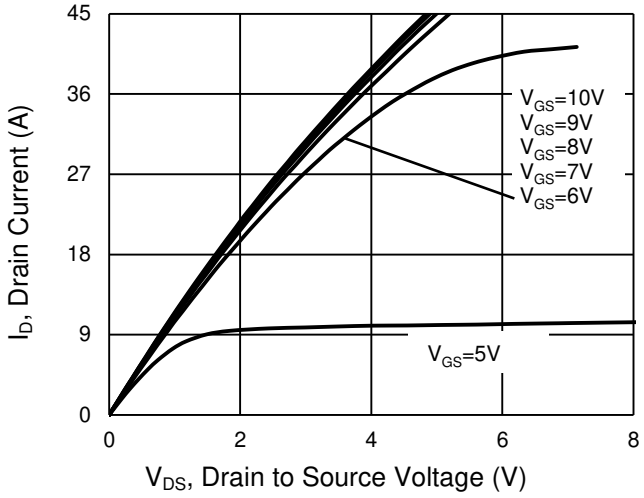
ORDERING INFORMATION

PART NO.	PACKAGE	PACKING
TSM60NB099CF C0G	ITO-220S	50pcs / Tube

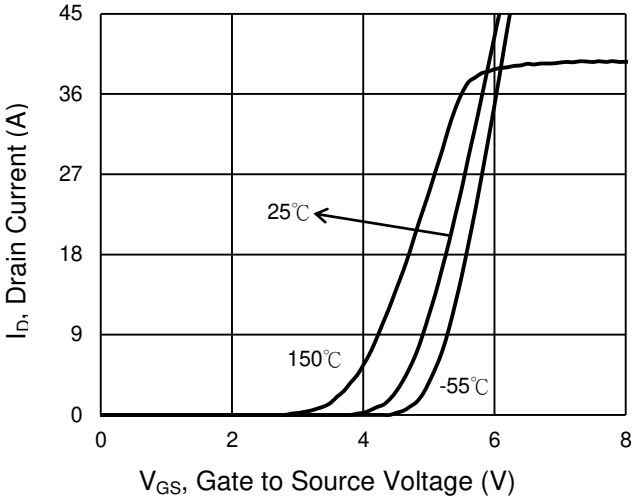
CHARACTERISTICS CURVES

(T_C = 25°C unless otherwise noted)

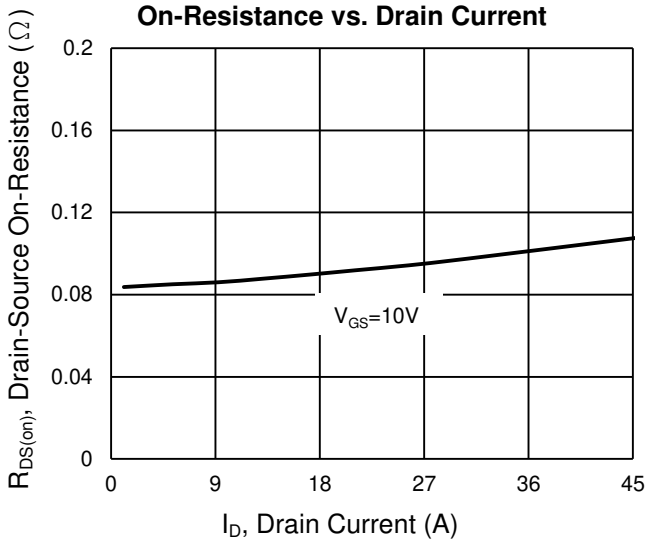
Output Characteristics



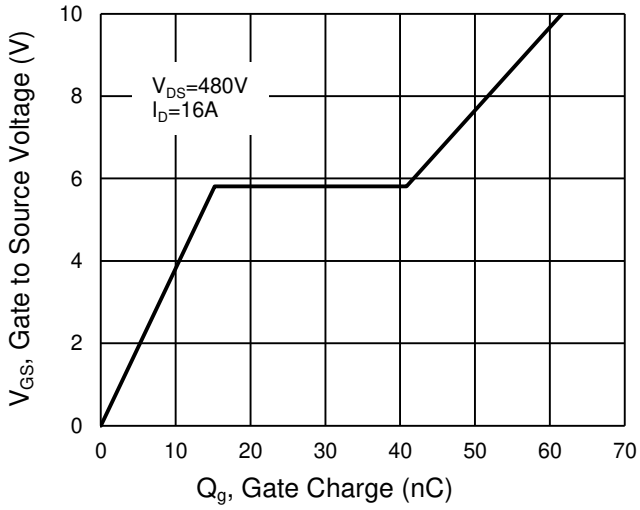
Transfer Characteristics



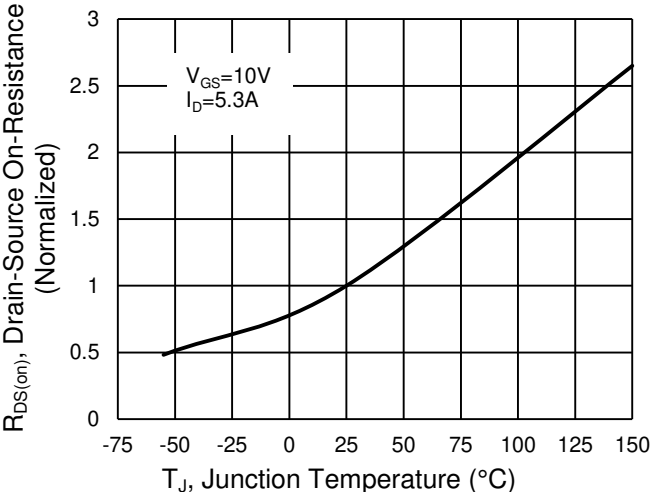
On-Resistance vs. Drain Current



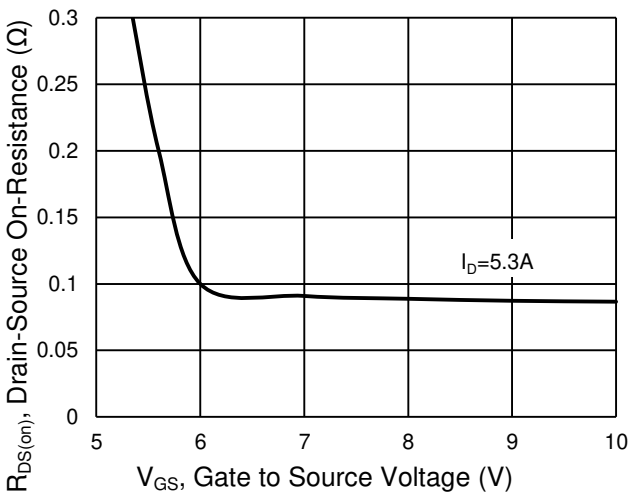
Gate-Source Voltage vs. Gate Charge



On-Resistance vs. Junction Temperature



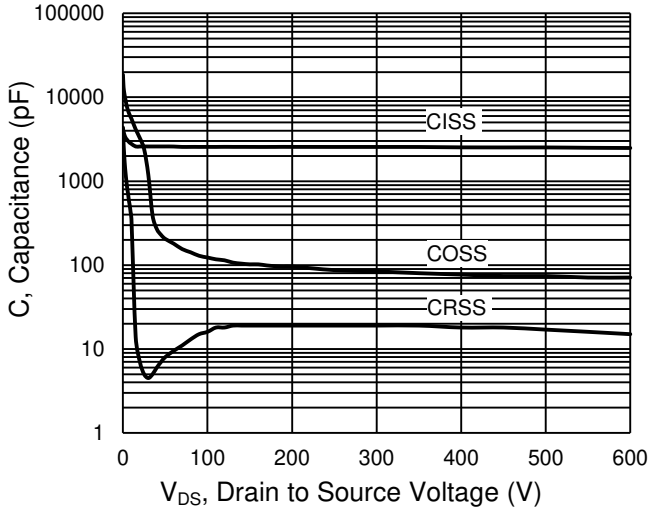
On-Resistance vs. Gate-Source Voltage



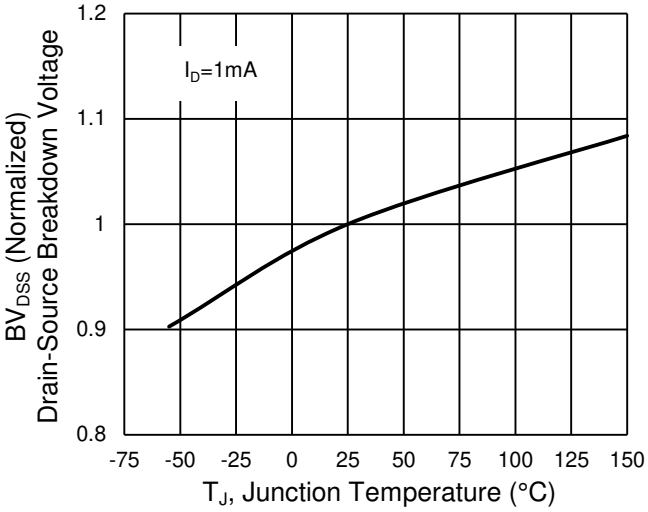
CHARACTERISTICS CURVES

(T_C = 25°C unless otherwise noted)

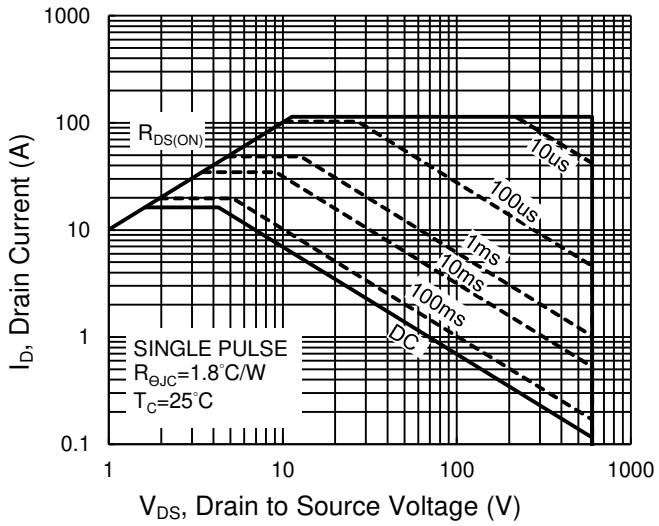
Capacitance vs. Drain-Source Voltage



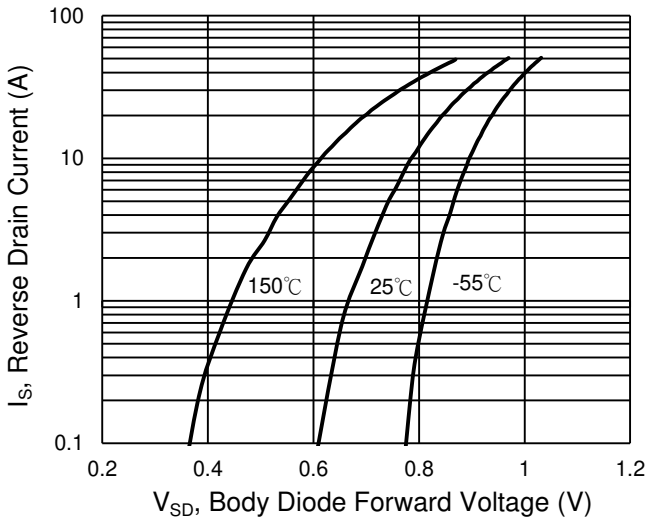
BV_{DSS} vs. Junction Temperature



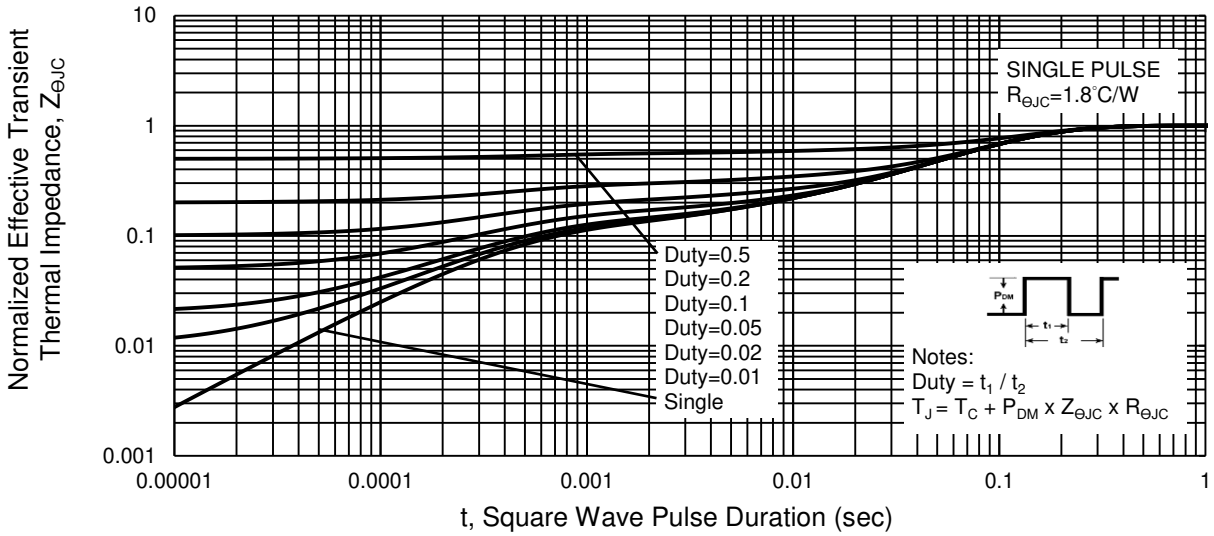
Maximum Safe Operating Area, Junction-to-Case



Source-Drain Diode Forward Current vs. Voltage

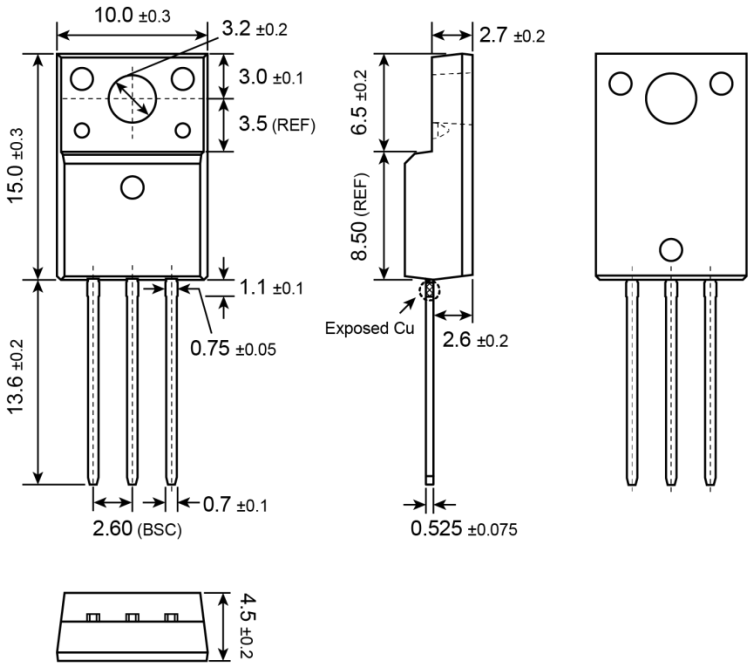


Normalized Thermal Transient Impedance, Junction-to-Case

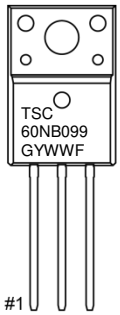


PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

ITO-220S



MARKING DIAGRAM



G = Halogen Free
Y = Year Code
WW = Week Code (01~52)
F = Factory Code

Notice

Specifications of the products displayed herein are subject to change without notice. TSC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in TSC's terms and conditions of sale for such products, TSC assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of TSC products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify TSC for any damages resulting from such improper use or sale.