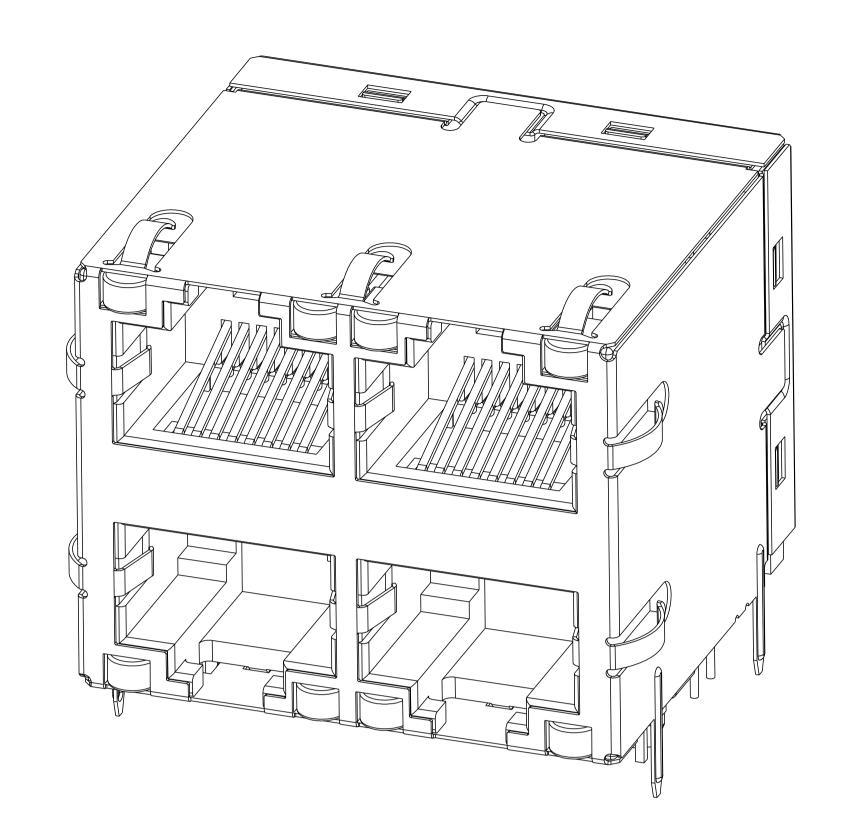
PRODUCT MAY BE PROTECTED BY ONE OR MORE OF THE FOLLOWING US PATENTS: 5736910 5939955 6425781 6428361 6554638 6840817 7123117 7429195 7717749 7808751 6217391 6149050 7924130

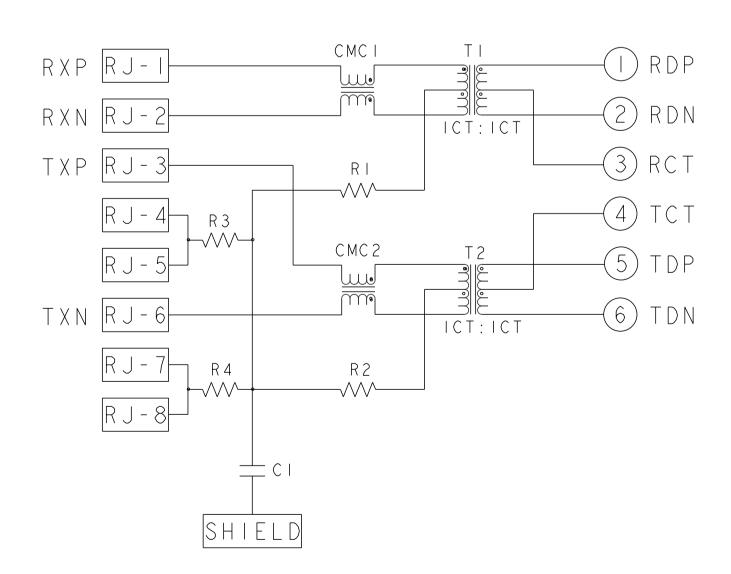
REVISIONS

P LTR DESCRIPTION DATE DWN APVD

3 EC-1411035 COMPANY LOGO CHANGE 18DEC2014 GZ TY



## S866ETP9 10/100 BASE-T CIRCUITAA TOP AND BOTTOM PORTS



CI = 1000pF, 2kV, X7R DECOUPLING CAP RI-R4 = 75 Ohms, RESISTORS

I. MATERIALS:
PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0
SHIELD: BRASS, PREPLATED WITH 0.76 um MIN SEMI-BRIGHT NICKEL,
POST DIPPED WITH 2.54 um MIN SAC SOLDER ON SOLDER TAILS,
CONTACTS: PHOSPHOR BRONZE, I.27 um MIN OVERALL NICKEL UNDERPLATE,
WITH SELECT I.27 um GOLD OR WITH SELECT 0.05 um MIN GOLD OVER 0.76 um MIN
PALLADIUM-NICKEL AT MATING INTERFACE AND 2.54 um MIN MATTE TIN ON
SOLDER TAILS.

LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME TAILS OF LED ARE PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL UNDERPLATE OVER 1.02um MIN COPPER UNDERPLATE. POST-PLATED WITH 2.54um MIN MATTE TIN AND/OR SAC SOLDER DIP OR PURE TIN SOLDER DIP

APPLICATION: 10/100 BASE-T, EXTENDED TEMPERATURE
IMPEDANCE: 100 OHMS
TURNS RATIO (CHIP:CABLE): TX = 1:1, RX = 1:1
OPEN CIRCUIT INDUCTANCE (OCL): 350 uH MIN @100 kHz, 0.1 VRMS,
8mADC BIAS FROM -40°C TO +85°C, TX AND RX
PERFORMANCE @ 25°C:

INSERTION LOSS (IL): I.IdB MAX FROM 0.5MHz TO 100MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 30MHz
18-20LOG(f/30)dB MIN FROM 30.1MHz TO 60MHz
12dB MIN FROM 60.1MHz TO 80MHz

CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
ISOLATION VOLTAGE: 2250VDC (MAX) FOR 60 SECONDS WITH A RISE TIME OF
500V/SEC AND WITH ALL PORTS CONNECTED.

PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED IN APPROXIMATE AREA SHOWN.DATE CODE: YYWWD WHERE "YY" IS YEAR, "WW" IS WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=I.

TRP LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN APPROXIMATE AREA SHOWN.

2 MAGNETICS

5. OPERATING TEMP: FROM -40°C TO +85°C.

6. RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.

INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.

DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.

BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE GREATER THAN 5.08mm.

LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA
LED COLOR: DOMINANT WAVELENGTH (λ D): GREEN 568 nm TYP. @ IF=20mA
FORWARD VOLTAGE (VF): GREEN 2.2V TYP. @ IF=20mA
DOMINANT WAVELENGTH (λ D): YELLOW 588 nm TYP. @ IF=20mA
FORWARD VOLTAGE (VF): YELLOW 2.1V TYP. @ IF=20mA

II. THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260°C MAX, IO SECONDS MAX.

	GRN/YEL	GRN/YEL	GRN/YEL	GRN/YEL	2250009-4
	GREEN	YELLOW	GREEN	YELLOW	2250009-3
	BOTTOM LED 2	BOTTOM LED 1	TOP LED 2	TOP LED 1	PART NUMBER
THIS DRAWING IS A CONTROLLED DOCUMENT.    DWN					DONGGUAN
DIMENSIONS: TOLERANCE OTHERWISE S	2 UNLE 22	07MAY2013	a bel arou		CHINA

OTHERWISE SPECIFIED:

