# 1N4148W



**Vishay Semiconductors** 

# **Small Signal Fast Switching Diode**



**DESIGN SUPPORT TOOLS** 



### MECHANICAL DATA

Case: SOD-123 Weight: approx. 10.3 mg

#### Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

OLS	click logo to get started

### FEATURES

- Silicon epitaxial planar diode
- Fast switching diodes
- AEC-Q101 qualified available
- Base P/N-E3 RoHS-compliant, commercial grade
- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

PARTS TABLE					
PART	ORDERING CODE	TYPE MARKING	CIRCUIT CONFIGURATION	REMARKS	
1N4148W	1N4148W-E3-08 or 1N4148W-E3-18	A2	Single	Tape and reel	
111414010	1N4148W-HE3-08 or 1N4148W-HE3-18	A2	Single	Tape and reel	

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V <sub>R</sub>	75	V
Repetitive peak reverse voltage		V <sub>RRM</sub>	100	V
Average rectified current half wave rectification with resistive load $^{\left(1\right)}$	$f \ge 50 Hz$	I <sub>F(AV)</sub>	150	mA
Surge forward current	t <sub>p</sub> < 1 s	I <sub>FSM</sub>	500	mA
Surge lorward current	t <sub>p</sub> = 1 μs	I <sub>FSM</sub>	2	A
Power dissipation <sup>(1)</sup>		P <sub>tot</sub>	350	mW

<b>THERMAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air <sup>(1)</sup>		R <sub>thJA</sub>	357	K/W	
Junction temperature		Тj	150	°C	
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C	
Operating temperature range		T <sub>op</sub>	-55 to +150	С°	

#### Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature.

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RoHS

COMPLIANT

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ELECTRICAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 10 mA	VF			1	V
	I <sub>F</sub> = 100 mA	V <sub>F</sub>			1.2	V
Leakage current	V <sub>R</sub> = 20 V	I <sub>R</sub>			25	nA
	V <sub>R</sub> = 75 V	I <sub>R</sub>			5	μA
	V <sub>R</sub> = 100 V	I <sub>R</sub>			100	μA
	V <sub>R</sub> = 20 V, T <sub>J</sub> = 150 °C	I <sub>R</sub>			50	μA
Diode capacitance	$V_F = V_R = 0 V$	CD			4	pF
Voltage rise when switching ON	Tested with 50 mA pulses, $t_p = 0.1 \ \mu s$ , rise time < 30 ns, $f_p = (5 \text{ to } 100) \ \text{kHz}$	V <sub>fr</sub>			2.5	v
Reverse recovery time	$I_F = 10 \text{ mA, } i_R = 1 \text{ mA, } V_R = 6 \text{ V,}$ $R_L = 100 \ \Omega$	t <sub>rr</sub>			4	ns

### TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

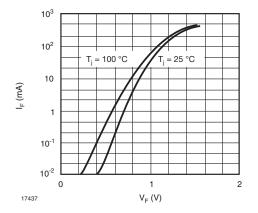


Fig. 1 - Forward Characteristics

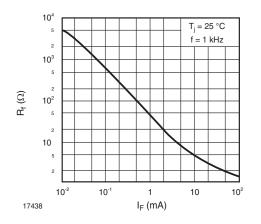


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

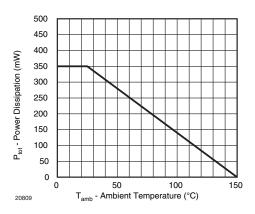


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

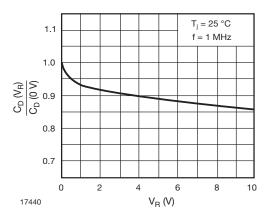


Fig. 4 - Relative Capacitance vs. Reverse Voltage

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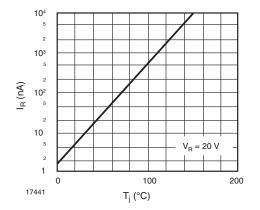


Fig. 5 - Leakage Current vs. Junction Temperature

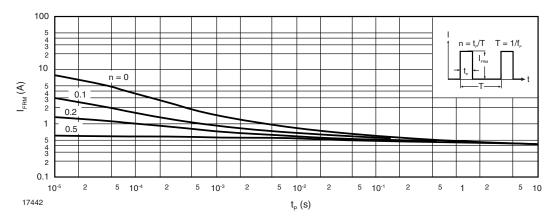
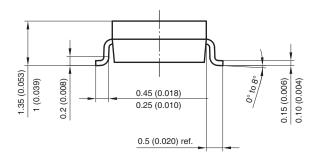


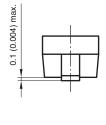
Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

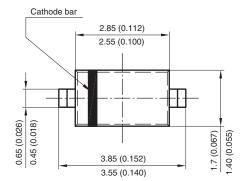


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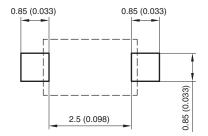
### PACKAGE DIMENSIONS in millimeters (inches): SOD-123







Mounting Pad Layout



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