

FGR15N40A

Strobe Flash N-Channel Logic Level IGBT

Features

- $V_{CE(SAT)} = 4.4V$ at $I_C = 150A$
- $t_{fi} = 1.1\mu s$, $t_{d(OFF)} = 0.46\mu s$
- 2kV ESD Protected
- High Peak Current Density
- SuperSOT - 8 package, small footprint, low profile (1mm thick)

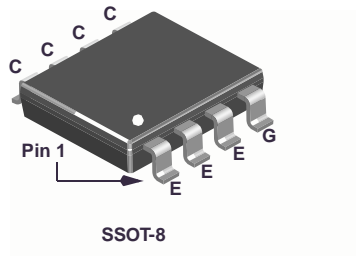
Applications

- Camera Strobe

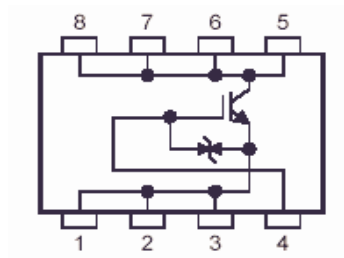
General Description

This N-Channel IGBT is a MOS gated, logic level device which has been especially tailored for camera flash applications where board space is a premium. These devices have been designed to offer exceptional power dissipation in a very small footprint for applications where bigger, more expensive packages are impractical. The gate is ESD protected with a zener diode.

FGR15N40A Strobe Flash N-Channel Logic Level IGBT



Internal Diagram



Device Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
BV_{CES}	Collector to Emitter Breakdown Voltage	400	V
I_C	Collector Current Continuous(DC)	8	A
I_{CP}	Collector Current Pulsed(100 μ s)	150	A
V_{GES}	Gate to Emitter Voltage Continuous(DC)	± 6	V
V_{GEP}	Gate to Emitter Voltage Pulsed	± 8	V
P_D	Power Dissipation Total $T_C = 25^\circ\text{C}$	1.25	W
T_J	Operating Junction Temperature Range	-40 to 150	$^\circ\text{C}$
T_{STG}	Storage Junction Temperature Range	-40 to 150	$^\circ\text{C}$
ESD	Electrostatic Discharge Voltage at 100pF, 1500 Ω	2	kV

Package Marking and Ordering Information

Device Marking	Device	Package	Tape Width	Quantity
15N40A	FGR15N40A	SuperSOT - 8	12mm	3000

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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Off State Characteristics

BV_{CES}	Collector to Emitter Breakdown Voltage	$I_C = 1\text{mA}, V_{GE} = 0\text{V}$	400	-	-	V	
BV_{GES}	Gate-Emitter Breakdown Voltage	$I_{GES} = \pm 1\text{mA}$	± 8	-	-	V	
I_{CES}	Collector to Emitter Leakage Current	$V_{CE} = 400\text{V}$	$T_C = +25^\circ\text{C}$	-	-	10	μA
			$T_C = +125^\circ\text{C}$	-	-	250	μA
I_{GES}	Gate-Emitter Leakage Current	$V_{GE} = \pm 8\text{V}$	-	-	± 10	μA	

On State Characteristics

$V_{CE(SAT)}$	Collector to Emitter Saturation Voltage	$I_C = 150\text{A}, V_{GE} = 4.0\text{V}$ (NOTE 1)	-	4.4	6.0	V
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Dynamic Characteristics

$Q_{G(ON)}$	Gate Charge	$I_C = 150\text{A}, V_{CE} = 300\text{V}, V_{GE} = 8\text{V}$	-	41	-	nC
V_{GEPL}	Gate to Emitter Plateau Voltage	$I_C = 150\text{A}, V_{CE} = 300\text{V}$	-	3.1	-	V
$V_{GE(TH)}$	Gate to Emitter Threshold Voltage	$I_C = 1.0\text{mA}, V_{CE} = V_{GE}$	0.4	0.61	0.75	V
C_{IES}	Input Capacitance	$V_{CE} = 10\text{V}, V_{GE} = 0\text{V}, f = 1\text{MHz}$	-	1800	-	pF

Switching Characteristics

t_{ON}	Turn-On Time	$V_{CE} = 300\text{V}, I_C = 150\text{A},$ $V_{GE} = 4\text{V}, R_L = 2\Omega$ $R_G = 51\Omega, T_J = 25^\circ\text{C}$	-	0.91	-	μs
$t_{d(ON)I}$	Current Turn-On Delay Time		-	0.18	-	μs
t_{rI}	Current Rise Time		-	0.73	-	μs
t_{OFF}	Turn-Off Time		-	1.56	-	μs
$t_{d(OFF)I}$	Current Turn-Off Delay Time		-	0.46	-	μs
t_{fI}	Current Fall Time		-	1.1	-	μs

Thermal Characteristics

$R_{\theta JA}$	Thermal Resistance Junction-Case	SuperSOT - 8 (NOTE 2)	-	80	-	$^\circ\text{C/W}$
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Notes:

- Pulse Duration = 100 μsec
- Mounted on a 1 inch² 1oz copper pad

Typical Characteristics

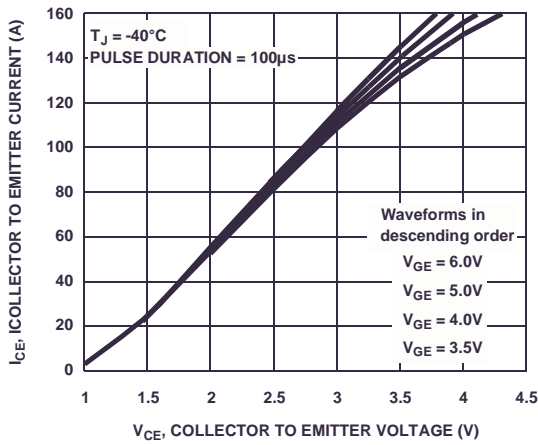


Figure 1. Collector to Emitter On-State Voltage vs Collector Current

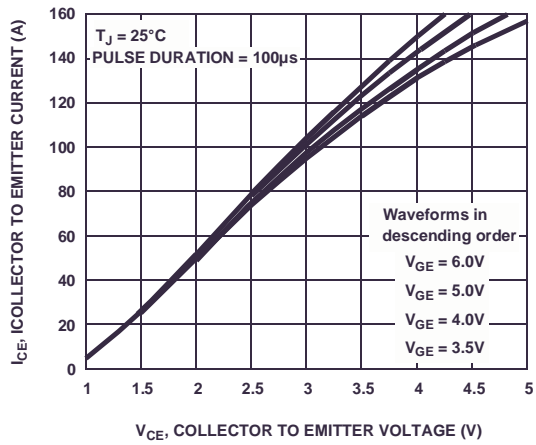


Figure 2. Collector to Emitter On-State Voltage vs Collector Current

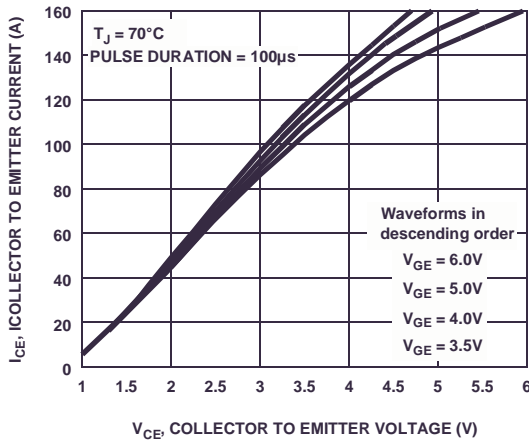


Figure 3. Collector to Emitter On-State Voltage vs Collector Current

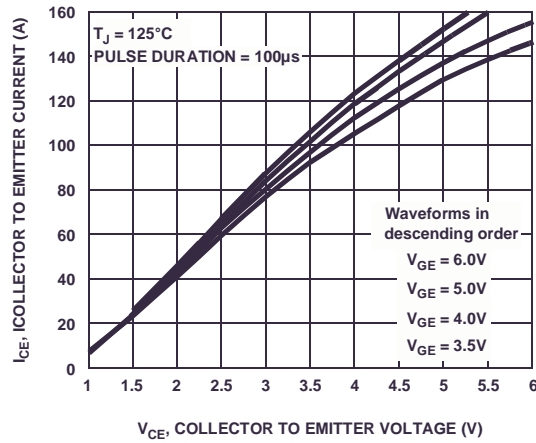


Figure 4. Collector to Emitter On-State Voltage vs Collector Current

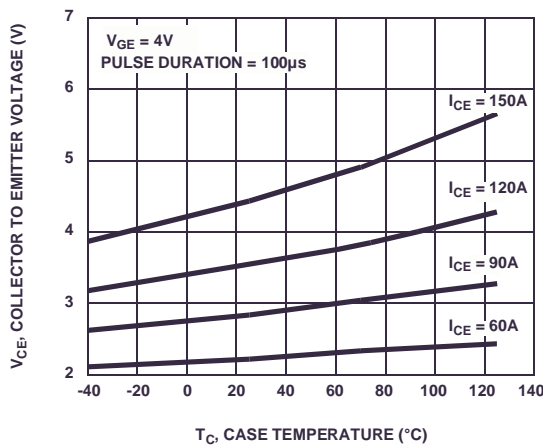


Figure 5. Collector to Emitter Saturation Voltage vs Case Temperature

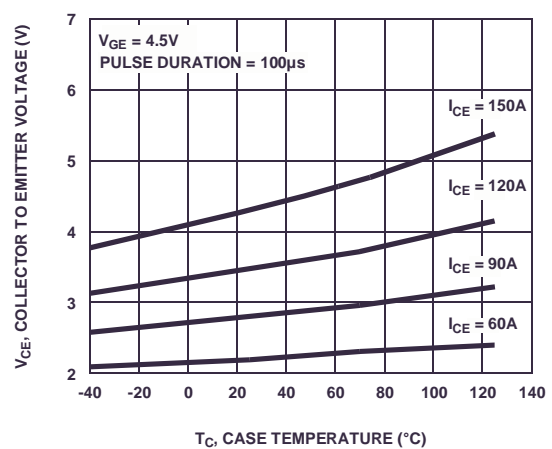


Figure 6. Collector to Emitter Saturation Voltage vs Case Temperature

Typical Characteristics (Continued)

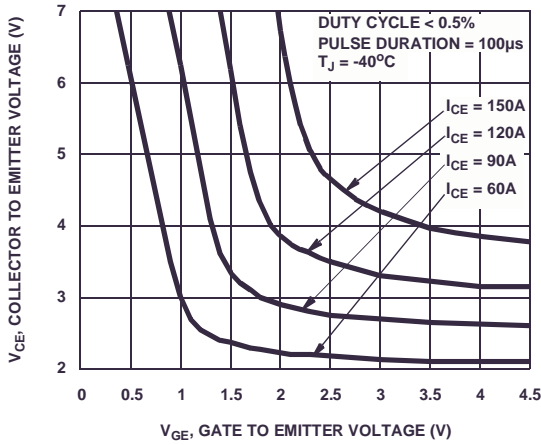


Figure 7. Collector to Emitter On-State Voltage vs Gate to Emitter Voltage

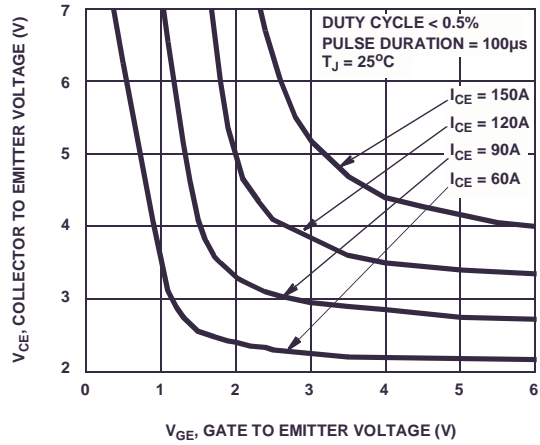


Figure 8. Collector to Emitter On-State Voltage vs Gate to Emitter Voltage

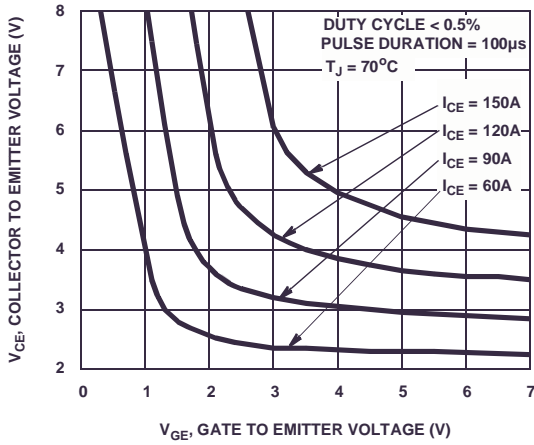


Figure 9. Collector to Emitter On-State Voltage vs Gate to Emitter Voltage

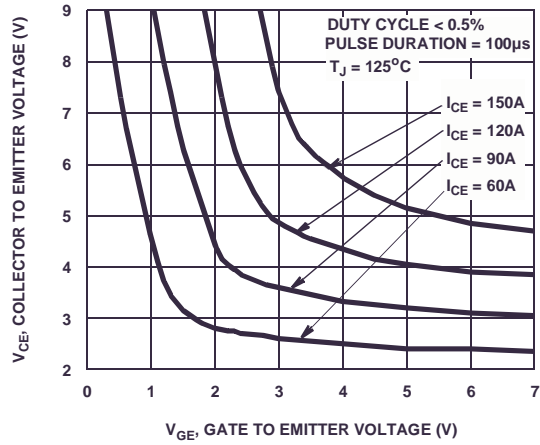


Figure 10. Collector to Emitter On-State Voltage vs Gate to Emitter Voltage

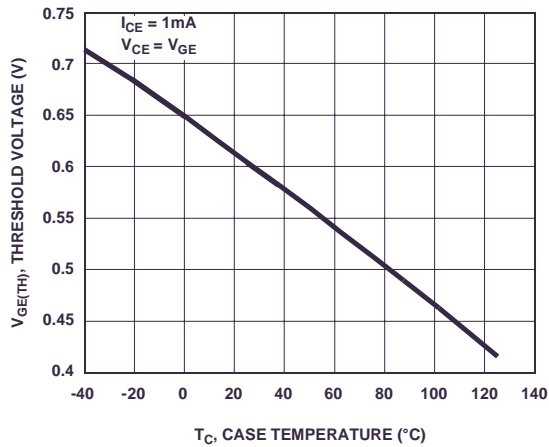


Figure 11. Gate to Emitter Threshold Voltage vs Case Temperature

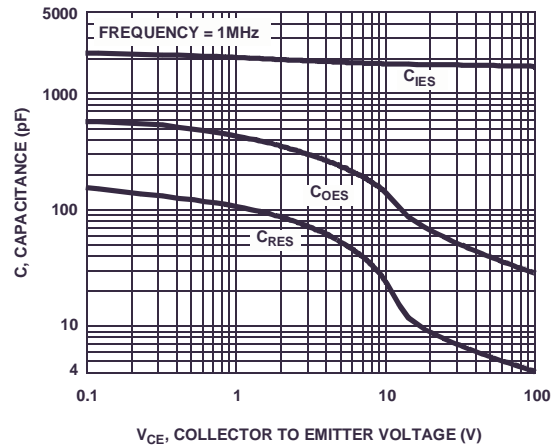


Figure 12. Capacitance vs Collector to Emitter Voltage

Typical Characteristics (Continued)

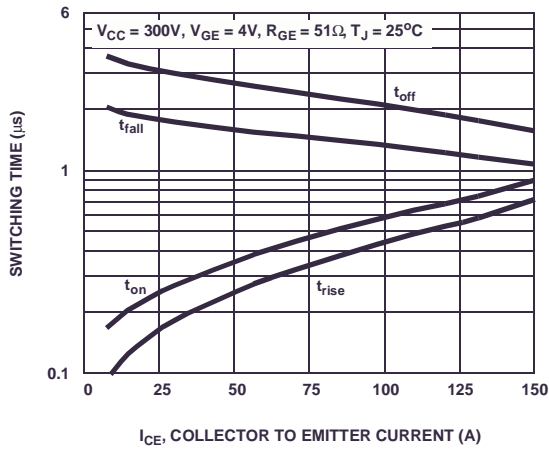


Figure 13. Switching Time vs Collector Current

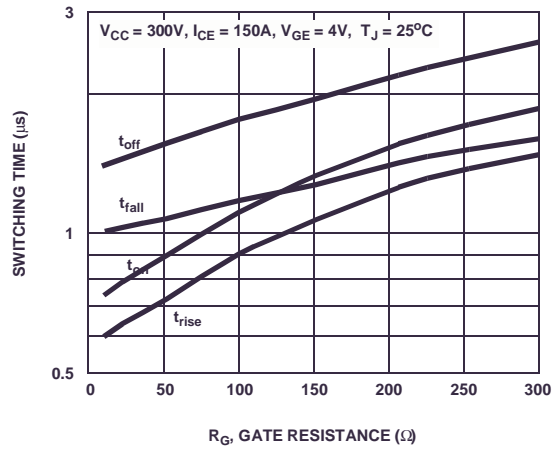


Figure 14. Switching Time vs Gate Resistance

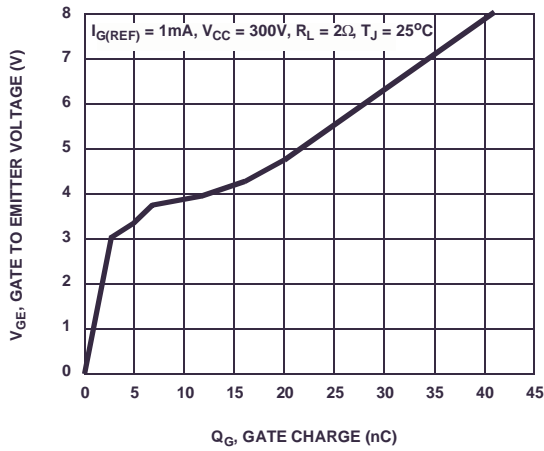


Figure 15. Gate Charge

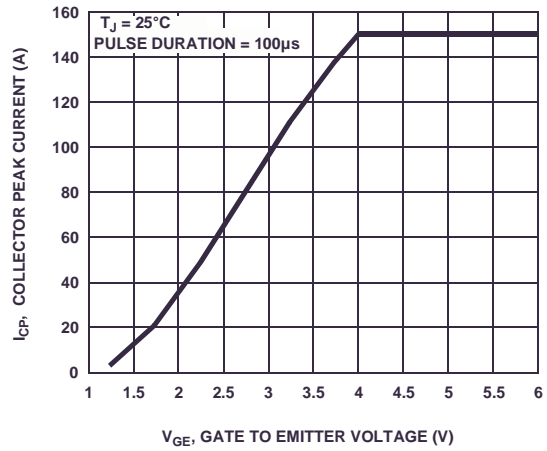


Figure 16. Collector Current Limit vs Gate to Emitter Voltage

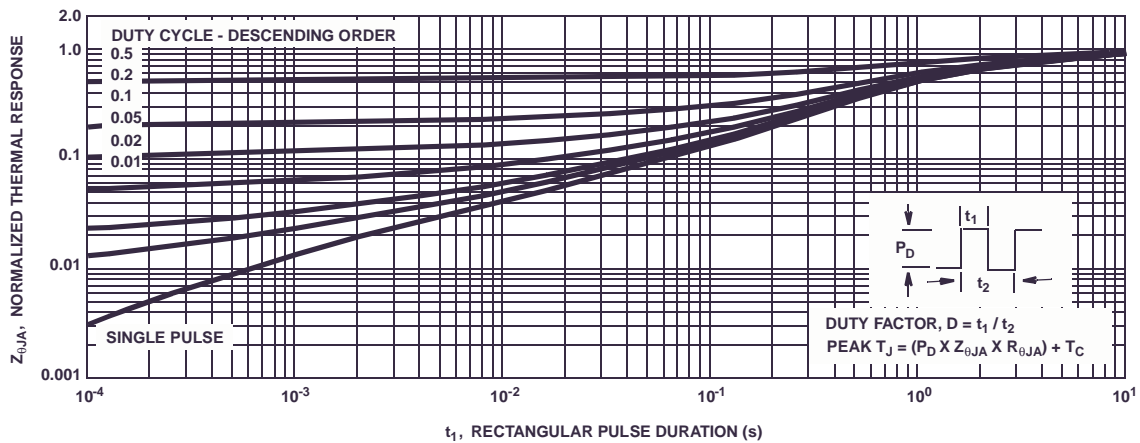


Figure 17. Normalized Transient Thermal Impedance, Junction to Case

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