

# Fast switching diode chip in Emitter Controlled Technology

### Features:

- 1200V technology 120 μm chip
- soft, fast switching
- low reverse recovery charge
- · small temperature coefficient
- qualified according to JEDEC for target applications

### Recommended for:

 power modules and discrete devices



## **Applications:**

SMPS, resonant applications, drives

Chip Type	$V_{R}$	<i>I</i> <sub>Fn</sub>	Die Size	Package
SIDC56D120F6	1200V	75A	7.5 x 7.5 mm <sup>2</sup>	sawn on foil

#### **Mechanical Parameters**

<i>7</i> 1			
	7.5 x 7.5		
	56.25		
	6.78 x 6.78		
	120	μm	
	150	mm	
er wafer	248		
	Photoimide		
	3200 nm AlSiCu		
	Ni Ag -system		
	Electrically conductive epoxy glue and soft solder		
	Al, ≤500μm		
	Ø 0.65mm; max 1.2mm		
for original and sealed MBB bags	Ambient atmosphere air, Temperature 17°C – 2 < 6 month	17°C – 25°C,	
for open MBB bags	Acc. to IEC62258-3: Atmosphere >99% Nitrogen or inert gas, Humidity <25%RH, Temperature 17°C – 25°C, < 6 month		
	for original and sealed MBB bags	56.25  6.78 x 6.78  120  150  r wafer  248  Photoimide  3200 nm AlSiCu  Ni Ag –system  Electrically conductive epoxy glue and soft so  Al, ≤500μm  Ø 0.65mm; max 1.2mm  for original and sealed MBB bags  Acc. to IEC62258-3: Atmosphere >99% Nitrogen or	



## **Maximum Ratings**

Parameter	Symbol Condition		Value	Unit
Repetitive peak reverse voltage	V <sub>RRM</sub>	<i>T</i> <sub>vj</sub> = 25 °C	1200	٧
Continuous forward current	I <sub>F</sub>	<i>T</i> <sub>vj</sub> < 150°C	1)	_
Maximum repetitive forward current <sup>2)</sup>	I <sub>FRM</sub>	<i>T</i> <sub>vj</sub> < 150°C	150	A
Operating junction and storage temperature	$T_{\rm vj}, T_{\rm stg}$		-55+150	°C

<sup>1)</sup> depending on thermal properties of assembly

# Static Characteristics (tested on wafer), $T_{vj}$ = 25 °C

Parameter	Symbol	Conditions	Value			Unit
rarameter		Conditions	min.	typ.	max.	Oilit
Reverse leakage current	I <sub>R</sub>	V <sub>R</sub> =1200V			20	μΑ
Cathode-Anode breakdown Voltage	V <sub>BR</sub>	I <sub>R</sub> =0.25mA	1200			V
Forward voltage drop	$V_{F}$	I <sub>F</sub> =75A	1.68	2.1	2.42	

# Electrical Characteristics (not subject to production test - verified by design/characterization)

Parameter		Symbol Conditions	Conditions	Value			Unit
			min.	typ.	max.	Unit	
Forward voltage drop	<i>T</i> <sub>vj</sub> = 125°C	V <sub>F</sub>	I <sub>F</sub> =75A		1.7		V

### **Further Electrical Characteristics**

Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

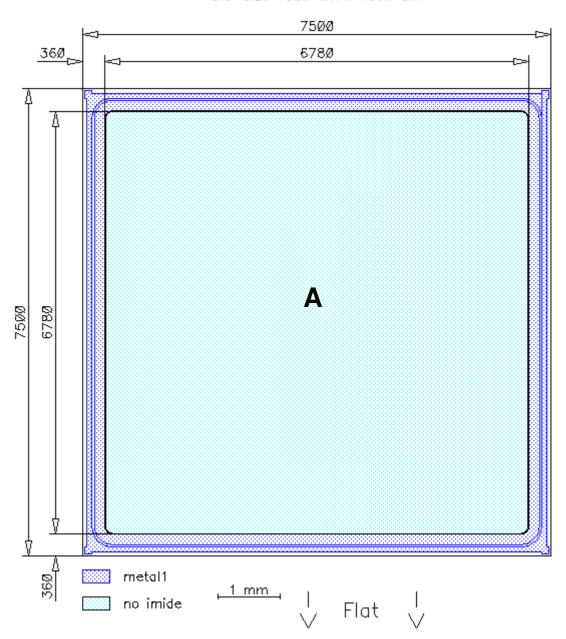
This chip data sheet refers to the device data sheet	F4-75R12KS4_B11	Rev. 2.0
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<sup>&</sup>lt;sup>2)</sup> not subject to production test - verified by design/characterisation



# **Chip Drawing**

Die-Size 7500 um x 7500 um



A: Anode pad



Description
AQL 0,65 for visual inspection according to failure catalogue
Electrostatic Discharge Sensitive Device according to MIL-STD 883

# **Revision History**

Version	Subjects (major changes since last revision)	Date

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