

# 0RQB-C5U54

## Isolated DC-DC Converter

The 0RQB-C5U54 is an isolated DC-DC converter that operates from a nominal 24 / 48 VDC source.

This unit will provide up to 162 W of output power from a nominal 24 / 48 VDC input. This unit is designed to be highly efficient.

Features include over current protection, over voltage protection and input under-voltage lockout.



### Key Features & Benefits

- 24 / 48 VDC Input
- 54 VDC @ 3 A Output
- 1/4th Brick Converter
- Fixed Frequency
- High Efficiency
- Output Over-Voltage Protection
- Over Temperature Protection
- Input Over / Under Voltage Lockout
- Over Current and Short Circuit Protection
- Approved to UL/CSA 62368-1
- Approved to IEC/EN 62368-1
- Class II, Category 2, Isolated DC-DC Converter (refer to IPC-9592B)

### Applications

- Industrial
- Computers and Peripherals
- Telecommunications



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## 1. MODEL SELECTION

MODEL NUMBER	OUTPUT VOLTAGE	INPUT VOLTAGE	MAX. OUTPUT CURRENT	MAX. OUTPUT POWER	TYPICAL EFFICIENCY
0RQB-C5U54LG 0RQB-C5U540G	54 VDC	24 / 48 VDC	3 A	162 W	89%

### PART NUMBER EXPLANATION

0	R	QB	-	C5	U	54	x	G
Mounting Type	RoHS Status	Series Name		Output Power	Input Range	Output Voltage	Active Logic	Package Type
Through Hole Mount	RoHS	1/4th Brick		162 W	24 / 48 V	54 V	L – Active Low, with Baseplate 0 – Active High, with Baseplate	Tray package

## 2. ABSOLUTE MAXIMUM RATINGS

PARAMETER	DESCRIPTION	MIN	TYP	MAX	UNITS
Input Voltage	Continuous Non-operating	-0.5	-	80	V
Remote On/Off		-0.3	-	15	V
Isolation Voltage	Input to output	-	-	2250	VDC
Operating Temperature	Temperature measured at the center of the baseplate, full load	-40	-	90	°C
	Temperature measured at the center of the baseplate, half load	-40	-	95	°C
Thermal Resistance	Baseplate to heatsink, flat greased surface	-	0.24	-	°C /W
Storage Temperature		-55	-	125	°C
Altitude		-	-	2000	m

**NOTE:** Ratings used beyond the maximum ratings may cause a reliability degradation of the converter or may permanently damage the device.

## 3. INPUT SPECIFICATIONS

All specifications are typical at 25°C unless otherwise stated.

PARAMETER	DESCRIPTION	MIN	TYP	MAX	UNIT
Operating Input Voltage		18	-	75	V
Input Current		-	-	11	A
Input Current (no load)	Vin = 48 V, Ta = 25°C	-	100	150	mA
Input Reflected Ripple Current (rms)	With simulated source impedance of 12 µH, 5 Hz to 20 MHz. Use a 47 µF/100 V electrolytic capacitor with ESR =1 ohm max, at 25°C.	-	-	15	mA
Input Reflected Ripple Current (pk-pk)		-	-	50	mA
Under-Voltage Turn on Voltage Threshold	Turn on threshold	16	16.8	17.5	V
Under-Voltage Turn off Voltage Threshold	Lockout turn off, non-latching	14	15	15.5	V
Over-Voltage Shutdown Threshold	Auto-recovery and non-latching.	76.5	78	80.3	V
Over-Voltage Recovery Threshold		76	77	78	V
Input Fast-Acting Fuse	Recommended (on system board)	-	15	-	A

**CAUTION:** This converter is not internally fused. An input line fuse must be used in application.

## 4. OUTPUT SPECIFICATIONS

All specifications are typical at nominal input, full load at 25°C unless otherwise stated.

PARAMETER	DESCRIPTION	MIN	TYP	MAX	UNIT
Output Voltage Set Point	Test condition of the output set point: Vin = 48 V, Io = 100% load at 25°C ambient.	53	54	55	V
Load Regulation		-	50	100	mV
Line Regulation		-	50	100	mV
Regulation Over Temperature		-	±200	±350	%/°C
Ripple and Noise (pk-pk)	40 kHz – 100 MHz BW, with a 0.1 µF ceramic cap and 220 µF electrolytic cap at output.	-	-	300	mV
Ripple and Noise (rms)		-	-	100	mV
Output Current Range		0	-	3	A
Output DC Current Limit	Enter a hiccup mode, non-latching.	3.45	4	4.6	A
Rise Time	Vin = 48 V, Io = 3 A, with 1000 µF bulk electrolytic at output.	-	0.5	1	s
Start-up Time (from Venable and Vin)		-	-	2	s
Overshoot at Turn on		-	0	3	%
Undershoot at Turn off		-	0	3	%
Output Capacitance		200	-	1000	µF
<b>Transient Response</b>					
ΔV 50%~75% of Max Load		-	-	3	%Vout
Settling Time	di/dt = 0.1 A/us, with 1000 µF bulk electrolytic at output.	-	-	2.5	ms
ΔV 75%~50% of Max Load		-	-	3	%Vout
Settling Time		-	-	2.5	ms

### 5. GENERAL SPECIFICATIONS

PARAMETER	DESCRIPTION	MIN	TYP	MAX	UNIT
Efficiency	Io = 60% -100% Irate	87	89	-	%
	Io = 40% -60% Irate	85	87	-	%
Switching Frequency	TA = 25°C, natural-convection air cooler	-	200	-	kHz
Output Voltage Trim Range		50	-	56	V
Over Temperature Protection	Baseplate temperature.	-	120	-	°C
Over Voltage Protection (Static)	Enter a latching, non-hiccup mode	57.5	58	58.5	V
FIT	Calculated Per IEC 62380 TR 1	-	177.58	-	-
MTBF	(UTEK 80-810) (Vin = 24 V, Vo = 54 V, Io = 3A, 0 LFM, Tac = 50°C, Tae = 35°C)	-	5.63	-	Mhrs
Weight		-	68	-	g
Dimensions (L x W x H)		2.30 x 1.45 x 0.59			inch
		58.42 x 36.83 x 15.0			mm
<i>Isolation Characteristics</i>					
Input to Output		-	-	2250	V
Input to Heatsink		-	-	2250	V
Output to Heatsink		-	-	2250	V
Isolation Resistance		10M	-	-	Ohm
Isolation Capacitance		-	-	3900	pF

**NOTE:** All specifications are typical at 25 °C unless otherwise stated.

### 6. EFFICIENCY DATA

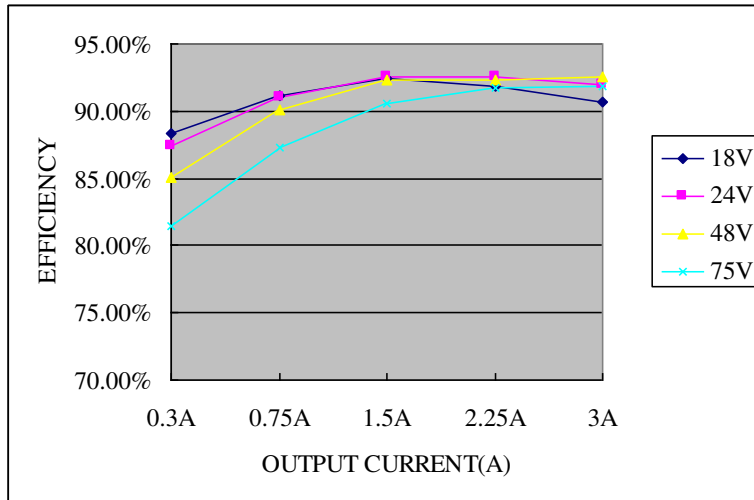


Figure 1. Efficiency data

7. REMOTE ON/OFF

PARAMETER	DESCRIPTION		MIN	TYP	MAX	UNIT
Signal Low (Unit On)	Active Low	Remote On/Off pin is open, the module is off.	-0.3	-	0.8	V
Signal High (Unit Off)			2.4	-	15	V
Signal Low (Unit Off)	Active High	Remote On/Off pin is open; the module is on.	-0.3	-	0.8	V
Signal High (Unit On)			2.4	-	15	V
Current Sink			0	-	1	mA

Recommended remote on/off circuit for active low

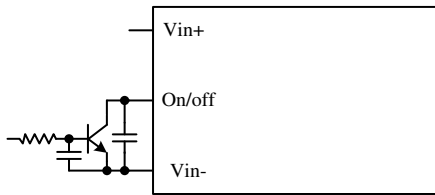


Figure 2. Control with open collector/drain circuit

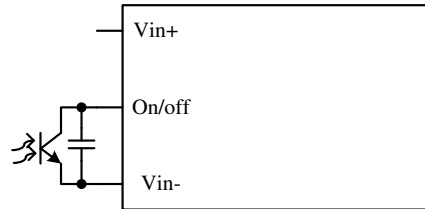


Figure 3. Control with photocoupler circuit

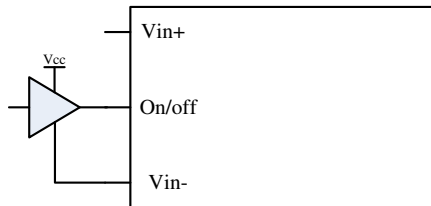


Figure 4. Control with logic circuit

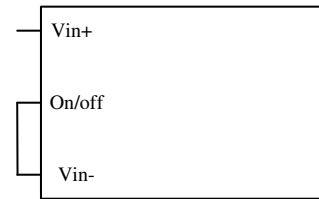


Figure 5. Permanently on

Recommended remote on/off circuit for active high

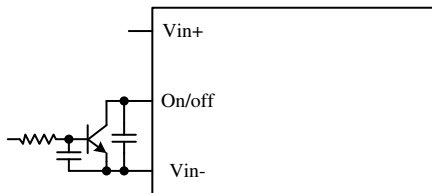


Figure 6. Control with open collector/drain circuit

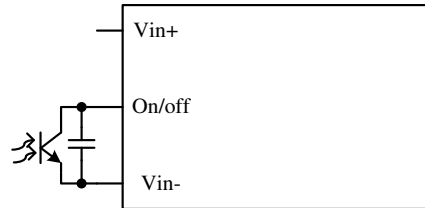


Figure 7. Control with photocoupler circuit

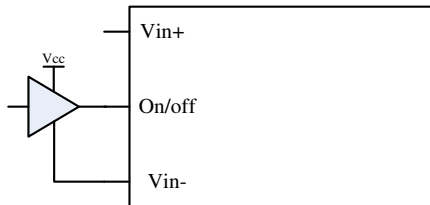


Figure 8. Control with logic circuit

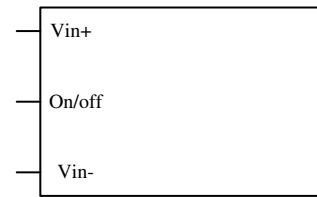


Figure 9. Permanently on

## 8. REMOTE SENSE

This module has remote sense compensation feature. It can minimize the effects of resistance between output and load in system layout and facilitate accurate voltage regulation at load terminals or other selected point.

1. The remote sense lines carry very little current and hence do not require a large cross-sectional area.
2. This module compensates for a maximum drop of 4% of the nominal output voltage.
3. If the unit is already trimmed up, the available remote sense compensation range should be correspondingly reduced. The total voltage increased by trim and remote sense should not exceed 4% of the nominal output voltage.
4. When using remote sense compensation, all the resistance, parasitic inductance and capacitance of the system are incorporated within the feedback loop of this module which can make an effect on the module's compensation, affecting the stability and dynamic response. A 0.1  $\mu\text{F}$  ceramic capacitor can be connected at the point of load to de-couple noise on the sense wires.
5. Recommend the connection of remote sense compensation as below figure. There are a resistor  $R_{S+}$  (100 ohm) from  $V_{O+}$  to Sense+ and a resistor  $R_{S-}$  (100 ohm) from  $V_{O-}$  to Sense- inside of this module.

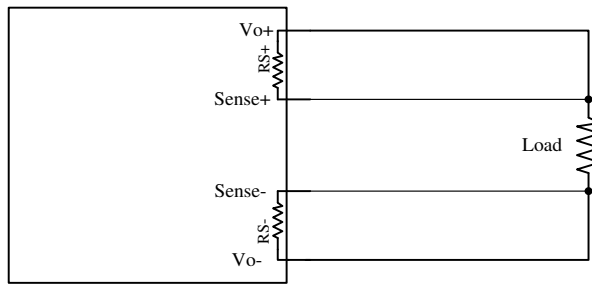


Figure 10.

6. If not using remote sense compensation, please connect sense directly to output at module's pin, that is, connect sense+ to  $V_{O+}$  and sense- to  $V_{O-}$  at module's pin, the shorter the better. see below figure.

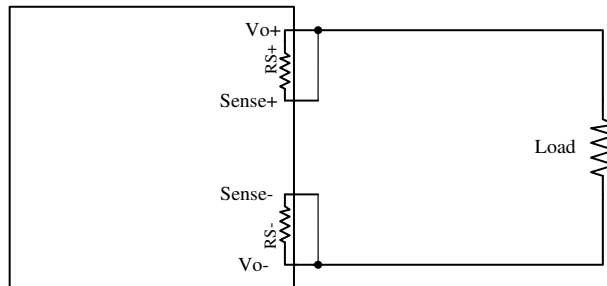


Figure 11.

9. RIPPLE AND NOISE WAVEFORM

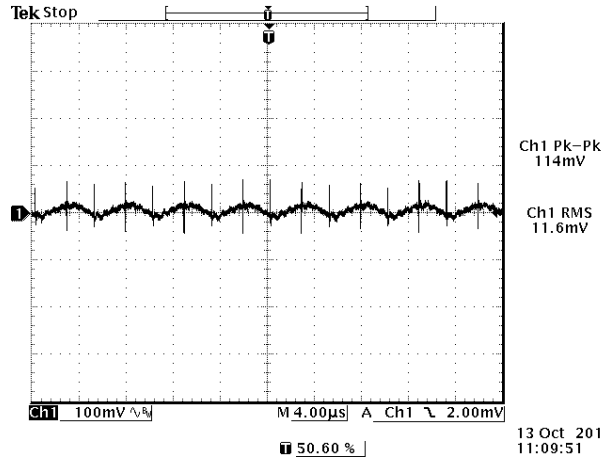


Figure 12.

**Note:** Ripple and noise 24 VDC input, 54 VDC / 3 A output and  $T_a = 25^\circ\text{C}$ , and with a 0.1 µF ceramic cap and 220 µF electrolytic cap at output.

10. TRANSIENT RESPONSE WAVEFORMS

Transient Response:  $di/dt = 0.1\text{ A}/\mu\text{s}$ , 0.1 µF ceramic cap and 1000 µF electrolytic cap at output.

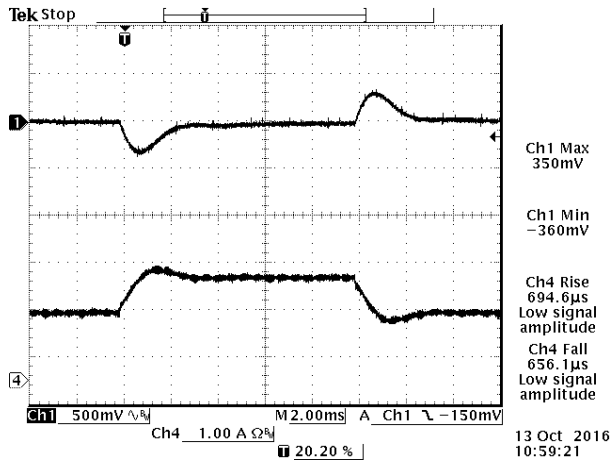


Figure 13.  $V_{out} = 54\text{ V}$ , 50%-75% Load Transients at  $V_{in} = 24\text{ V}$ ,  $T_a = 25^\circ\text{C}$

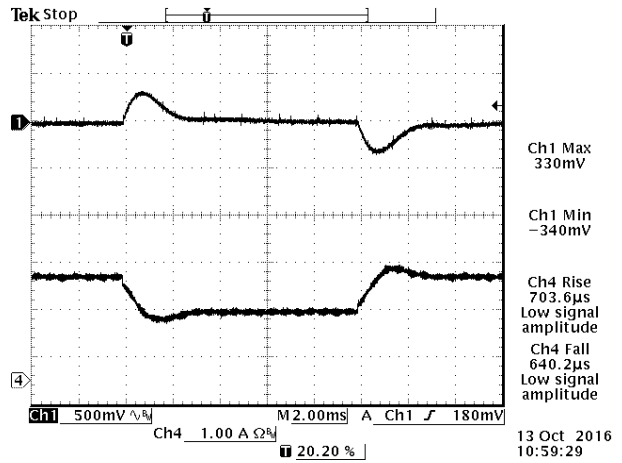


Figure 14.  $V_{out} = 54\text{ V}$ , 75%-50% Load Transients at  $V_{in} = 24\text{ V}$ ,  $T_a = 25^\circ\text{C}$

11. STARTUP & SHUTDOWN

Startup

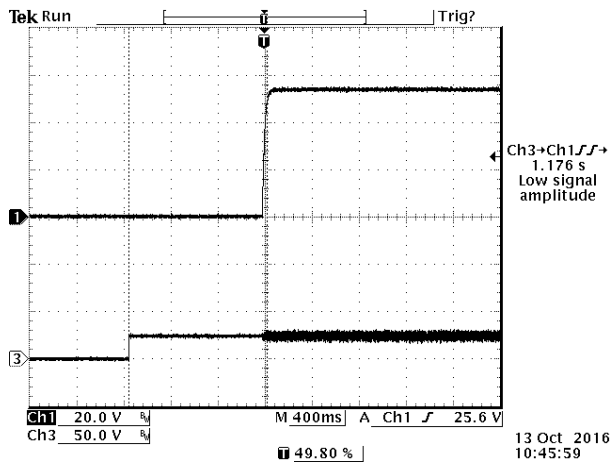


Figure 15. Startup from Vin

Ch1: Vo  
Ch3: Vin

Test Condition: Vin = 48 V, Vout = 54 V, Iout = 3.0 A and Ta = 25 °C, Co = 1000 μF

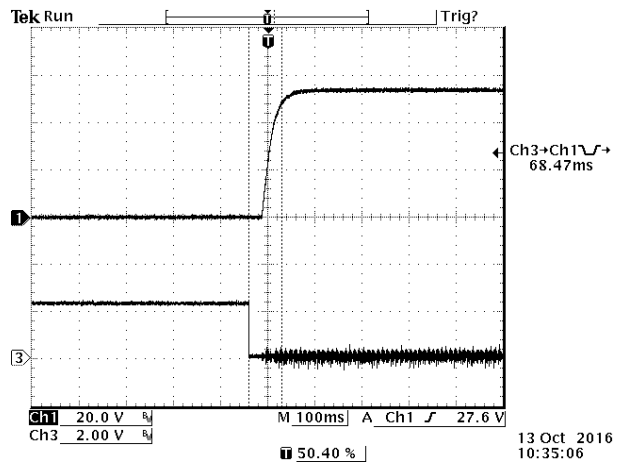


Figure 16. Startup from on/off (for 0RQB- C5U54L)

Ch1: Vo  
Ch3: on/off

Test Condition: Vin = 48 V, Vout = 54 V, Iout = 3.0 A and Ta = 25 °C, Co = 1000 μF

Shutdown

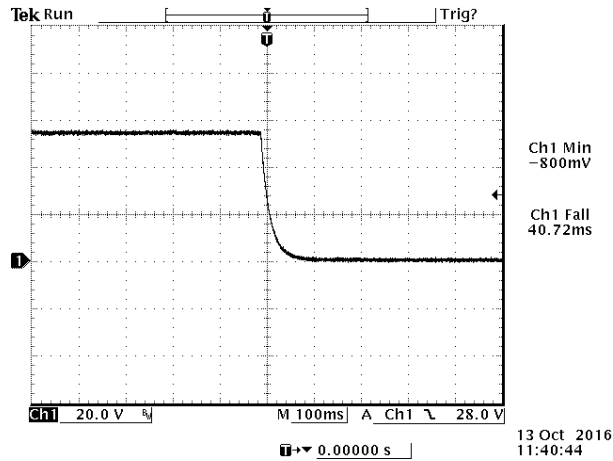


Figure 17.

Test Condition: Vin = 48 V, Vout = 54 V, Iout = 3.0 A and Ta = 25 °C, Co = 1000 μF



## 12. OVER CURRENT PROTECTION

To provide protection in a fault output overload condition, the module is equipped with internal current-limiting circuitry which can endure current limiting for a few milliseconds. If the over current condition persists beyond a few milliseconds, the module will shut down into hiccup mode and restart once every 800 ms. The module operates normally when the output current goes into specified range.

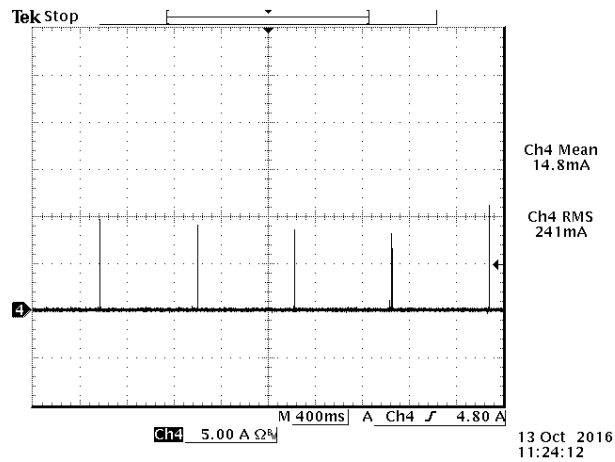


Figure 18. Over current protection

### 13. TRIM

#### Trim down test circuit

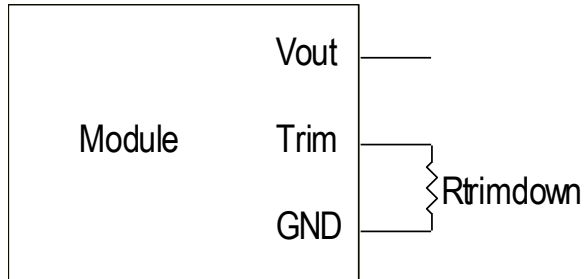


Figure 19. Trim down test circuit

$$R_{trimdown} = \frac{V_{o\_req}}{54 - V_{o\_req}} - 1 [k\Omega]$$

#### Trim up test circuit

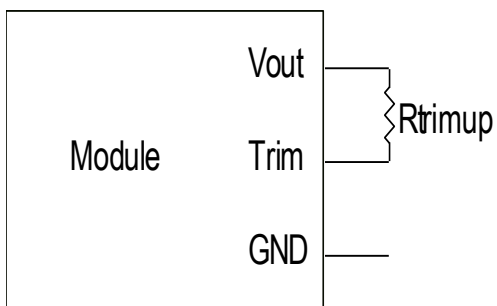


Figure 20. Trim up test circuit

$$R_{trimup} = \frac{1 - 0.02296}{0.02296 - 1.24/V_{o\_req}} - 1 [k\Omega]$$

**Note:**  $V_{o\_req}$  = Desired (trimmed) output voltage [V]

14. THERMAL DERATING CURVE

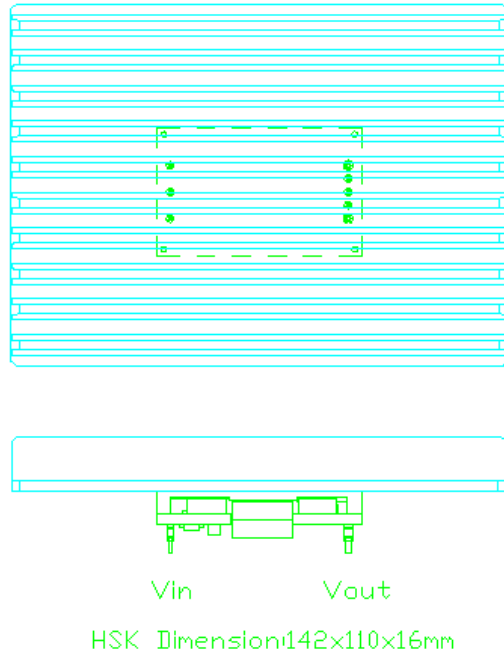


Figure 21. Thermal test setup

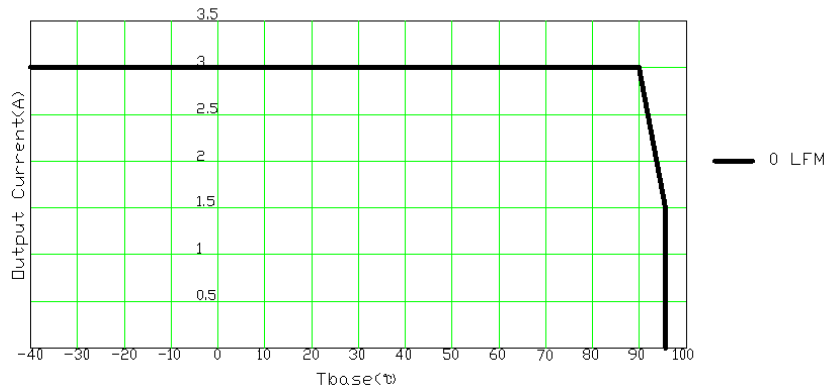


Figure 22. Thermal derating curve

15. MECHANICAL DIMENSIONS

OUTLINE

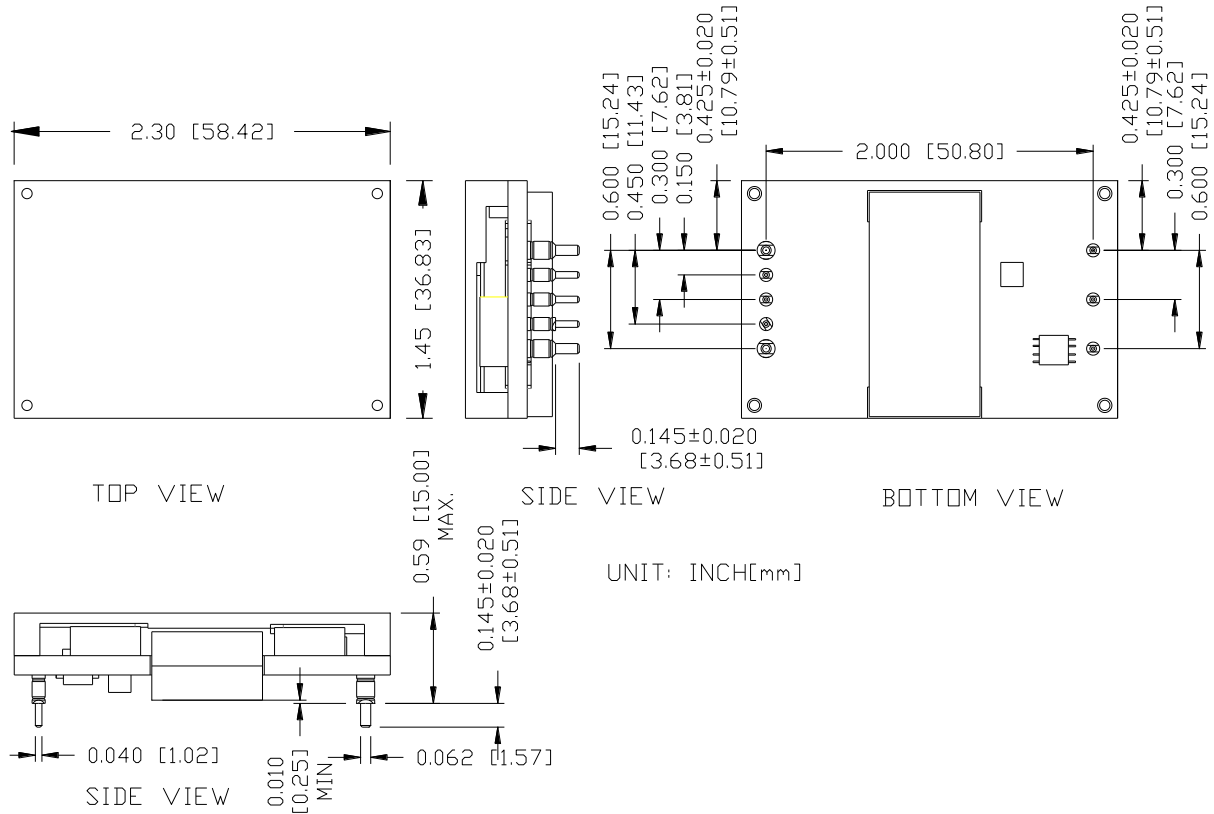


Figure 23. Outline

**Note:** This module is recommended and compatible with Pb-Free Wave Soldering and must be soldered using a peak solder temperature of no more than 260 °C for less than 5 seconds.

**NOTES:**

- 1) All Pins: Material - Copper Alloy;  
Finish - Gold plated.
- 2) Un-dimensioned components are shown for visual reference only.
- 3) All dimensions in inch [mm]; Tolerances: x.xx +/-0.02 inch [0.5 mm]. x.xxx +/-0.010 inch [0.25 mm]. Unless otherwise stated.

## PIN DEFINITIONS

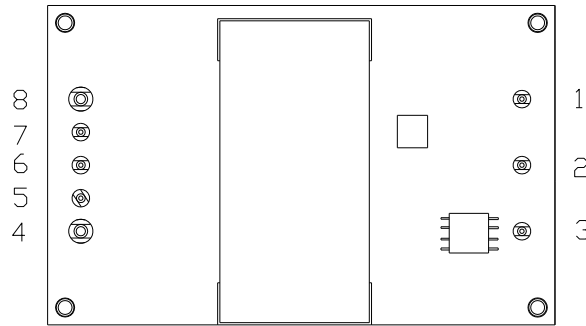


Figure 24. Pins

PIN	FUNCTION	PIN	FUNCTION
1	Vin (+)	5	Sense(-)
2	ON/OFF	6	Trim
3	Vin (-)	7	Sense(+)
4	Vout(-)	8	Vout(+)

## RECOMMENDED PAD LAYOUT

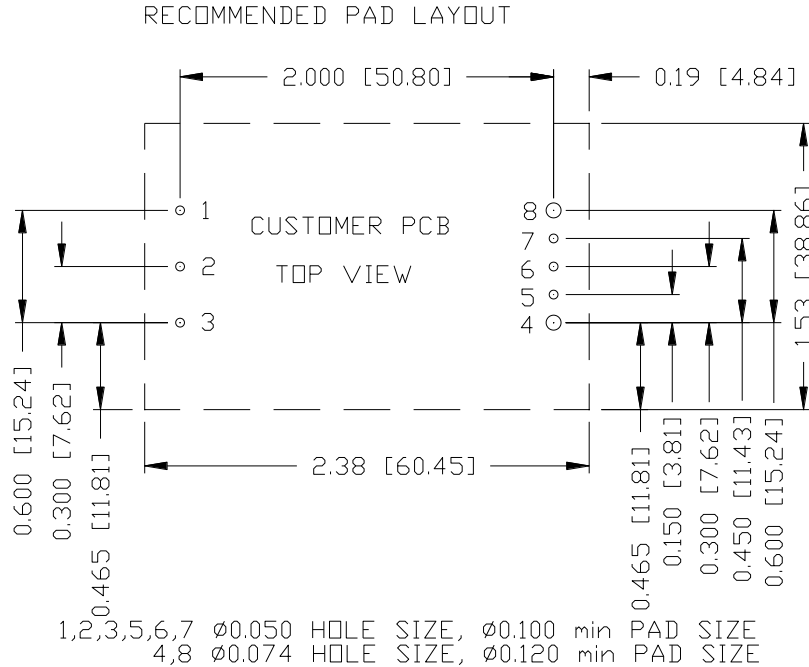


Figure 25. Recommended pad layout



## 16. REVISION HISTORY

DATE	REVISION	CHANGES DETAIL	APPROVAL
2013-05-13	A	First release	S.Wang
2013-05-21	B	Update General	J.Yan
2013-06-25	C	Update General	J.Yan
2013-12-17	D	Update Abs Max, Output Specs, General, Efficiency Data, TD, MD	J.Yan
2014-02-13	E	Update TD	J.Yan
2014-02-26	F	Update Input Specs and TD	J.Yan
2014-02-28	G	Update TD	J.Yan
2014-09-03	H	Update Input and out specs	J.Yan
2014-12-24	I	Update MTBF and FIT	J.Yan
2015-02-10	J	Update MD, Description	J.Yan
2015-10-23	K	Update the maximum module height from 0.57" to 0.59".	J.Yan
2016-02-26	L	Update Absolute maximum rating	J.Yan
2016-04-21	M	Update Safety Certification, MTBF, Thermal Derating Curve.	J.Yan
2017-06-07	AN	Update the version.	J.Yan
2018-05-17	AO	Add NR, TR, Abs Max, Startup&Shutdown and OCP	J.Yao
2019-02-18	AP	Update test condition for Ripple and Noise (pk-pk)	J.Yao
2021-05-14	AQ	Add object ID. Update safety certificate, mechanical outline and recommended pad layout.	J.Yao

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