



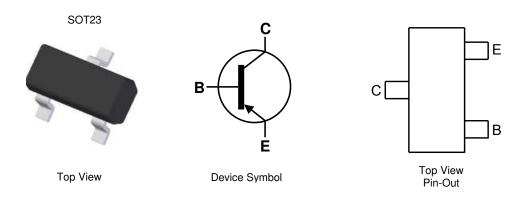
### 300V PNP SMALL SIGNAL TRANSISTOR IN SOT23

### **Features**

- BV<sub>CEO</sub> > 300V
- Ideal for Medium Power Amplification and Switching
- Complementary NPN Type: MMBTA42
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

### **Mechanical Data**

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound;
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish-Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 (§3)
- Weight: 0.008 grams (Approximate)



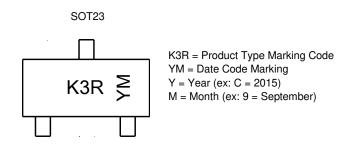
## Ordering Information (Notes 4 & 5)

Part Number	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
MMBTA92-7-F	AEC-Q101	K3R	7	8	3,000
MMBTA92-13-F	AEC-Q101	K3R	13	8	10,000
MMBTA92Q-7-F	Automotive	K3R	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**



### Date Code Key

Year	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026
Code	С	D	E	F	G	Н	1	J	K	L	М	N
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# **Absolute Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	$V_{CBO}$	-300	V
Collector-Emitter Voltage	$V_{CEO}$	-300	V
Emitter-Base Voltage	$V_{EBO}$	-5.0	V
Collector Current	Ic	-500	mA

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Power Dissipation	(Note 5)	P <sub>D</sub>	300	mW
Thermal Resistance, Junction to Ambient (Note 5)		$R_{ heta JA}$	417	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

## ESD Ratings (Note 6)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes:

## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

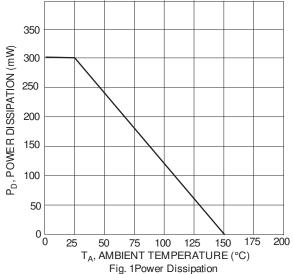
Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)					
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-300	_	V	$I_C = -100\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	$BV_{CEO}$	-300	_	V	$I_C = -1.0 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$BV_{EBO}$	-5.0	_	V	$I_E = -100 \mu A, I_C = 0$
Collector Cut-Off Current	I <sub>CBO</sub>	_	-250	nA	$V_{CB} = -200V, I_{E} = 0$
Emitter Cut-Off Current	I <sub>EBO</sub>	_	-100	nA	$V_{EB} = -3.0V, I_{C} = 0$
ON CHARACTERISTICS (Note 7)					
		25	_		$I_C = -1.0 \text{mA}, V_{CE} = -10 \text{V}$
DC Current Gain	h <sub>FE</sub>	40		_	$I_C = -10mA, V_{CE} = -10V$
		25			$I_C = -30 \text{mA}, V_{CE} = -10 \text{V}$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	-0.5	V	$I_C = -20mA$ , $I_B = -2.0mA$
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	_	-0.9	V	I <sub>C</sub> = -20mA, I <sub>B</sub> = -2.0mA
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	Cobo	_	6.0	pF	$V_{CB} = -20V$ , $f = 1.0MHz$ , $I_E = 0$
Current Gain-Bandwidth Product	f⊤	50	_	MHz	$V_{CE} = -20V, I_{C} = -10mA,$ f = 100MHz

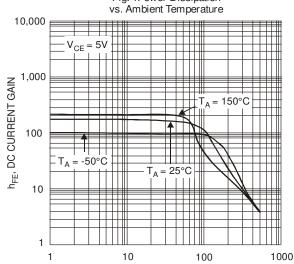
Note: 7. Measured under pulsed conditions. Pulse width  $\leq$  300 $\mu$ s. Duty cycle  $\leq$  2%.

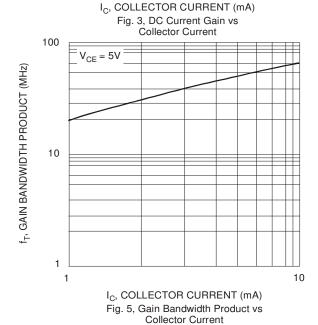
<sup>5.</sup> For a device mounted on minimum recommended pad layout 1oz copper that is on a single-sided FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.

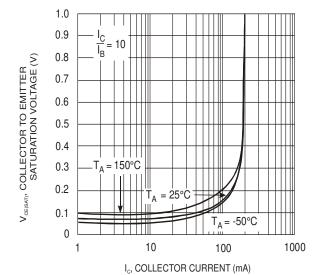
<sup>6.</sup> Refer to JEDEC specification JESD22-A114 and JESD22-A115.











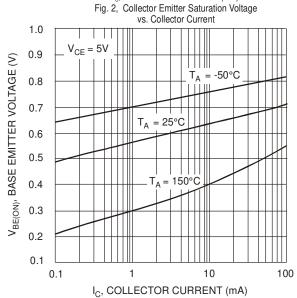
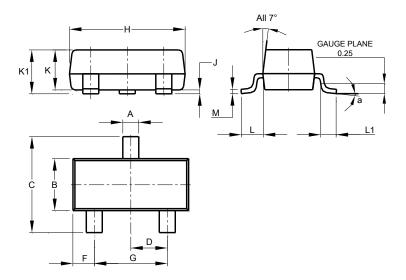


Fig. 4, Base Emitter Voltage vs Collector Current



# **Package Outline Dimensions**

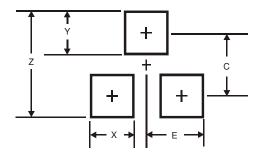
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version



SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а		8°	•			
All	All Dimensions in mm					

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Υ	0.9
С	2.0
Е	1.35

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.



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  - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
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