BLF8G38LS-75V

Power LDMOS transistor

AMPLEON

Rev. 4 — 1 September 2015

Product data sheet

1. Product profile

1.1 General description

75 W LDMOS power transistor with improved video bandwidth for base station applications at frequencies from 3400 MHz to 3800 MHz.

Table 1. Typical performance

Typical RF performance at T_{case} = 25 °C in a common source class-AB production test circuit.

Test signal	f	I _{Dq}	V _{DS}	P _{L(AV)}	Gp	η_D	ACPR _{5M}
	(MHz)	(mA)	(V)	(W)	(dB)	(%)	(dBc)
1-carrier W-CDMA	3400 to 3800	600	30	20	15.5	26	-30 <u>[1]</u>

^[1] Test signal: 3GPP test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF.

1.2 Features and benefits

- Excellent ruggedness
- High efficiency
- Low thermal resistance providing excellent thermal stability
- Decoupling leads to enable improved video bandwidth
- Designed for broadband operation (3400 MHz to 3800 MHz)
- Lower output capacitance for improved performance in Doherty applications
- Designed for low memory effects providing excellent pre-distortability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

RF power amplifiers for base stations and multi carrier applications in the 3400 MHz to 3800 MHz frequency range

2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	drain		4
2	gate	$\begin{array}{c c} 4 & 5 \\ \hline 1 & 1 \end{array}$	6.7 → 1
3	source [1]		6,7 - 4,5
4	decoupling lead		3
5	decoupling lead	2	aaa-003619
6	n.c.	6 7	
7	n.c.		

^[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

Type number	Package	ackage		
	Name	Description Versio		
BLF8G38LS-75V	-	earless flanged LDMOST ceramic package; 6 leads	SOT1239B	

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage		-	65	V
V_{GS}	gate-source voltage		-0.5	+13	V
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature	[1]	-	225	°C

^[1] Continuous use at maximum temperature will affect the reliability, for details refer to the on-line MTF calculator.

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-c)}	thermal resistance from junction to case	T _{case} = 80 °C; P _L = 20 W	0.48	K/W

6. Characteristics

Table 6. DC characteristics

 $T_i = 25$ °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 1 \text{ mA}$	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	V _{DS} = 10 V; I _D = 153 mA	1.5	1.9	2.3	V
V_{GSq}	gate-source quiescent voltage	V _{DS} = 30 V; I _D = 600 mA	1.7	2.0	2.5	V
I _{DSS}	drain leakage current	V _{GS} = 0 V; V _{DS} = 28 V	-	-	2.8	μΑ
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	-	19.7	-	Α
I _{GSS}	gate leakage current	V _{GS} = 11 V; V _{DS} = 0 V	-	-	280	nA
g _{fs}	forward transconductance	V_{DS} = 10 V; I_{D} = 153 mA	-	0.9	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 5.35 \text{ A}$	-	0.1	-	Ω

Table 7. RF characteristics

Test signal: 1-carrier W-CDMA, 3GPP test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on the CCDF; f_1 = 3400 MHz; f_2 = 3500 MHz; f_3 = 3600 MHz; RF performance at V_{DS} = 30 V; I_{Dq} = 600 mA; T_{case} = 25 °C; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Gp	power gain	P _{L(AV)} = 20 W	13.8	15.5	-	dB
η_{D}	drain efficiency	P _{L(AV)} = 20 W	21	26	-	%
RLin	input return loss	P _{L(AV)} = 20 W	-	-10	-6	dB
ACPR _{5M}	adjacent channel power ratio (5 MHz)	P _{L(AV)} = 20 W	-	-30	-25	dBc

7. Test information

7.1 Ruggedness in class-AB operation

The BLF8G38LS-75V is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 30 V; I_{Dq} = 600 mA; P_L = 75 W; f = 3400 MHz.

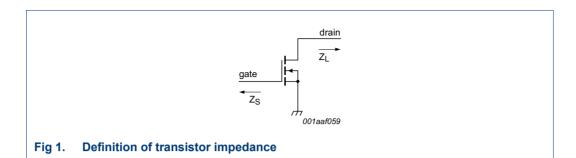
7.2 Impedance information

Table 8. Typical impedance

Measured load-pull data; $I_{Dq} = 600 \text{ mA}$; $V_{DS} = 30 \text{ V}$.

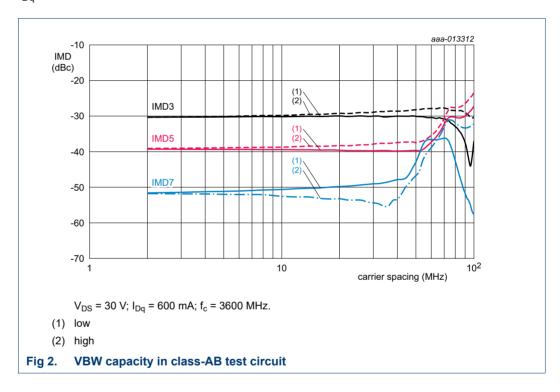
f	Z _S [1]	Z _L [1]
(MHz)	(Ω)	(Ω)
3400	1.6 – j10.2	12.6 – j3.2
3500	3.1 – j12.0	11.9 – j4.6
3600	4.7 – j12.8	12.2 – j6.9
3700	8.0 – j13.8	13.6 – j8.2
3800	19.0 – j15.7	15.0 – j10.0

[1] Z_S and Z_L defined in <u>Figure 1</u>.



7.3 VBW in a class-AB operation

The BLF8G38LS-75V has a video bandwidth of 95 MHz (typical) when measured in a class-AB test circuit operating at a center frequency of 3600 MHz for V_{DS} = 30 V and I_{Dq} = 600 mA.



7.4 Test circuit

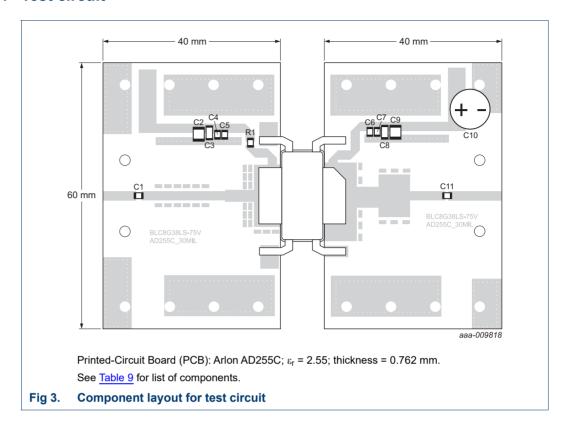


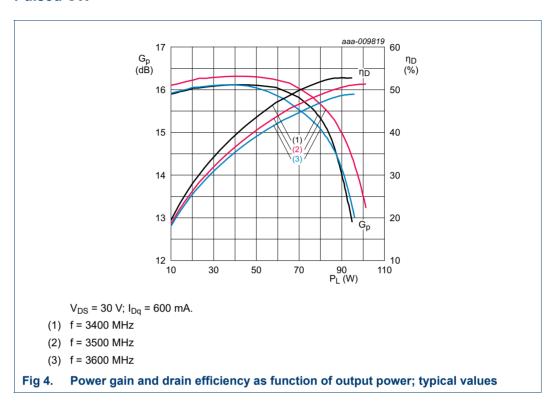
Table 9. List of components

For test circuit, see Figure 3.

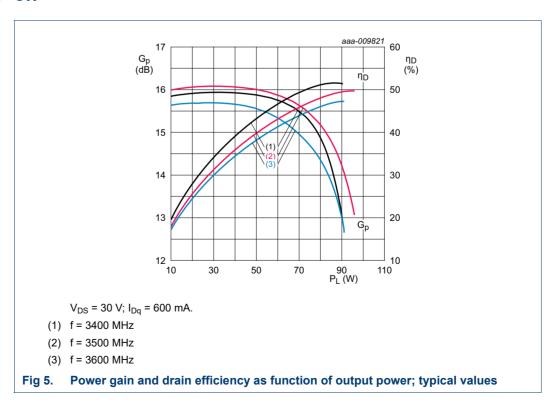
Component	Description	Value	Remarks
C1, C5, C6, C11	multilayer ceramic chip capacitor	20 pF	ATC600F
C2, C9	C2, C9 multilayer ceramic chip capacitor 10 μF Murata		Murata
C3, C8	multilayer ceramic chip capacitor	0.1 μF	Murata
C4, C7	multilayer ceramic chip capacitor	0.01 μF	Murata
C10	electrolytic capacitor	1000 μF, 100 V	
R1	chip resistor	5.1 Ω	Vishay Dale SMD 0805

7.5 Graphical data

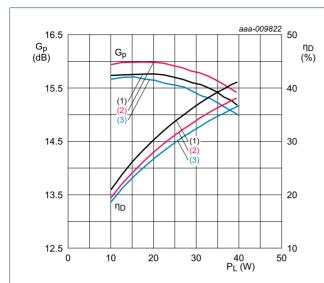
7.5.1 Pulsed CW



7.5.2 CW



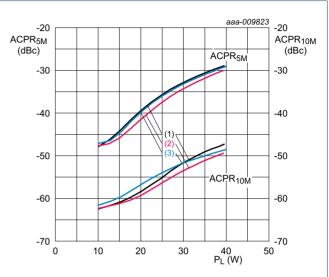
7.5.3 1-Carrier W-CDMA



 $V_{DS} = 30 \text{ V}; I_{Dq} = 600 \text{ mA}.$

- (1) f = 3400 MHz
- (2) f = 3500 MHz
- (3) f = 3600 MHz

Fig 6. Power gain and drain efficiency as function of output power; typical values



 V_{DS} = 30 V; I_{Dq} = 600 mA.

- (1) f = 3400 MHz
- (2) f = 3500 MHz
- (3) f = 3600 MHz

Fig 7. Adjacent channel power ratio (5 MHz) and adjacent channel power ratio (10 MHz) as function of output power; typical values

8. Package outline

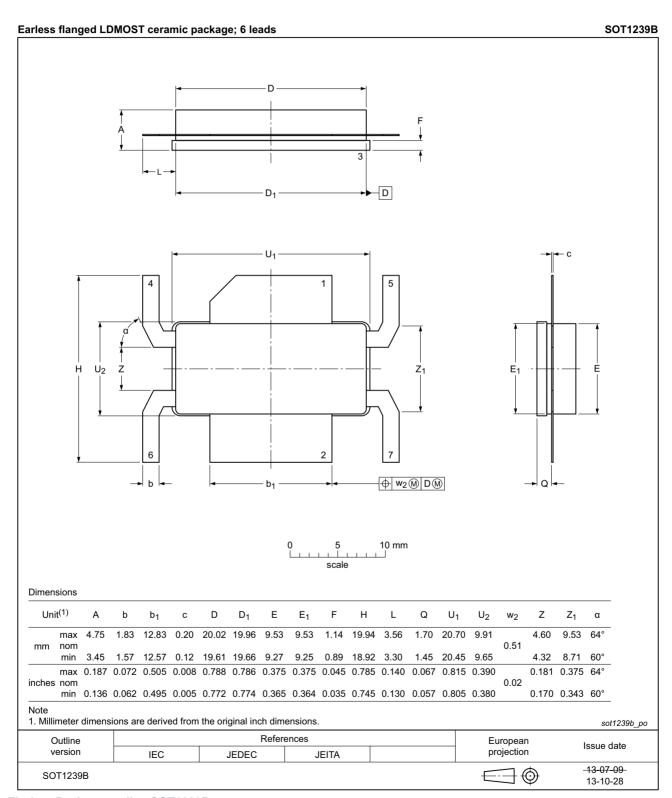


Fig 8. Package outline SOT1239B

Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

10. Abbreviations

Table 10. Abbreviations

Acronym	Description	
3GPP	3rd Generation Partnership Project	
CCDF	Complementary Cumulative Distribution Function	
CW	Continuous Wave	
DPCH	Dedicated Physical CHannel	
ESD	ElectroStatic Discharge	
LDMOS	Laterally Diffused Metal Oxide Semiconductor	
LDMOST	Laterally Diffused Metal Oxide Semiconductor Transistor	
MTF	Median Time to Failure	
PAR	Peak-to-Average Ratio	
SMD	Surface Mounted Device	
VBW	Video BandWidth	
VSWR	Voltage Standing Wave Ratio	
W-CDMA	Wideband Code Division Multiple Access	

11. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
BLF8G38LS-75V#4	20150901	Product data sheet		BLF8G38LS-75V v.3	
Modifications:	guidelines of	 The format of this document has been redesigned to comply with the new identity guidelines of Ampleon. Legal texts have been adapted to the new company name where appropriate. 			
	Legal lexis na	ive been adapted to the new t	ompany name where	е арргорпате.	
BLF8G38LS-75V v.3	20140701	Product data sheet	-	BLF8G38LS-75V v.2	
BLF8G38LS-75V v.2	20140109	Preliminary data sheet	-	BLF8G38LS-75V v.1	
BLF8G38LS-75V v.1	20131104	Objective data sheet	-	-	

12. Legal information

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Document status[1][2]	Product status[3]	Definition	
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.	
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.	
Product [short] data sheet	Production	This document contains the product specification.	

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14. Contents

1	Product profile
1.1	General description 1
1.2	Features and benefits
1.3	Applications
2	Pinning information 2
3	Ordering information 2
4	Limiting values
5	Thermal characteristics 2
6	Characteristics
7	Test information
7.1	Ruggedness in class-AB operation 3
7.2	Impedance information
7.3	VBW in a class-AB operation 4
7.4	Test circuit 5
7.5	Graphical data 6
7.5.1	Pulsed CW 6
7.5.2	CW 6
7.5.3	1-Carrier W-CDMA
8	Package outline 8
9	Handling information 9
10	Abbreviations 9
11	Revision history 9
12	Legal information
12.1	Data sheet status
12.2	Definitions
12.3	Disclaimers
12.4	Trademarks11
13	Contact information 11
14	Contents 12

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