VLMB/BG/TG31..

Vishay Semiconductors

Standard SMD LED PLCC-2



DESCRIPTION

SHA

This device has been designed to meet the increasing demand for InGaN technology.

The package of the VLMB/BG/TG31.. is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Package: SMD PLCC-2
- · Product series: standard
- Angle of half intensity: ± 60°

FEATURES

- SMD LED with exceptional brightness
- Luminous intensity categorized
- Compatible with automatic placement
 equipment
- EIA and ICE standard package
- Compatible with IR reflow, vapor phase and wave solder processes according to CECC 00802 and J-STD-020B
- Available in 8 mm tape
- Low profile package
- Non-diffused lens: excellent for coupling to light pipes and backlighting
- Low power consumption
- Luminous intensity ratio in one packaging unit $I_{Vmax}/I_{Vmin} \leq 1.6$
- Lead (Pb)-free device
- Preconditioning: acc. to JEDEC level 2a
- ESD-withstand voltage: > 1 kV acc. to MIL STD 883 D, Method 3015.7

APPLICATIONS

- Automotive: backlighting in dashboards and switches
- Telecommunication: indicator and backlighting in telephone and fax
- Indicator and backlight for audio and video equipment
- · Indicator and backlight in office equipment
- · Flat backlight for LCDs, switches and symbols
- General use

PARTS TABLE		
PART	COLOR, LUMINOUS INTENSITY	TECHNOLOGY
VLMB3140-GS08	Blue, I _V > 45 mcd	InGaN on SiC
VLMB3140-GS18	Blue, I _V > 45 mcd	InGaN on SiC
VLMBG3100-GS08	Blue green, I _V > 140 mcd	InGaN on SiC
VLMBG3100-GS18	Blue green, I _V > 140 mcd	InGaN on SiC
VLMTG3100-GS08	True green, I _V > 180 mcd	InGaN on SiC
VLMTG3100-GS18	True green, I _V > 180 mcd	InGaN on SiC

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ABSOLUTE MAXIMUM RATINGS ¹⁾ VLMB3140, VLMBG3100, VLMTG3100						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Reverse voltage ²⁾		V _R	5	V		
DC Forward current	$T_{amb} \le 80 \ ^{\circ}C$	۱ _F	20	mA		
Surge forward current	t _p ≤ 10 μs	I _{FSM}	0.2	A		
Power dissipation		P _V	84	mW		
Junction temperature		Т _ј	110	°C		
Operating temperature range		T _{amb}	- 40 to + 100	°C		
Storage temperature range		T _{stg}	- 40 to + 100	°C		
Thermal resistance junction/ ambient	mounted on PC board (pad size > 16 mm ²)	R _{thJA}	350	K/W		

Note:

⁽¹⁾ $T_{amb} = 25$ °C, unless otherwise specified ⁽²⁾ Driving LED in reverse direction is suitable for short term application

OPTICAL AND ELECTRICAL CHARACTERISTICS ¹⁾ VLMB3140, BLUE						
PARAMETER	TEST CONDITION	SYMBOL	MIN	TYP	MAX	UNIT
Luminous intensity ²⁾	I _F = 20 mA	Ι _V	45	100		mcd
Dominant wavelength	I _F = 20 mA	λ_{d}	462	470	476	nm
Peak wavelength	I _F = 20 mA	λ _p		464		nm
Angle of half intensity	I _F = 20 mA	φ		± 60		deg
Forward voltage	I _F = 20 mA	V _F		3	4.2	V
Reverse voltage	I _R = 10 μA	V _R	5			V
Temperature coefficient of V_F	I _F = 20 mA	TC _V		- 4		mV/K
Temperature coefficient of ${\rm I}_{\rm V}$	I _F = 20 mA	TCI		- 0.4		%/K

Note:

⁽¹⁾ $T_{amb} = 25$ °C, unless otherwise specified ²⁾ in one Packing Unit $I_{Vmax}/I_{Vmin} \le 1.6$

OPTICAL AND ELECTRICAL CHARACTERISTICS ¹⁾ VLMBG3100, BLUE GREEN										
PARAMETER TEST CONDITION SYMBOL MIN TYP MAX U										
Luminous intensity ²⁾	I _F = 20 mA	Ι _V	140	220		mcd				
Dominant wavelength	I _F = 20 mA	λ_d	496	505	514	nm				
Peak wavelength	I _F = 20 mA	λ _p		502		nm				
Angle of half intensity	I _F = 20 mA	φ		± 60		deg				
Forward voltage	I _F = 20 mA	V _F		3	4.2	V				
Reverse voltage	I _R = 10 μA	V _R	5			V				
Temperature coefficient of V_F	I _F = 20 mA	TCV		- 4		mV/K				
Temperature coefficient of I_V	I _F = 20 mA	TCI		- 0.2		%/K				

Note:

 $^{(1)}T_{amb}$ = 25 °C, unless otherwise specified $^{(2)}$ in one Packing Unit $I_{Vmax}/I_{Vmin} \leq 1.6$



OPTICAL AND ELECTRICAL CHARACTERISTICS ¹⁾ VLMTG3100, TRUE GREEN						
PARAMETER	TYP	MAX	UNIT			
Luminous intensity ²⁾	I _F = 20 mA	۱ _V	180	300		mcd
Dominant wavelength	I _F = 20 mA	λ _d	515	528	541	nm
Peak wavelength	I _F = 20 mA	λ _p		522		nm
Angle of half intensity	I _F = 20 mA	φ		± 60		deg
Forward voltage	I _F = 20 mA	V _F		3	4.2	V
Reverse voltage	I _R = 10 μA	V _R	5			V
Temperature coefficient of V_F	I _F = 20 mA	TC _V		- 3.5		mV/K
Temperature coefficient of ${\rm I}_{\rm V}$	I _F = 20 mA	TCI		- 0.3		%/K

Note:

 $^{1)}$ T_{amb} = 25 °C, unless otherwise specified

²⁾ In one Packing Unit $I_{Vmax}/I_{Vmin} \le 1.6$

LUMINOUS INTENSITY CLASSIFICATION							
GROUP	LIGI	LIGHT INTENSITY (MCD)					
STANDARD	OPTIONAL	OPTIONAL MIN					
Р	1	45	56				
Г	2	56	71				
Q	1	71	90				
Q	2	90	112				
B	1	112	140				
n	2	140	180				
S	1	180	224				
3	2	224	280				
т	1	280	355				
	2	355	450				
11	1	450	560				
0	2	560	710				

Note:

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of \pm 11 %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).

In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups will not be orderable.

CROSSING '	TABLE

VISHAY	OSRAM		
VLMB3140	LBT673		
VLMBG3100	LVT673		
VLMTG3100	LTT673		

COLOR CLASSIFICATION								
	B	LUE	BLUE	GREEN	TRUE	GREEN		
GROUP		DOM. WAVELENGTH (NM)						
	MIN.	MAX.	MIN.	MAX.	MIN.	MAX.		
2	458	464						
3	462	468	496	502	515	523		
4	466	472	500	506	521	529		
5	470	476	504	510	527	535		
6	474	480	508	514	533	541		



TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

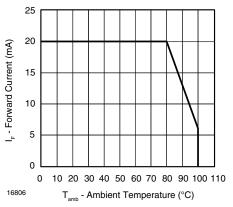


Figure 1. Forward Current vs. Ambient Temperature for InGaN

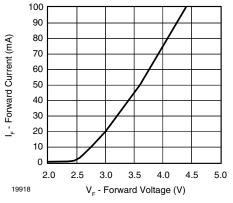


Figure 2. Forward Current vs. Forward Voltage

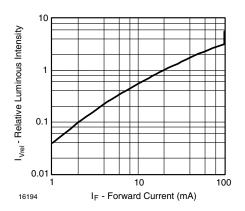


Figure 3. Specific Luminous Flux vs. Forward Current

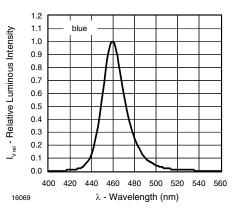
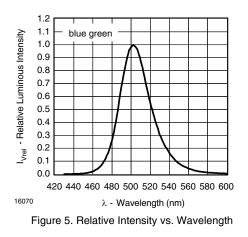


Figure 4. Relative Intensity vs. Wavelength



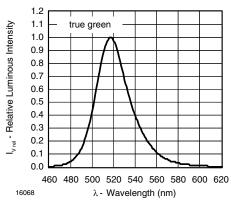


Figure 6. Relative Intensity vs. Wavelength



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Figure 7. Dominant Wavelength vs. Forward Current

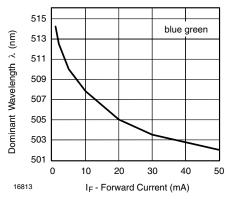
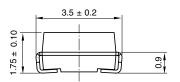
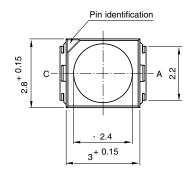


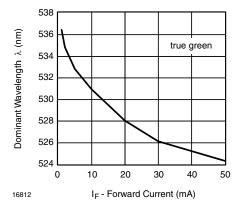
Figure 8. Dominant Wavelength vs. Forward Current

PACKAGE DIMENSIONS in millimeters





Drawing-No.: 6.541-5025.01-4 Issue: 8; 22.11.05



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Figure 9. Dominant Wavelength vs. Forward Current

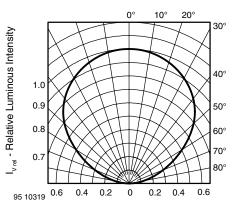
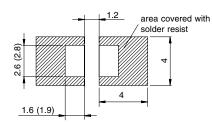


Figure 10. Relative Luminous Intensity

technical drawings according to DIN specifications

Mounting Pad Layout

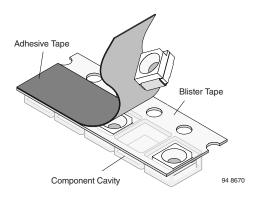




METHOD OF TAPING/POLARITY AND TAPE AND REEL

SMD LED (VLM3 - SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



REEL PACKAGE DIMENSION IN MM FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

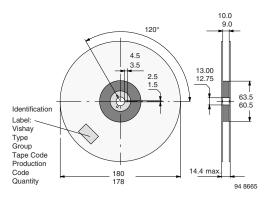


Figure 12. Reel Dimensions - GS08

TAPING OF VLM.3..

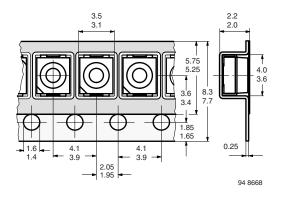


Figure 11. Tape Dimensions in mm for PLCC-2

REEL PACKAGE DIMENSION IN MM FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS.) PREFERRED

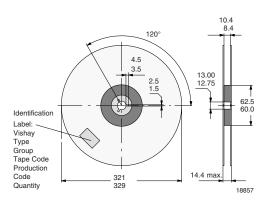


Figure 13. Reel Dimensions - GS18



SOLDERING PROFILE

SHA

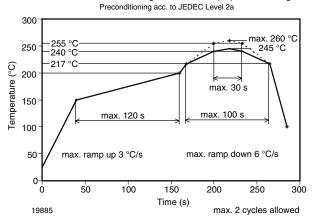


Figure 14. Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020B)

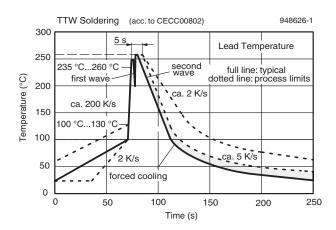
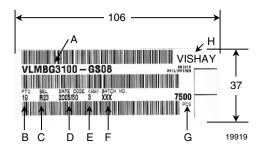


Figure 15. Double Wave Soldering of Opto Devices (all Packages)

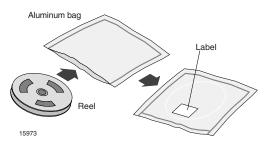
BARCODE PRODUCT LABEL EXAMPLE:



- A) Type of component
- B) Manufacturing plant
- C) SEL selection code (bin):
 - e.g.: R2 = code for luminous intensity group 3 = code for color group
- D) Date code year/week
- E) Day code (e. g. 3: Wednesday)
- F) Batch no.
- G) Total quantity
- H) Company code

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

IR Reflow Soldering Profile for lead (Pb)-free soldering



RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity \leq 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

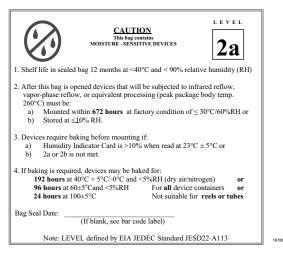
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/ nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC Standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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Vishay

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