

ON Semiconductor

NC7SZ157 TinyLogic[®] UHS 2-Input Non-Inverting Multiplexer

Features

- Broad V_{CC} Operating Range: 1.65V to 5.5V
- Ultra High-Speed
- Pow er Dow n High-Impedance Inputs/Outputs
- Over-Voltage Tolerance Inputs Facilitate 5V to 3V Translation
- Proprietary Noise/EMI Reduction Circuitry
- Ultra-Small MicroPak™ Packages
- Space-Saving SC70 Package

Description

The NC7SZ157 is a single, high performance, 2-to-1 CMOS non-inverting multiplexer from ON Semiconductor's Ultra-High Speed series of TinyLogic $^{\circledR}$. The device is fabricated with advanced CMOS technology to achieve ultra high speed with high output drive while maintaining low static power dissipation over a broad $V_{\rm CC}$ operating range. The device is specified to operate over the 1.65V to 5.5V Vcc operating range. The inputs and outputs are high impedance when $V_{\rm CC}$ is 0V. Inputs tolerate voltages up to 5.5V independent of $V_{\rm CC}$ operating range.

Ordering Information

Part Number	Top Mark	© Eco Status	Package	Packing Method
NC7SZ157P6X	ZF7	RoHS	6-Lead SC70, EIAJ SC-88, 1.25mm Wide	3000 Units on Tape & Reel
NC7SZ157L6X	B9	RoHS	6-Lead MicroPak™, 1.00mm Wide	5000 Units on Tape & Reel
NC7SZ157FHX	B9	Green	6-Lead, MicroPak2, 1x1mm Body, .35mm Pitch	5000 Units on Tape & Reel

Connection Diagrams

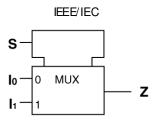


Figure 1. Logic Symbol

Pin Configurations

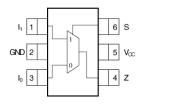


Figure 2. SC70 (Top View)

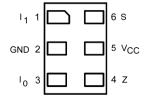


Figure 3. MicroPak™ (Top Through View)

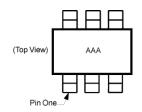


Figure 4. Pin 1 Orientation

Notes:

- 1. AAA represents product code top mark (see Ordering Information).
- 2. Orientation of top mark determines pin one location.
- 3. Reading the top mark left to right, pin one is the low er left pin.

Pin Definitions

Pin # SC70	Pin # MicroPak	Name	Description
1	1	l ₁	Data Input
2	2	GND	Ground
3	3	lo	Data Input
4	4	Z	Output
5	5	V _{CC}	Supply Voltage
6	6	S	Control Input

Function Table

Inputs			Output
S	I ₁	I ₀	$Z = (I_0) \cdot (S) + (I_1) \cdot (S)$
L	X	L	L
L	Х	Н	Н
Н	L	X	L
Н	Н	Х	Н

H = HIGH Logic Level

L = LOW Logic Level

X = Don't' Care

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Para	Min.	Max.	Unit	
V _{CC}	Supply Voltage	Supply Voltage			V
V _{IN}	DC Input Voltage		-0.5	7.0	V
V _{OUT}	DC Output Voltage		-0.5	7.0	V
lık	DC Input Diode Current	$V_{IN} \leq 0.5V$		-50	mA
Юк	DC Output Diode Current	V _{OUT} ≤ -0.5V		-50	mA
ЮПТ	DC Output Current		±50	mA	
Icc or Ignd	DC V _{CC} or Ground Current		±50	mA	
T _{STG}	Storage Temperature Range	-65	+150	°C	
TJ	Junction Temperature Under B	ias		+150	°C
TL	Junction Lead Temperature (S	oldering, 10 Seconds)		+260	°C
		SC70-6		180	
P_D	Power Dissipation at +85°C	MicroPak-6		130	mW
		MicroPak2-6		120	
ESD	Human Body Model, JEDEC:JE	SD22-A114		4000	V
130	Charge Device Model, JEDEC:		2000	v	

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. ON Semiconductor does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Conditions	Min.	Max.	Unit	
V _{CC}	Supply Voltage Operating		1.65	5.50	V	
V GC	Supply Voltage Data Retention		1.50	5.50	V	
V _{IN}	Input Voltage		0	5.5	V	
V _{OUT}	Output Voltage		0	Vcc	V	
TA	Operating Temperature		-40	+85	°C	
		V _{CC} at 1.8V ± 0.15V, 2.5V ± 0.2V	0	20	0.4	
t _r , t _f	Input Rise and Fall Times	V _{CC} at 3.3V ± 0.3V	0	10	ns/V	
		V _{CC} at 5.0V ± 0.5V	0	5		
		SC70-6		350		
$\theta_{\sf JA}$	Thermal Resistance	MicroPak-6		500	°C/W	
		MicroPak2-6		560		

DC Electrical Characteristics

Symbol	Parameter	Vcc	Cor	nditions	T	_{Α=+} 25°	C	T _A =-40	Units													
Symbol	Parameter	V CC	Cor	iditions	Min.	Тур.	Max.	Min.	Max.	Units												
V	HIGH Level Input	1.65 to 1.95			0.75V _{CC}			$0.75 V_{\text{CC}}$		V												
V_{IH}	Voltage	2.30 to 5.50			0.70V _{CC}			$0.70 V_{\text{CC}}$		V												
VII	LOW Level Input	1.65 to 1.95					0.25V _{CC}		0.25V _{CC}	٧												
V IL	Voltage	2.30 to 5.50					0.30V _{CC}		0.30V _{CC}	V												
		1.65			1.55	1.65		1.55														
		2.30	V _{IN} =V _{IL} or V _{IH}	I _{OH} = -100μA	2.20	2.30		2.20														
		3.00	OI VIH	10H= -100μΑ	2.90	3.00		2.90														
	LUCIU	4.50			4.40	4.50		4.40														
V_{OH}	HIGH Level Output Voltage	1.65	V _{IN} =V _{IL} I or V _{IH} I	I _{OH} = -4mA	1.29	1.52		1.29		V												
		2.30		I _{OH} = -8mA	1.90	2.15		1.90														
		3.00		I _{OH} = -16mA	2.40	2.80		2.40														
		3.00		I _{OH} = -24mA	2.30	3.68		2.30														
		4.50		I _{OH} = -32mA	3.90	4.20		3.80														
		1.65	V _{IN} =V _{IL} or V _{IH}				0	0.10		0.10												
		2.30					I _{OL} = 100μΑ		0	0.10		0.10	V									
		3.00					OI VIH	OI VIH	OI VIH	OI VIH	OI VIH	OI VIH	OI VIH	OI VIH	OI VIH	OI VIH	OI VIH	OI VIH NOL- I	Ιομ- 100μ/1		0	0.10
	L OW L and	4.50				0	0.10		0.10													
V_{OL}	LOW Level Output Voltage	1.65		I _{OL} = 4mA		0.08	0.24		0.24													
		2.30	V _{IN} =V _{IL}	I _{OL} = 8mA		0.10	0.30		0.30													
		3.00	or V _{IH}	I _{OL} = 16mA		0.15	0.40		0.40	V												
		3.00		I _{OL} = 24mA		0.22	0.55		0.55													
		4.5		I _{OL} = 32mA		0.22	0.55		0.55													
I _{IN}	Input Leakage Current	0 to 5.50	V _{IN} =5.5	V, GND			±0.1		±1	μΑ												
I _{OFF}	Power Off Leakage Current	0	V _{IN} or V ₀	_{DUT} =5.5V			1		10	μΑ												
I _{cc}	Quiescent Supply Current	1.65 to 5.50	V _{IN} =5.5	V, GND					10	μΑ												

AC Electrical Characteristics

Symbol	Parameter V _{CC} Con		Conditions	T _{A=+} 25°C		;	T _A =-40 to +85°C		Units	Figure
Cymbol	r ar arrie ter	V CC	Conditions	Min.	Тур.	Max.	Min.	Max.	Omis	rigure
		1.80 ± 0.15		2.5	6.0	11.5	2.5	12.0		
	Propagation Delay	2.50 ± 0.20	C _L =15pF,	1.2	3.5	6.1	1.2	6.5		
	S to Z	3.30 ± 0.30	$R_L=1M\Omega$,	0.8	2.6	4.1	0.8	4.5		
		5.00 ± 0.50		0.5	1.9	3.2	0.5	3.5	1	
		1.80 ± 0.15	$R_L=1M\Omega$,	2.5	5.9	10.0	2.5	10.5	ns	
t _{PLH.} t _{PHL}	Propagation Delay In to Z	5.00 ± 0.50		1.2	3.5	5.8	1.2	6.1		Figure 5 Figure 6
IPLH, IPHL		3.30 ± 0.30		0.8	2.6	3.9	0.8	4.2		
		5.00 ± 0.50		0.5	1.9	3.1	0.5	3.3		
	Propagation Delay	3.30 ± 0.30	C _L =50pF,	1.2	3.2	4.8	1.2	5.2		
	S to Z	5.00 ± 0.50	$R_L=500\Omega$,	0.8	2.4	3.8	0.8	4.1		
	Propagation Delay	3.30 ± 0.30	C _L =50pF,	1.2	3.2	4.6	1.2	5.0		
	I_n to Z	5.00 ± 0.50	$R_L=500\Omega$,	0.8	2.4	3.7	0.8	4.0		
C _{IN}	Input Capacitance	0.00			2				pF	
C _{PD}	Power Dissipation	3.30			14				pF	Figure 7
ОРО	Capacitance ⁽⁴⁾	5.00			17				Pi	rigule /

Note:

4. C_{PD} is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I_{CCD}) at no output loading and operating at 50% duty cycle. C_{PD} is related to I_{CCD} dynamic operating current by the expression: I_{CCD}=(C_{PD})(V_{CC})(f_{IN})+(I_{CC}static).

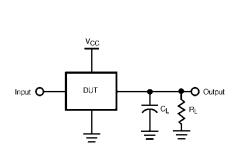


Figure 5. AC Test Circuit

Note:

 C_L includes load and stray capacitance. Input PRR=1.0MHz, t_w=500ns.

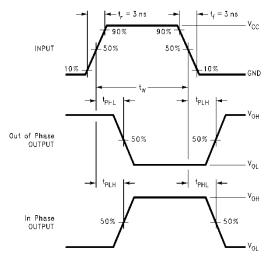
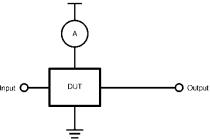


Figure 6. AC Waveforms





Note:

6. Input=AC Waveform; PRR=Variable; Duty Cycle=50%.

Figure 7. I_{CCD} Test Circuit

Physical Dimensions

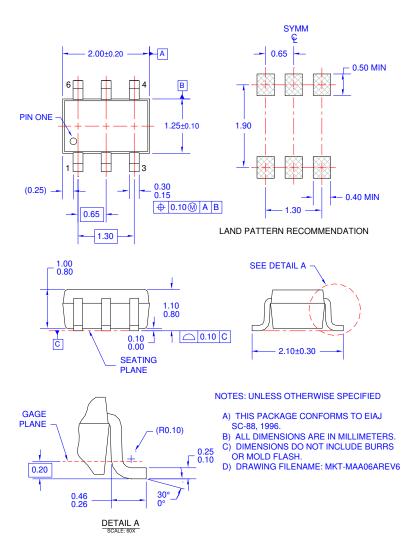


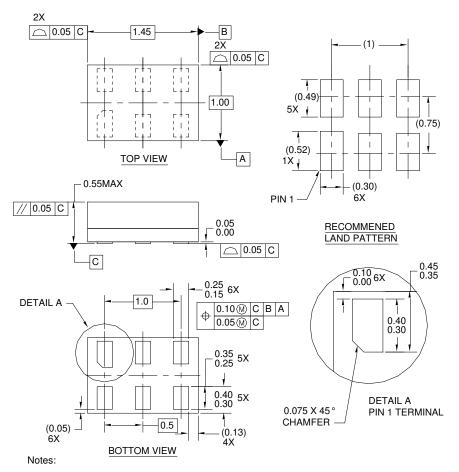
Figure 8. 6-Lead, SC70, EAJ SC-88, 1.25mm Wide

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Tape and Reel Specifications

Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
P6X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

Physical Dimensions



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD 2. DIMENSIONS ARE IN MILLIMETERS 3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06AREVC

Figure 9. 6-Lead, MicroPak™, 1.0mm Wide

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Tape and Reel Specifications

Package Designator Tape Section		Cavity Number	Cavity Status	Cover Type Status	
	Leader (Start End)	125 (Typical)	Empty	Sealed	
L6X	Carrier	5000	Filled	Sealed	
	Trailer (Hub End)	75 (Typical)	Empty	Sealed	

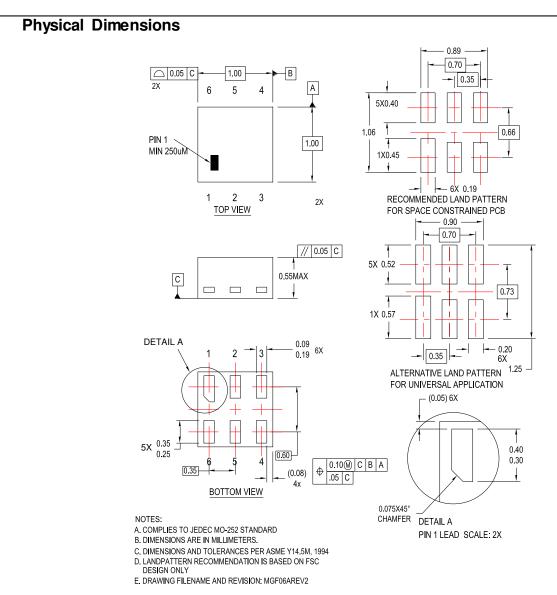


Figure 10.6-Lead, MicroPak2, 1x1mm Body, .35mm Pitch

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Tape and Reel Specifications

Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status	
	Leader (Start End)	125 (Typical)	Empty	Sealed	
FHX	Carrier	5000	Filled	Sealed	
	Trailer (Hub End)	75 (Typical)	Empty	Sealed	

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