TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC4S584F

SCHMITT TRIGGER

TC4S584F is the one circuit inverter having the schmitt trigger function at the input terminal.

That is, since the circuit threshold level voltage at the leading and trailing edges of input waveform are different (Vp, V_N), the TC4S584F can be used in the broad range application, including line receiver, waveform shaping circuit, astable multivibrator, etc. In addition to ordinary inverter.

SSOP5-P-0.95

Weight: 0.016g (Typ.)

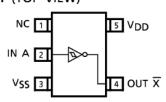
ABSOLUTE MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V_{DD}	Vss - 0.5~Vss + 20	V
Input Voltage	VIN	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	V
Output Voltage	Vout	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	V
DC Input Current	IN	± 10	mA
Power Dissipation	PD	200	mW
Operating Temperature Range	T _{opr}	- 40~85	°C
Storage Temperature Range	T _{stg}	- 65~150	°C
Lead Temperature (10s)	TL	260	°C

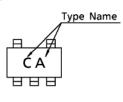
LOGIC DIAGRAM



PIN ASSIGNMENT (TOP VIEW)



MARKING



OPERATING RANGES ($V_{SS} = 0V$)

CHARACTERISTIC	SYMBOL		MIN.	TYP.	MAX.	UNIT
DC Supply Voltage	V_{DD}	_	3		18	V
Input Voltage	VIN	_	0	_	V_{DD}	V

I CHARACTERISTIC I		SYM-	TEST CONDITION	V _{DD}	– 40°C		25°C			85°C		UNIT
CHARAC	CILKISTIC	BOL	1231 CONDITION	(V)	MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	
High-Leve Output V		Voн	I _{OUT} <1μΑ V _{IN} = V _{SS} , V _{DD}	5 10 15	4.95 9.95 14.95	_	4.95 9.95 14.95	10.00	_	4.95 9.95 14.95	_	V
Low-Level Output V		VOL	$ I_{OUT} < 1\mu A$ $V_{IN} = V_{SS}, V_{DD}$	5 10 15	 	0.05 0.05 0.05		0.00 0.00 0.00	0.05		0.05 0.05 0.05	V
Output Hi Current	igh	ЮН	V _{OH} = 4.6V V _{OH} = 2.5V V _{OH} = 9.5V V _{OH} = 13.5V V _{IN} = V _{SS} , V _{DD}	5 5 10 15	- 0.61 - 2.5 - 1.5 - 4.0	_	- 0.51 - 2.1 - 1.3 - 3.4		_	- 0.42 - 1.7 - 1.1 - 2.8	_	4
Output Lo Current	ow	loL	V _{OL} = 0.4V V _{OL} = 0.5V V _{OL} = 1.5V V _{IN} = V _{SS} , V _{DD}	5 10 15	0.61 1.5 4.0		0.51 1.3 3.4		—	0.42 1.1 2.8	1 1	mA
Positive T Threshold	rigger Voltage*	V _P	V _{OUT} = 0.5V V _{OUT} = 1.0V V _{OUT} = 1.5V	5 10 15	1.95 4.3 6.9	7.1	2.05 4.5 7.1		7.1	2.05 4.7 7.1	3.75 7.2 10.8	
Negative Threshold	Trigger Voltage*	ν _N	V _{OUT} = 4.5V V _{OUT} = 9.0V V _{OUT} = 13.5V	5 10 15	1.05 2.1 3.2	4.9 7.0	1.1 2.2 3.3			0.95 2.0 3.1	4.8 6.9	V
Hystersis \	Voltage*	VΗ	_	5 10 15	0.1 1.7 3.1	1.35 3.2 4.8	0.4 1.8 3.2	4.0	3.2 4.8	0.4 1.7 3.2	3.4	
Input Current	H Level	ηн	V _{IH} = 18V V _{IL} = 0V	18 18	_	0.1		10 ⁻⁵	0.1	_	1.0 - 1.0	μΑ
Quiescent Device Cu		IDD	V _{IN} = V _{SS} , V _{DD}	5 10 15	_ _ _	1 2 4	_ _ _	0.001 0.002 0.004	1 2	_ _ _	7.5 15 30	μΑ

(Note) Values are different to TC4584BP, TC4584BF marked* (Vp, V_N , V_H).

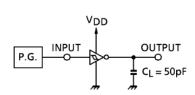
DYNAMIC ELECTRICAL CHARACTERISTICS (Ta = 25°C, V_{SS} = 0V, C_L = 50pF)

CHARACTERISTIC	SYMBOL	TEST CONDITION	V _{DD} (V)	MIN.	TYP.	MAX.	UNIT
Output Transition Time			5	_	80	200	
(Low to High)	tTLH	_	10	_	50	100	
(Low to High)			15	_	40	80	ns
Output Transition Time	tтнь		5	_	80	200	
Output Transition Time (High to Low)		_	10	_	50	100	
			15	_	40	80	
	+		5	_	170	340	
Propagation Delay Time	t _{pLH} t _{pHL}	_	10	_	80	160	ns
			15	_	60	120	
Input Capacitance	CIN	_	_	5	7.5	pF	

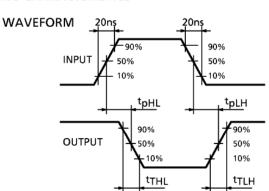
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CIRCUIT AND WAVEFORM FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

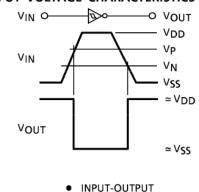
CIRCUIT



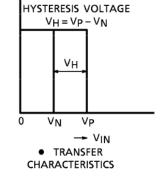
P.G.: PULSE GENERATOR



INPUT-OUTPUT VOLTAGE CHARACTERISTICS



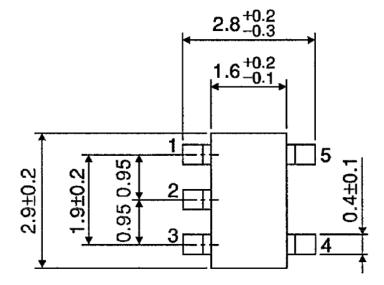
VOLTAGE WAVEFORM

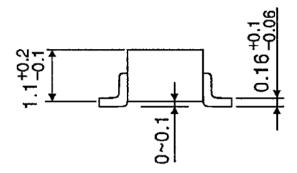


PACKAGE DIMENSIONS

SSOP5-P-0.95

Unit: mm





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Weight: 0.016g (Typ.)

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