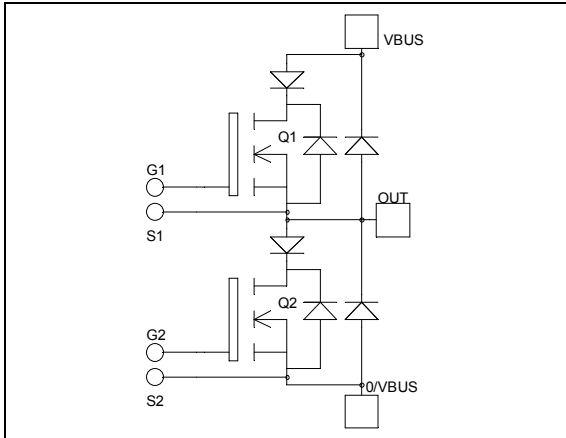


*Phase leg Series & SiC parallel diodes  
Super Junction MOSFET Power Module*

$V_{DSS} = 800V$   
 $R_{DSon} = 75m\Omega \text{ max @ } T_j = 25^\circ C$   
 $I_D = 56A \text{ @ } T_c = 25^\circ C$

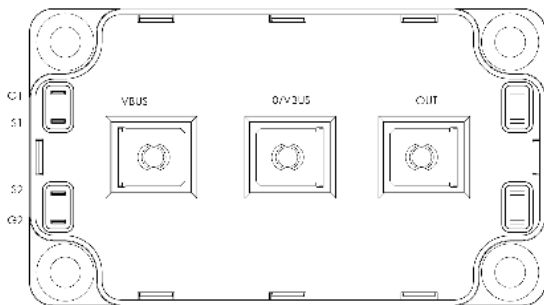


#### Application

- Motor control
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

#### Features

- **CoolMOS™**
  - Ultra low  $R_{DSon}$
  - Low Miller capacitance
  - Ultra low gate charge
  - Avalanche energy rated
- **Parallel SiC Schottky Diode**
  - Zero reverse recovery
  - Zero forward recovery
  - Temperature Independent switching behavior
  - Positive temperature coefficient on VF
- Kelvin source for easy drive
- Very low stray inductance
  - Symmetrical design
  - M5 power connectors
- High level of integration



#### Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile
- RoHS Compliant

**All ratings @  $T_j = 25^\circ C$  unless otherwise specified**

#### Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
$V_{DSS}$	Drain - Source Breakdown Voltage	800	V
$I_D$	Continuous Drain Current	$T_c = 25^\circ C$	56
		$T_c = 80^\circ C$	43
$I_{DM}$	Pulsed Drain current	232	A
$V_{GS}$	Gate - Source Voltage	$\pm 30$	V
$R_{DSon}$	Drain - Source ON Resistance	75	$m\Omega$
$P_D$	Maximum Power Dissipation	$T_c = 25^\circ C$	568
$I_{AR}$	Avalanche current (repetitive and non repetitive)	17	A
$E_{AR}$	Repetitive Avalanche Energy	0.5	mJ
$E_{AS}$	Single Pulse Avalanche Energy	670	

**CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on [www.microsemi.com](http://www.microsemi.com)

**Electrical Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 800V			100	μA
		T <sub>j</sub> = 25°C				
		V <sub>GS</sub> = 0V, V <sub>DS</sub> = 800V			1000	
R <sub>DS(on)</sub>	Drain – Source on Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 28A			75	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 4mA	2.1	3	3.9	V
I <sub>GSS</sub>	Gate – Source Leakage Current	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0V			±200	nA

**Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V		9015		pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 25V		4183		
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1MHz		215		
Q <sub>g</sub>	Total gate Charge	V <sub>GS</sub> = 10V		364		nC
Q <sub>gs</sub>	Gate – Source Charge	V <sub>Bus</sub> = 400V		48		
Q <sub>gd</sub>	Gate – Drain Charge	I <sub>D</sub> = 56A		184		
T <sub>d(on)</sub>	Turn-on Delay Time	<b>Inductive switching @ 125°C</b>		10		ns
T <sub>r</sub>	Rise Time	V <sub>GS</sub> = 15V		13		
T <sub>d(off)</sub>	Turn-off Delay Time	V <sub>Bus</sub> = 533V		83		
T <sub>f</sub>	Fall Time	I <sub>D</sub> = 56A R <sub>G</sub> = 1.2Ω		35		
E <sub>on</sub>	Turn-on Switching Energy	<b>Inductive switching @ 25°C</b>		583		μJ
E <sub>off</sub>	Turn-off Switching Energy	V <sub>GS</sub> = 15V, V <sub>Bus</sub> = 533V I <sub>D</sub> = 56A, R <sub>G</sub> = 1.2Ω		556		
E <sub>on</sub>	Turn-on Switching Energy	<b>Inductive switching @ 125°C</b>		1020		μJ
E <sub>off</sub>	Turn-off Switching Energy	V <sub>GS</sub> = 15V, V <sub>Bus</sub> = 533V I <sub>D</sub> = 56A, R <sub>G</sub> = 1.2Ω		684		
R <sub>thJC</sub>	Junction to Case Thermal Resistance				0.22	°C/W

**Series diode ratings and characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V <sub>RRM</sub>	Maximum Peak Repetitive Reverse Voltage		1000			V
I <sub>RM</sub>	Maximum Reverse Leakage Current	V <sub>R</sub> = 1000V			300	μA
I <sub>F</sub>	DC Forward Current	T <sub>c</sub> = 80°C		120		A
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 120A		1.9	2.5	V
		I <sub>F</sub> = 240A		2.2		
		I <sub>F</sub> = 120A	T <sub>j</sub> = 125°C		1.7	
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 120A V <sub>R</sub> = 667V di/dt = 400A/μs	T <sub>j</sub> = 25°C		280	ns
			T <sub>j</sub> = 125°C		350	
Q <sub>rr</sub>	Reverse Recovery Charge	I <sub>F</sub> = 120A V <sub>R</sub> = 667V di/dt = 400A/μs	T <sub>j</sub> = 25°C		1.52	μC
			T <sub>j</sub> = 125°C		7.2	
R <sub>thJC</sub>	Junction to Case Thermal Resistance				0.46	°C/W

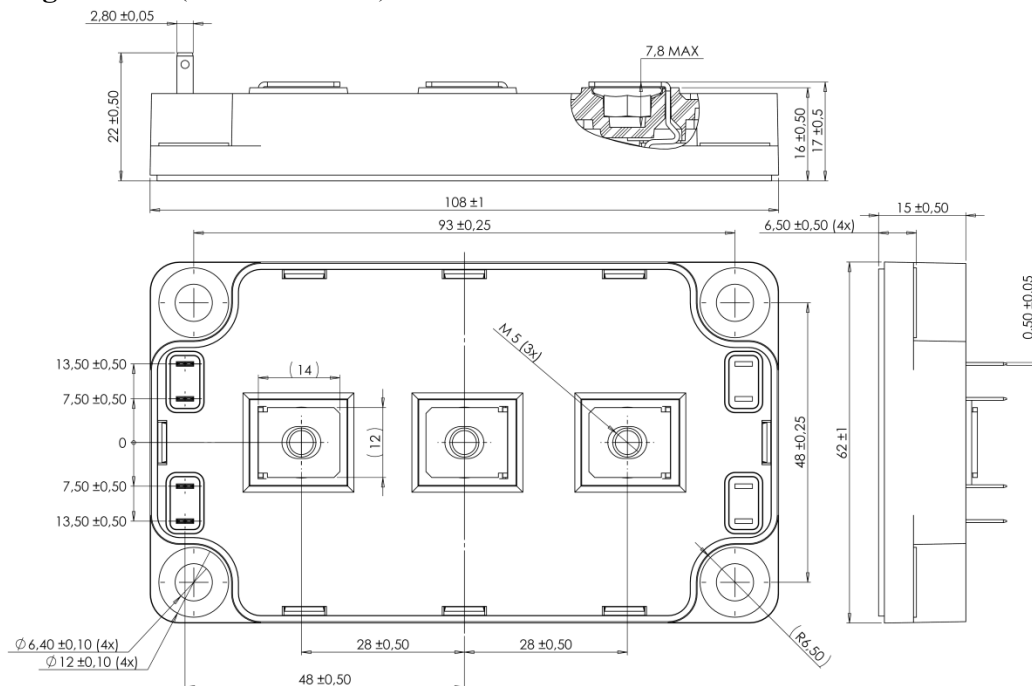
## Parallel diode ratings and characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$V_{RRM}$	Maximum Peak Repetitive Reverse Voltage		1200			V
$I_{RM}$	Maximum Reverse Leakage Current	$V_R = 1200V$		300 600	1200 6000	$\mu A$
$I_F$	DC Forward Current			30		A
$V_F$	Diode Forward Voltage	$I_F = 30A$		1.6 2.6	1.8 3.0	V
$Q_C$	Total Capacitive Charge	$I_F = 30A, V_R = 1200V$ $di/dt = 1600A/\mu s$		168		nC
$Q$	Total Capacitance	$f = 1MHz, V_R = 200V$ $f = 1MHz, V_R = 400V$		270 198		pF
$R_{thJC}$	Junction to Case Thermal Resistance				0.45	$^{\circ}C/W$

## Thermal and package characteristics

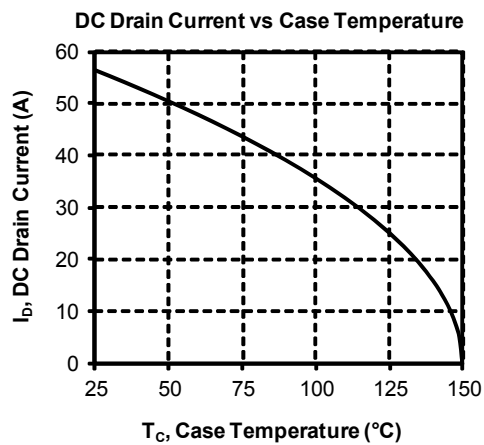
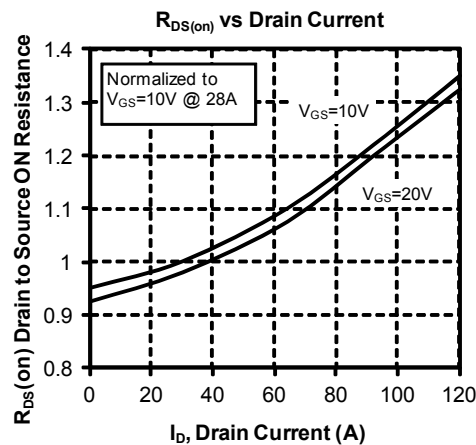
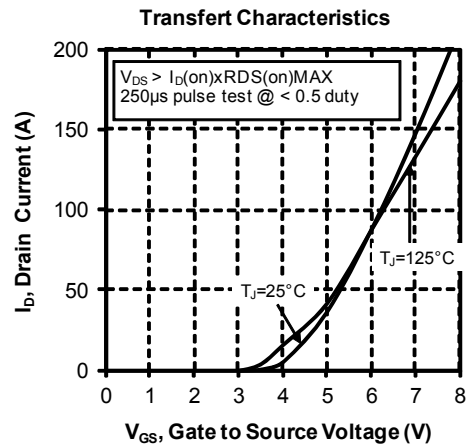
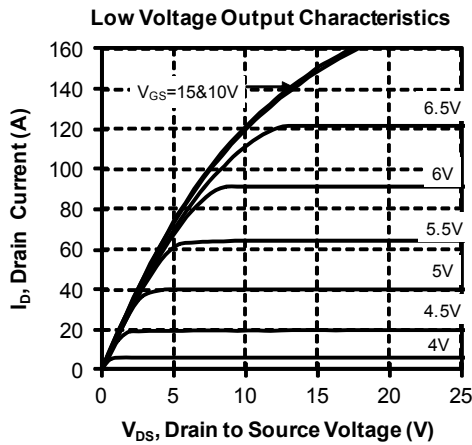
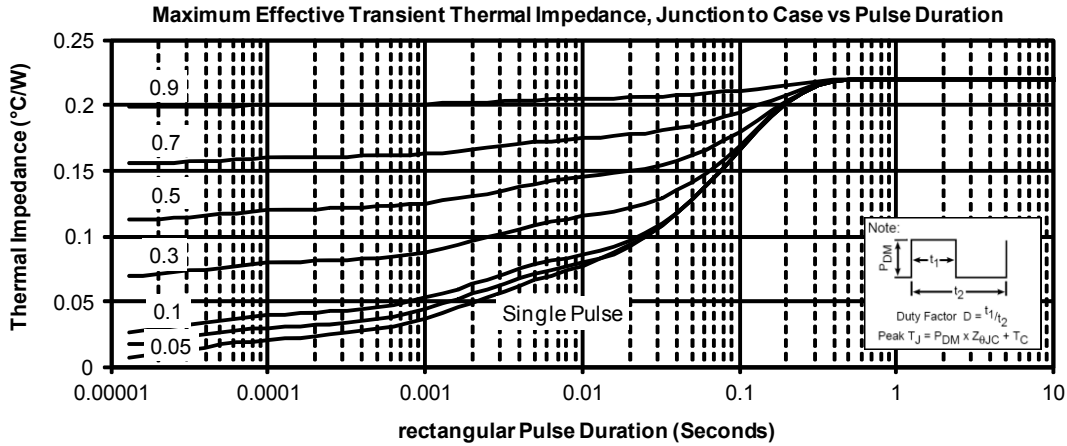
Symbol	Characteristic	Min	Max	Unit		
$V_{ISOL}$	RMS Isolation Voltage, any terminal to case t = 1 min, 50/60Hz	4000		V		
$T_J$	Operating junction temperature range	-40	150	$^{\circ}C$		
$T_{JOP}$	Recommended junction temperature under switching conditions	-40	$T_{Jmax} - 25$			
$T_{STG}$	Storage Temperature Range	-40	125			
$T_C$	Operating Case Temperature	-40	100			
Torque	Mounting torque	To heatsink	M6	3	5	N.m
		For terminals	M5	2	3.5	
Wt	Package Weight				300	g

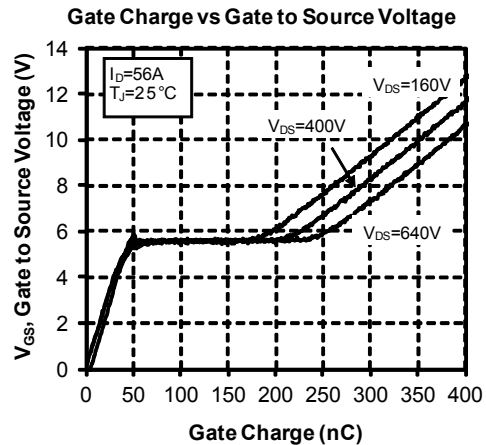
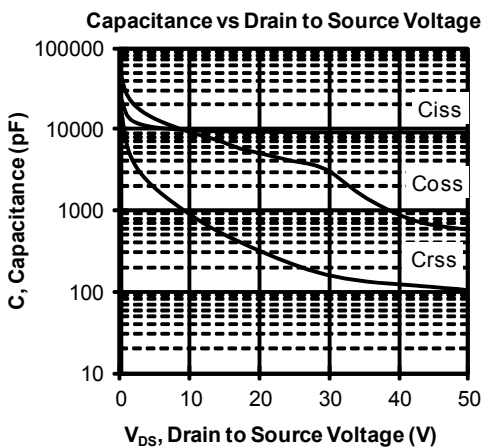
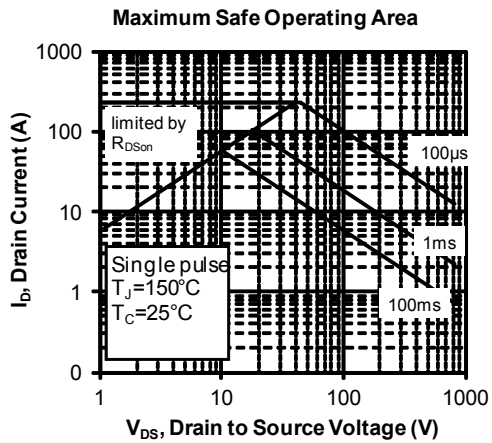
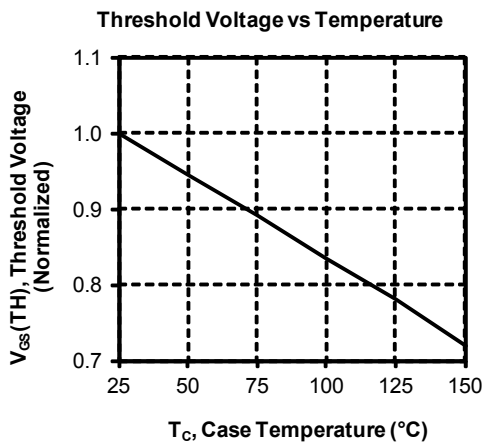
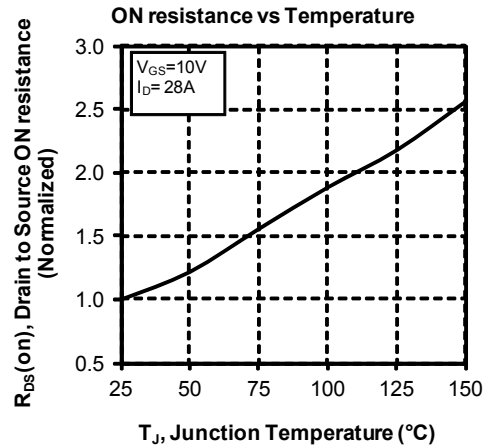
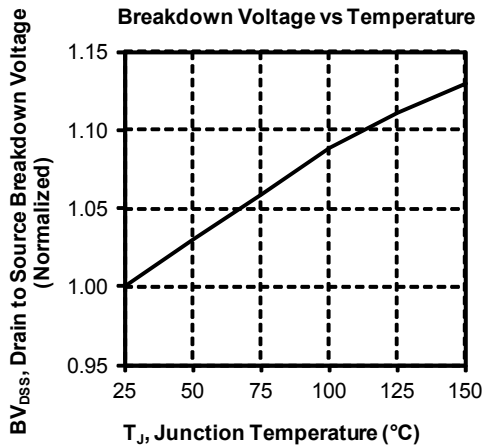
## SP6 Package outline (dimensions in mm)



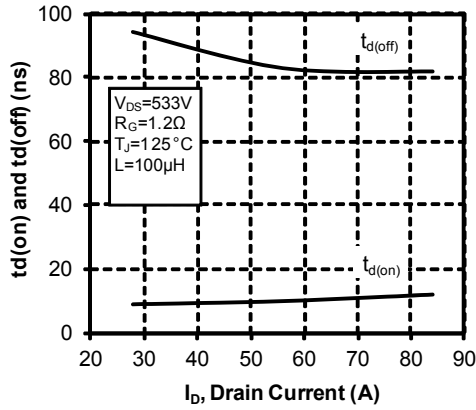
See application note APT0601 - Mounting Instructions for SP6 Power Modules on [www.microsemi.com](http://www.microsemi.com)

## Typical CoolMOS Performance Curve

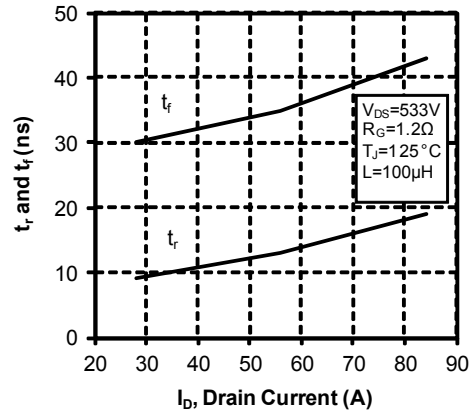




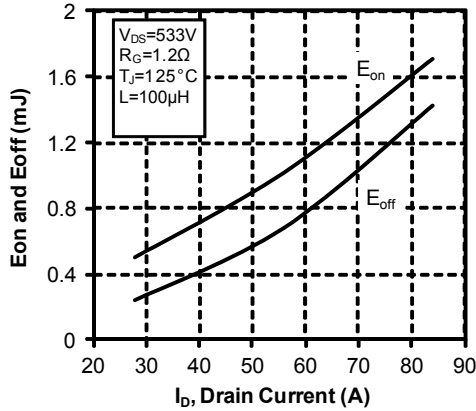
**Delay Times vs Current**



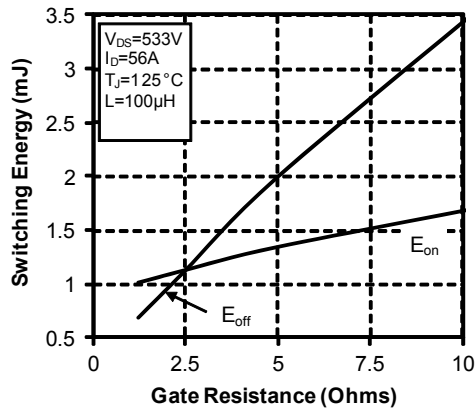
**Rise and Fall times vs Current**



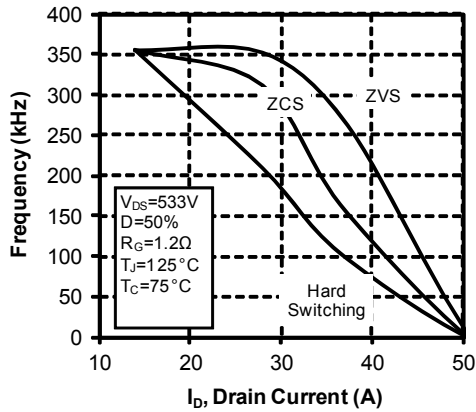
**Switching Energy vs Current**



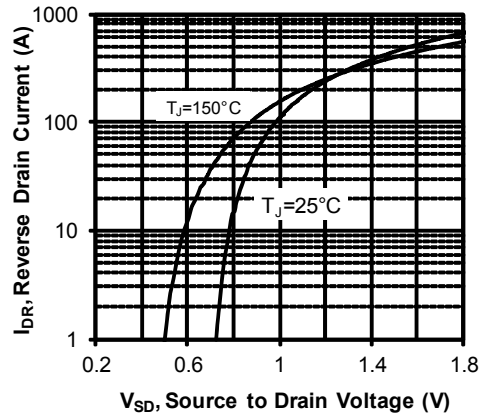
**Switching Energy vs Gate Resistance**

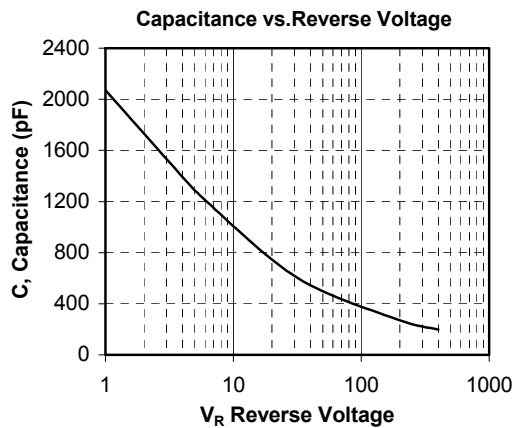
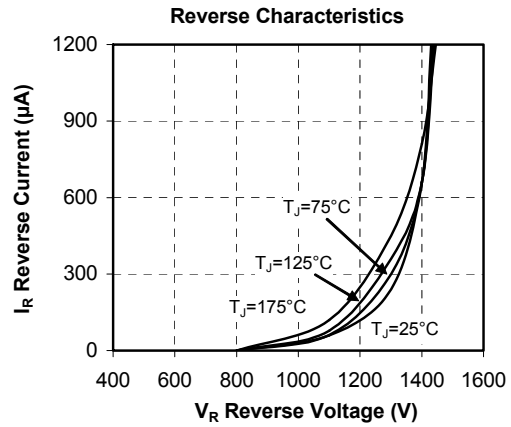
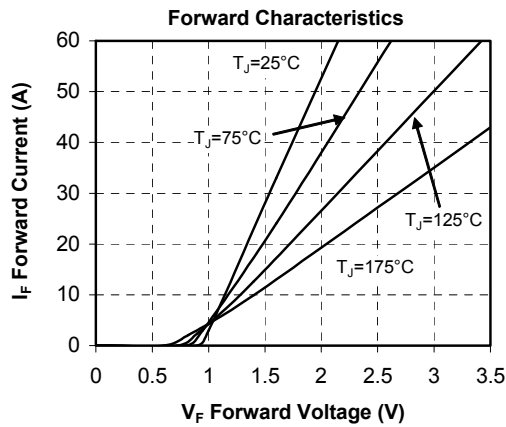
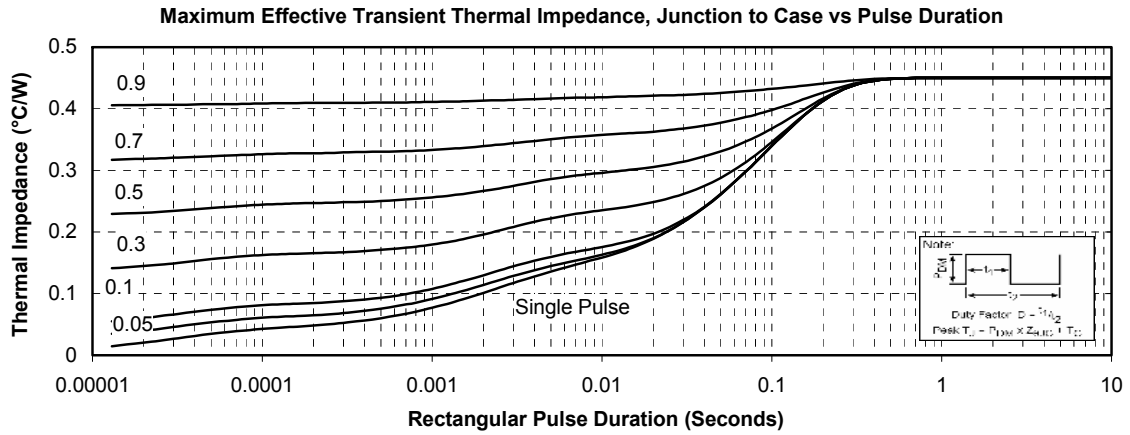


**Operating Frequency vs Drain Current**



**Source to Drain Diode Forward Voltage**



**Typical SiC Diode Performance Curve**


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