# RENESAS

# μ**PA2825T1S** MOS FIELD EFFECT TRANSISTOR

R07DS0755EJ0100 Rev.1.00 May 25, 2012

## Description

The  $\mu$ PA2825T1S is N-channel MOS Field Effect Transistor designed for power management applications of a notebook computer and Lithium-Ion battery protection circuit.

## Features

- $V_{DSS} = 30 V (T_A = 25^{\circ}C)$
- Low on-state resistance

----  $R_{DS(on)} = 4.6 \text{ m}\Omega \text{ MAX.} (V_{GS} = 10 \text{ V}, I_D = 24 \text{ A})$ 

- 4.5 V Gate-drive available
- Small & thin type surface mount package with heat spreader (HWSON-8)
- Pb-free, Halogen Free

## **Ordering Information**

Part No.	Lead Plating	Packing	Package
μPA2825T1S-E2-AT <sup>*1</sup>	Pure Sn (Tin)	Tape 5000 p/reel	HWSON-8
			typ. 0.022 g

Note: \*1. Pb-free (This product does not contain Pb in external electrode and other parts.)

## Absolute Maximum Ratings (T<sub>A</sub> = 25°C)

Item	Symbol	Ratings	Unit
Drain to Source Voltage ( $V_{GS} = 0 V$ )	V <sub>DSS</sub>	30	V
Gate to Source Voltage ( $V_{DS} = 0 V$ )	V <sub>GSS</sub>	±20	V
Drain Current (DC) ( $T_c = 25^{\circ}C$ )	I <sub>D(DC)</sub>	±24	A
Drain Current (pulse) *1	I <sub>D(pulse)</sub>	±96	A
Total Power Dissipation *2	P <sub>T1</sub>	1.5	W
Total Power Dissipation (PW = 10 sec) *2	P <sub>T2</sub>	3.8	W
Total Power Dissipation (T <sub>C</sub> = 25°C)	P <sub>T3</sub>	16.5	W
Channel Temperature	T <sub>ch</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C
Single Avalanche Current *3	I <sub>AS</sub>	18	A
Single Avalanche Energy *3	E <sub>AS</sub>	32.4	mJ

## **Thermal Resistance**

Channel to Ambient Thermal Resistance *2	R <sub>th(ch-A)</sub>	83.3	°C/W
Channel to Case (Drain) Thermal Resistance	R <sub>th(ch-C)</sub>	7.6	°C/W

Notes: \*1. PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

- \*2. Mounted on a glass epoxy board of 25.4 mm x 25.4 mm x 0.8 mmt
- \*3. Starting T\_{ch} = 25°C, V\_DD = 15 V, R\_G = 25  $\Omega,$  V\_GS = 20  $\rightarrow$  0 V, L = 100  $\mu H$



			TVD		11	To al Operativity of
Item	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1	μA	$V_{DS} = 30 V, V_{GS} = 0 V$
Gate Leakage Current	I <sub>GSS</sub>			±10	μA	$V_{GS} = \pm 16 \text{ V},  V_{DS} = 0 \text{ V}$
Gate Cut-off Voltage	V <sub>GS(off)</sub>	1.0		2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Forward Transfer Admittance *1	y <sub>fs</sub>	16			S	$V_{DS} = 10 V, I_D = 6 A$
Drain to Source On-state	R <sub>DS(on)1</sub>		3.7	4.6	mΩ	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 24 \text{ A}$
Resistance *1	R <sub>DS(on)2</sub>		7.0	12	mΩ	$V_{GS} = 4.5 V, I_D = 6 A$
Input Capacitance	C <sub>iss</sub>		2600		pF	$V_{DS} = 10 V,$
Output Capacitance	C <sub>oss</sub>		850		pF	$V_{GS} = 0 V,$
Reverse Transfer Capacitance	C <sub>rss</sub>		800		pF	f = 1 MHz
Turn-on Delay Time	t <sub>d(on)</sub>		17		ns	$V_{DD} = 15 \text{ V}, \text{ I}_{D} = 12 \text{ A},$
Rise Time	tr		86		ns	$V_{GS} = 10 V$ ,
Turn-off Delay Time	t <sub>d(off)</sub>		100		ns	R <sub>G</sub> = 10 Ω
Fall Time	t <sub>f</sub>		58		ns	
Total Gate Charge	Q <sub>G</sub>		57		nC	$V_{GS} = 10 V$ ,
			35		nC	$V_{GS} = 5 V$
Gate to Source Charge	Q <sub>GS</sub>		7.5		nC	$V_{DD} = 15 V$ ,
Gate to Drain Charge	Q <sub>GD</sub>		22		nC	I <sub>D</sub> =24 A
Body Diode Forward Voltage *1	V <sub>F(S-D)</sub>		0.9		V	$I_F = 24 \text{ A}, V_{GS} = 0 \text{ V}$
Reverse Recovery Time	t <sub>rr</sub>		42		ns	$I_F = 24 \text{ A}, V_{GS} = 0 \text{ V},$
Reverse Recovery Charge	Qrr		37		nC	di/dt = 100 A/ <i>µ</i> s

## **Electrical Characteristics (T<sub>A</sub> = 25°C)**

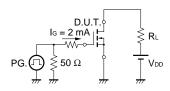
Note: \*1. Pulsed

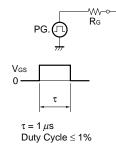
### **TEST CIRCUIT 1 AVALANCHE CAPABILITY**

## D.U.T. ສL $R_G = 25 \Omega$ 50 Ω VDD $V\text{Gs}=20 \rightarrow 0 \; V$ BVDS DS VD

-Starting Tch

### TEST CIRCUIT 3 GATE CHARGE

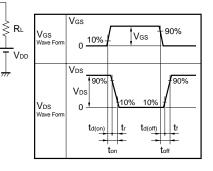




**TEST CIRCUIT 2 SWITCHING TIME** 

✐

D.U.T.

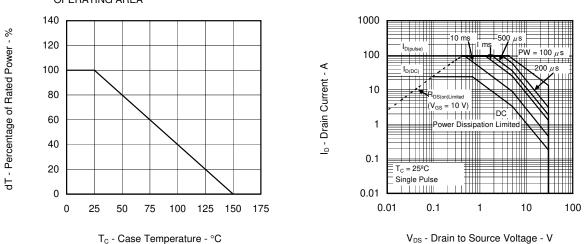




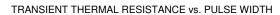
## Typical Characteristics (T<sub>A</sub> = 25°C)

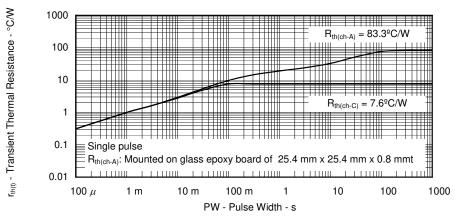
DERATING FACTOR OF FORWARD BIAS SAFE **OPERATING AREA** 

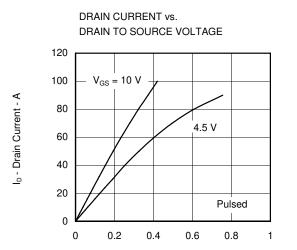
FORWARD BIAS SAFE OPERATING AREA



V<sub>DS</sub> - Drain to Source Voltage - V

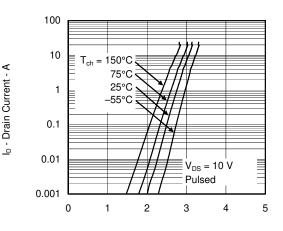






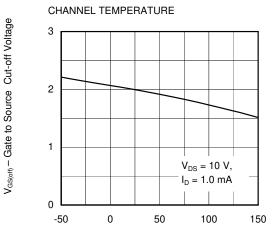
V<sub>DS</sub> - Drain to Source Voltage - V





V<sub>GS</sub> - Gate to Source Voltage - V

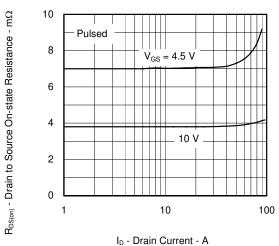




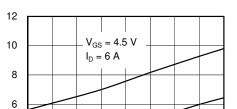
GATE TO SOURCE CUT-OFF VOLTAGE vs.



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

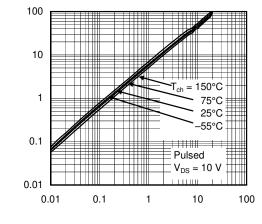


 $R_{\text{DS(on)}}$  - Drain to Source On-state Resistance -  $m\Omega$ 



DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE

FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

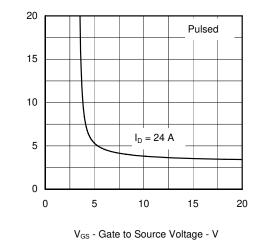


| y<sub>fs</sub> | - Forward Transfer Admittance - S

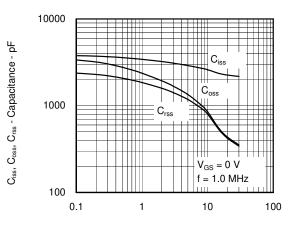
 $R_{DS(on)}$  - Drain to Source On-state Resistance -  $m\Omega$ 

I<sub>D</sub> - Drain Current - A

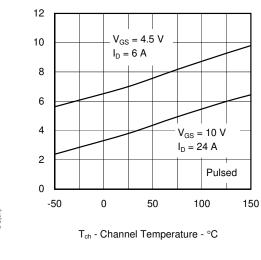
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



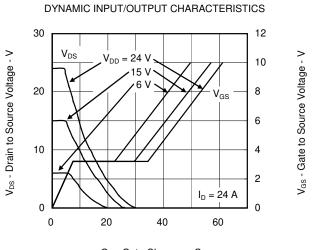
#### CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



V<sub>DS</sub> - Drain to Source Voltage - V

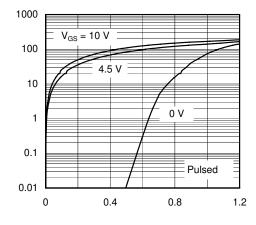






Q<sub>G</sub> - Gate Charge - nC

SOURCE TO DRAIN DIODE FORWARD VOLTAGE



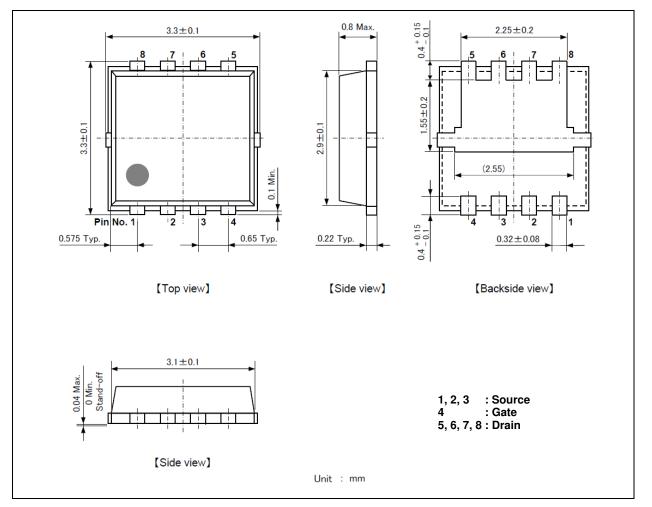
IF - Diode Forward Current - A

 $V_{\text{F(S-D)}}$  - Source to Drain Voltage - V

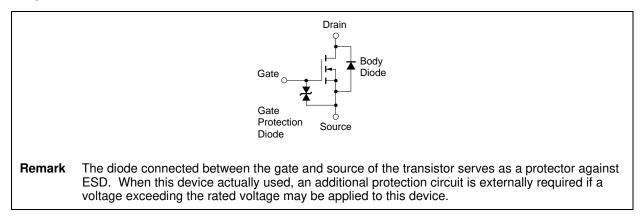


## Package Drawings (Unit: mm)

## HWSON-8



## **Equivalent Circuit**





<b>Revision History</b>	$\mu$ PA2825T1S Data Sheet

		Description		
Rev.	Date	Page	Summary	
1.00	May 25, 2012	_	First Edition Issued	

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