PAN	JIT
	SEMI
	CONDUCTOR



# PE1605M2Q

## Ultra Low Capacitance ESD Protection

5 V

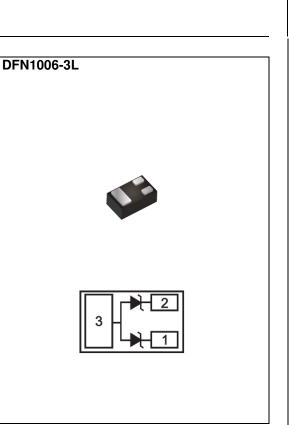
Voltage

#### Features

- IEC61000-4-2(ESD) : ±20kV Air, ±15kV Contact
- IEC61000-4-4(EFT) : 40A(5/50ns)
- IEC61000-4-5(Lightning) : 4A(8/20uS)
- Low leakage current, maximum of 50nA at rated voltage
- Ultra low capacitance
- Low clamping voltage
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case : DFN1006-3L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0007 grams



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	VALUE	UNITS	
ESD IEC61000-4-2(Air)	N	±20	kV	
ESD IEC61000-4-2(Contact)	VESD	±15		
Operating Junction Temperature Range	TJ	-55~150	°C	
Storage Temperature Range	Tstg	-55~150	٥C	





#### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Reverse Stand-Off Voltage <sup>(Note 1)</sup>	VRWM	-	-	-	5	V
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>BR</sub> =1mA	5.5	-	-	V
Reverse Leakage Current	I <sub>R</sub>	V <sub>R</sub> =5.0V	-	-	50	nA
Clamping Voltage	V <sub>CL</sub>	I <sub>PP</sub> =1A, t <sub>P</sub> =8/20μs, any I/O pins to GND	-	-	10	V
		I <sub>PP</sub> =4A, t <sub>P</sub> =8/20μs, any I/O pins to GND	-	-	15	V
Clamping Voltage TLP <sup>(Note 2)</sup>	V <sub>CL</sub>	IPP=8A, tP=100ns, any I/O pins to GND	-	16	-	V
		I <sub>PP</sub> =16A, t <sub>P</sub> =100ns, any I/O pins to GND	-	23.5	-	V
Dynamic Resistance	R <sub>DYN</sub>	t <sub>P</sub> =100ns	-	0.94	-	Ω
Off State Junction Capacitance	CJ	2.5Vdc Bias f=1MHz, any I/O pins to GND	-	0.3	0.35	pF
		2.5Vdc Bias f=1MHz, Between any I/O pins	-	0.2	-	pF

NOTES :

- 1. A transient suppressor is selected according to the working peak reverse voltage(V<sub>RWM</sub>), which should be equal to or greater than the DC or continuous peak operation voltage level.
- 2. Testing using Transmission Line Pulse (TLP) conditions : Z0 =  $50\Omega$ , t<sub>P</sub> = 100 ns.

January 9,2023

0

1.0

2.0

2.5

IPP, Peak Current (A)

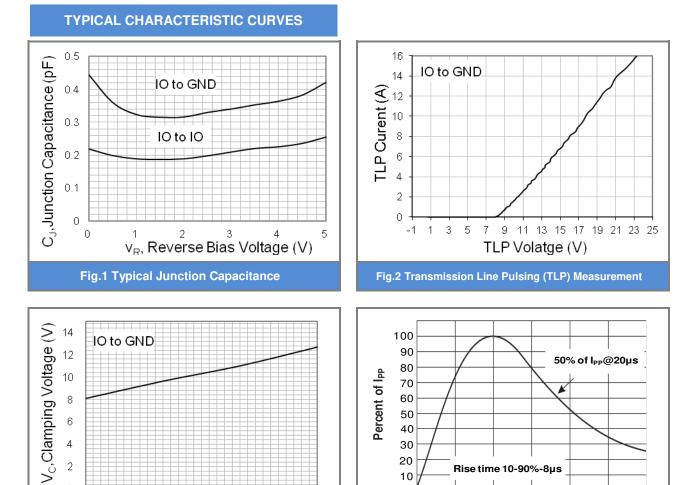
Fig.3 Typical Peak Clamping Voltage(8/20us)

3.0

3.5

4.0

1.5



40 30 20

10

0

0

Rise time 10-90%-8µs

Fig.4 8/20us Pulse Waveform

15

Time, µs

20

25

30

10

5

**PE1605M2Q** 





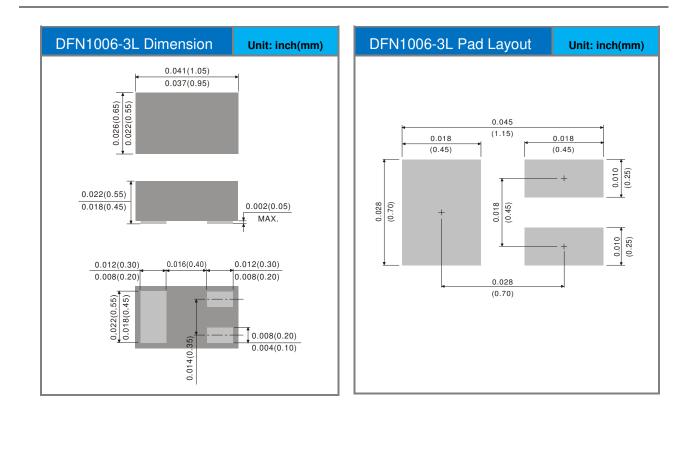


# PE1605M2Q

### **Product and Packing Information**

Part No.	Package Type	Packing Type	Marking	Version
PE1605M2Q	DFN1006-3L	10K pcs / 7" reel	U3	Halogen free RoHS compliant

## Packaging Information & Mounting Pad Layout







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